BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE, PILANI
Hyderabad Campus, Second Semester 2014-2015

CS/ ECE /EEE/ INSTR F241 Microprocessor Programming & Interfacing

Quiz-2 (Closed Book)

Date: 27/03/15   Day: Friday   Time : 12.00 – 12.30 PM
Max Marks   : 20

Answer all the questions.   [2X10=20]

1. The LODSB instruction copies the byte contents of _____segment memory location addressed by SI register to the _____ segment memory location addressed by DI register.

2. For 80486, if AL=EDH, indicate the contents of AL after the execution of following instructions
   a) SHR AL, 3   AL= _____
   b) RCR AL,2   AL= _____

3. The 8086 LOOP instruction decrements register _____ and tests it for a _____ to decide if a jump occurs.

4. _____ instruction compares the AX register with a word block of memory , _____ instruction compares the EAX register with a double -word block of memory.

5. For an 80386 descriptor that contains a base address of 00280000H, a limit of 00010H, and G=1, the starting and ending locations addressed by this descriptor are _____and _____respectively.

6. Indicate the function of $\overline{BHE}$ of 8086 processor?

7. For an 8088 processor, if the clock is operated at 5 MHz, the bus cycle completes in_____ sec.

8. When DT/$\overline{R}$ is a logic 1, what condition does it indicate about the operation of the 8086/8088?

9. The CPU sends the address to memory or I/O and the ALE signal for demultiplexing in _____ T-state.

10. Write a code in ALP to set the trap flag.