Dr. Babasaheb Ambedkar Technological University

Teaching and Examination Scheme for

M.Tech. (Embedded Engineering) w.e.f. July 2017

Sr. No.	Course Code	Name of the course	Hours/Week			Credit	Examination scheme				
							Theory		IA	PR/OR	TOTAL
			L	P	T		TH	Test			
First Semester											
01	MTESC101	System Design using Embedded Processors	03	--	1	04	60	20	20	--	100
02	MTESC102	Embedded Programming	03	--	1	04	60	20	20	--	100
03	MTESC103	Advanced Digital System Design	03	--	1	04	60	20	20	--	100
04	MTESE114	Elective-I	03	--	--	03	60	20	20	--	100
05	MTESE125	Elective-II	03	--	--	03	60	20	20	--	100
06	MTESC106	Communication Skills	02	--	--	02	--	--	25	25	50
07	MTESL107	PG Lab-I*	--	03	--	02	--	--	25	25	50
		Total for Semester I	17	03	03	22	300	100	150	50	600
Second Semester											
01	MTESC201	Product Design and Quality Management	03	--	1	04	60	20	20	--	100
02	MTESC202	Embedded OS and RTOS	03	--	1	04	60	20	20	--	100
03	MTESE233	Elective-III	03	--	--	03	60	20	20	--	100
04	MTESE244	Elective- IV	03	--	--	03	60	20	20	--	100
05	MTESE255	Elective-V- (Open to all)	03	--	--	03	60	20	20	--	100
06	MTESS206	Seminar-I	--	04	--	02	--	--	50	50	100
07	MTESP207	Mini-Project	--	04	--	02	--	--	50	50	100
		Total for Semester II	15	8	02	21	300	100	200	100	700
Third Semester											
1	MTESC301	Project Management & Intellectual Property Rights (Self Study)#	--	--	--	02	--	--	50	50	100
2	MTESP302	Project-I	--	--	--	10	--	--	50	50	100
		Total for Semester III	--	--	-	12	--	--	100	100	200
Fourth Semester											
1	MTESP401	Project-II	--	--	--	20	--	--	100	100	200
		Total for Semester IV	--	--	--	20	--	--	100	100	200
GRAND TOTAL											1700

* PG Lab-I –Practical shall be based on courses of first semester.

\# Student has to choose this course either from NPTEL/MOOC pool and submission of course completion certificate is mandatory.