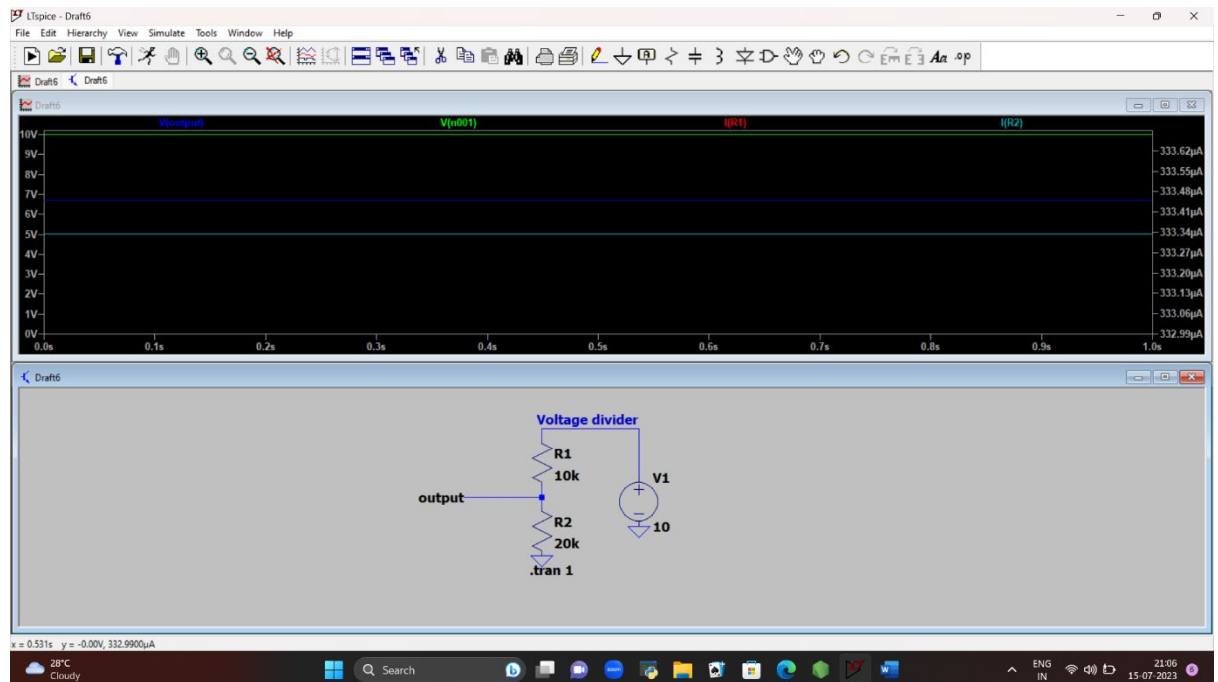


## Simulations Using LT Spice :

### 1. Voltage Divider

Voltage divider, a fundamental circuit that finds widespread use in electronics, here I had explored its behaviour and performance using LT Spice simulation.

We use 2 resistors connected in series with a voltage of 10V.



### Transient Simulation for Voltage Divider

Power Dissipation in each resistor:

$$\text{Total current flowing} = V/(R1+R2)$$

$$I = 0.333\text{mA}$$

$$\text{Power Dissipated in } R1 = I^2 R1$$

$$= 1.10\text{mW}$$

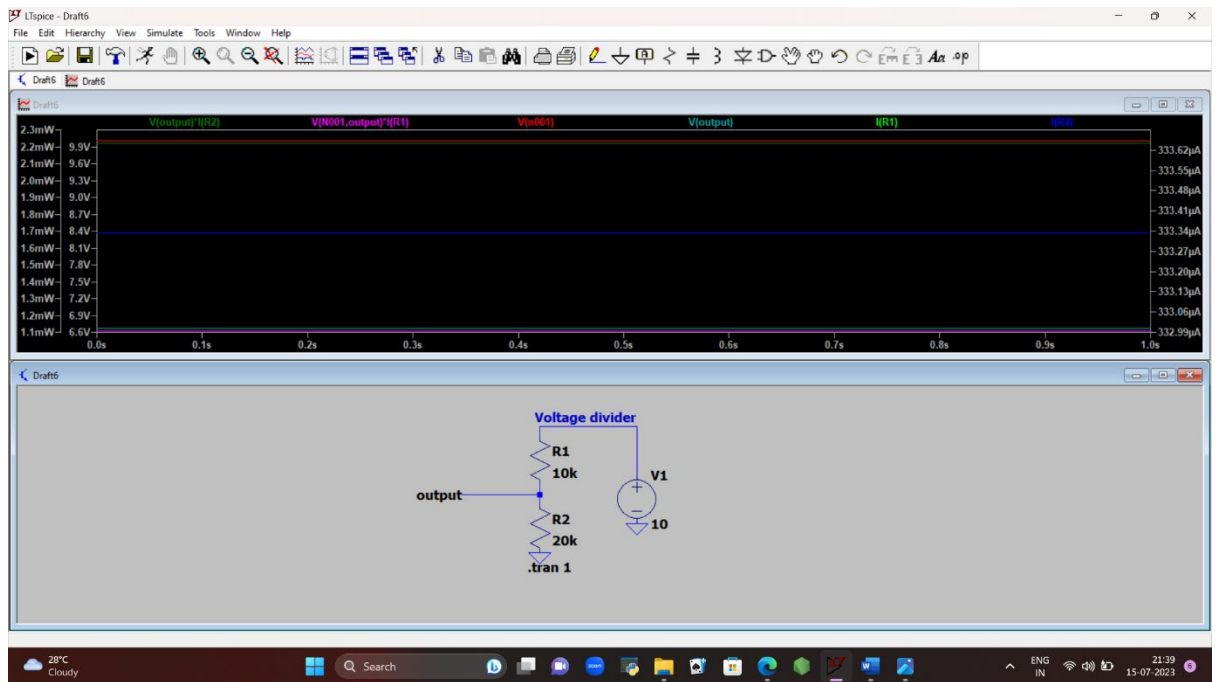
$$\text{Power Dissipated in } R2 = I^2 R2$$

$$= 2.21\text{mW}$$

Practical Applications of voltage divider:

Voltage dividers are commonly used for level shifting, biasing, and scaling signals.

The above simulation results can guide us in selecting suitable resistor values for specific applications, ensuring accurate voltage division.

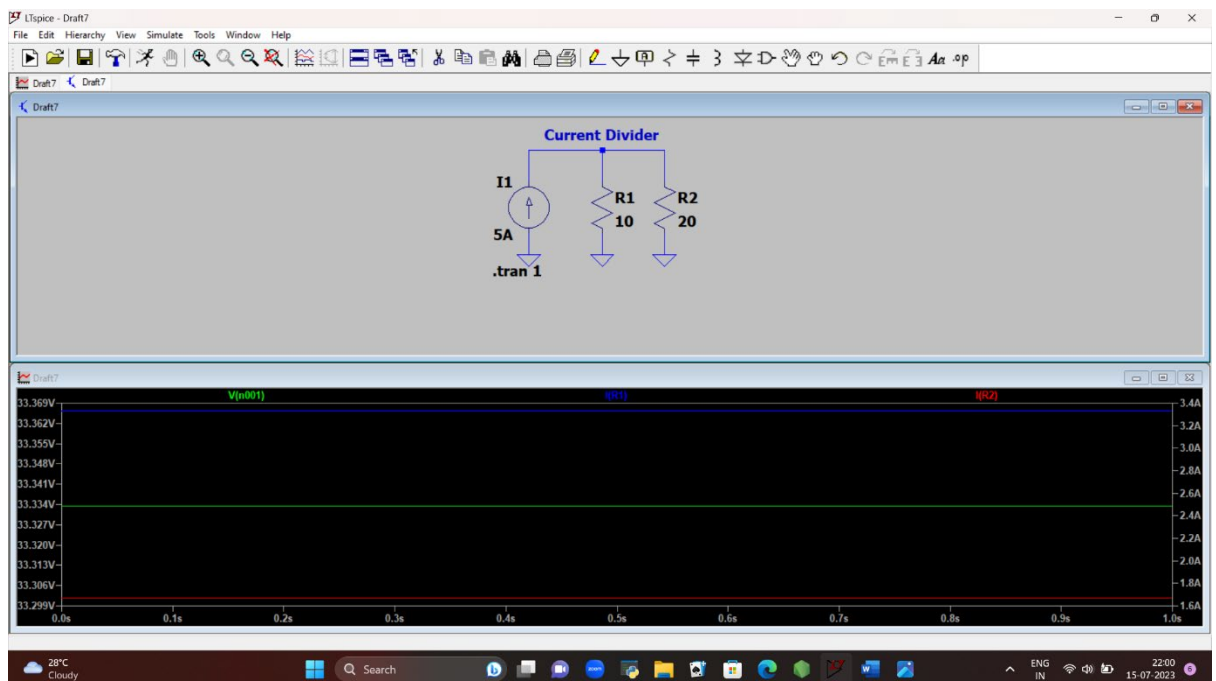


Power Dissipation Simulation

## 2. Current Divider

The current divider is an essential circuit used in electronic systems to divide a current into multiple branches.

Here we use 2 resistors connected in parallel with a current source of 5A.



Transient Simulation of current divider

Theoretical Analysis:

For this we apply KCL and ohm's law to derive equations governing current division.

$$V/R_1 + V/R_2 = 5A$$

Putting respective values, we get  $V = 33.33V$

By deriving the theoretical equations and performing simulations using LT Spice, we gained valuable insights into the expected current division ratios and verified them through practical experiments.

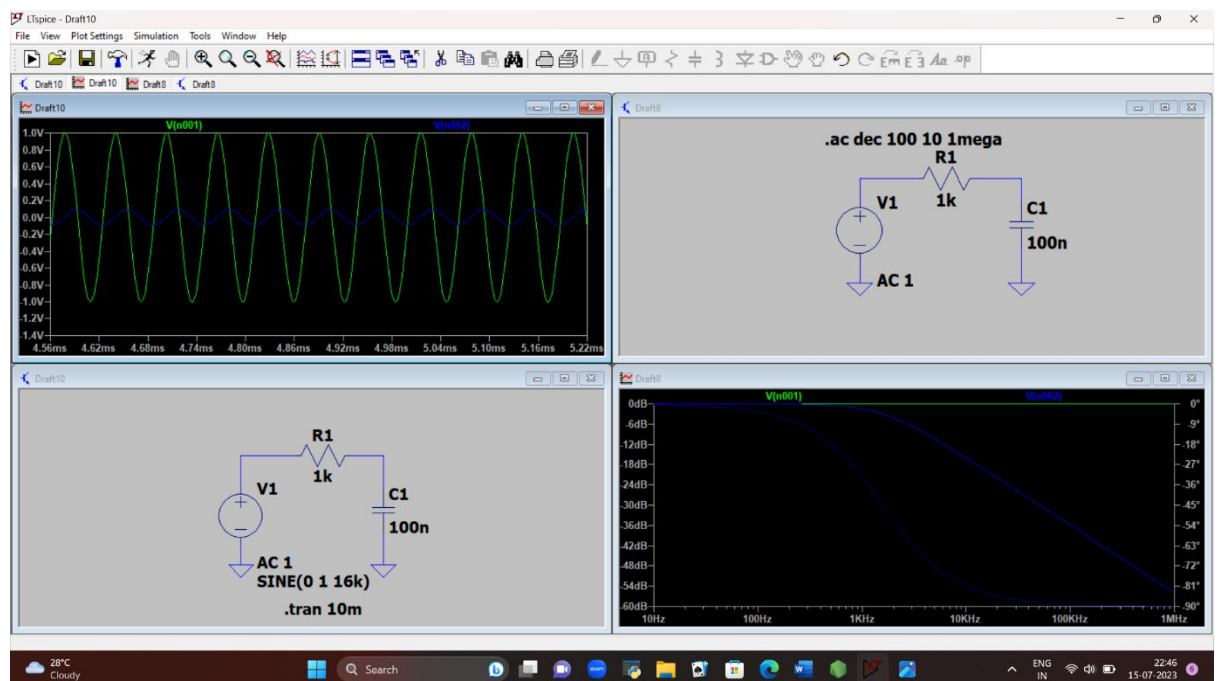
Practical Use of Current Divider:

Current dividers are frequently used to bias transistors, distribute currents in parallel circuits and balance loads in power distribution networks.

### 3. Low Pass Filter

The low pass filter typically consists of a resistor and a capacitor connected in series with the output taken from the junction between them.

The circuit allows low frequency signals to pass through while attenuating higher frequencies. Its purpose is to filter out noise or unwanted high frequency components from a signal.



Transient Analysis

AC Analysis

Theoretical Analysis:

By applying KVL, Ohm's Law and impedance properties of capacitor, we derived the transfer function and the cutoff frequency as follows,

$$H(j\omega) = 1/(1+j\omega RC)$$

And cut off frequency =  $1/(2\pi RC)$   
 $\sim 1.59\text{kHz}$

By systematically varying the resistor and capacitor values within specified ranges, we observed the changes in the frequency response. This analysis helped us understand the impact of component tolerances on the filter's cutoff frequency and attenuation characteristics.

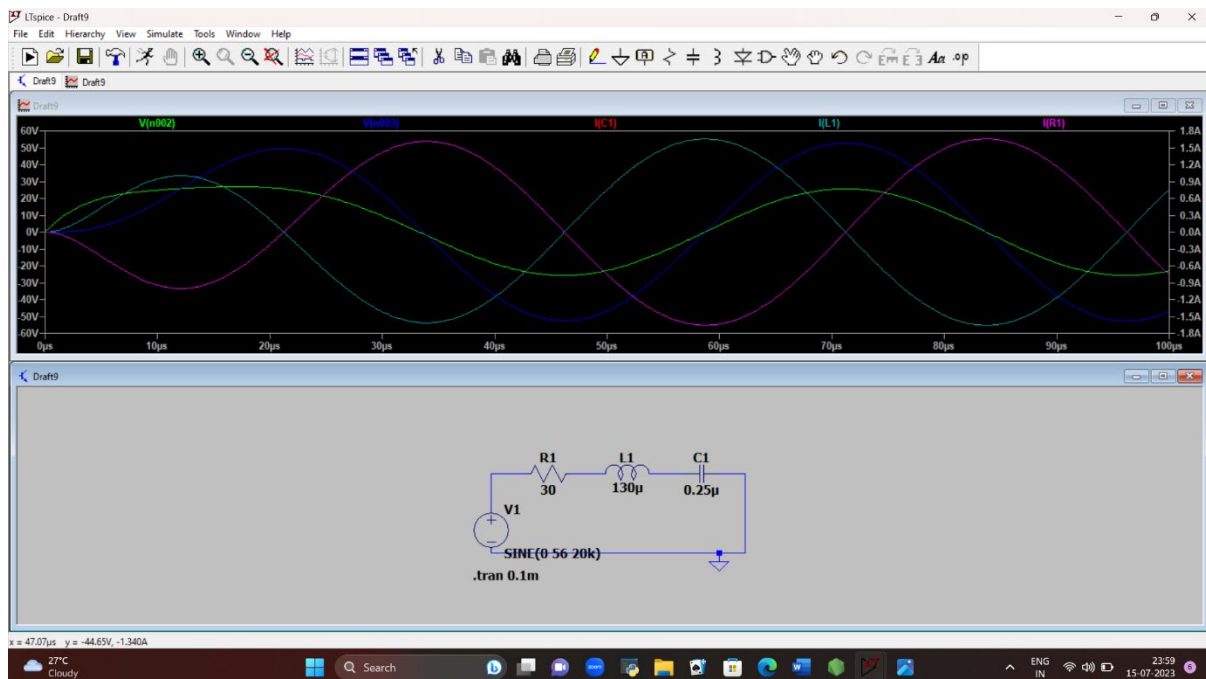
Practical Applications Of low pass filters:

Low pass filters are commonly used in audio signals, communication signals.

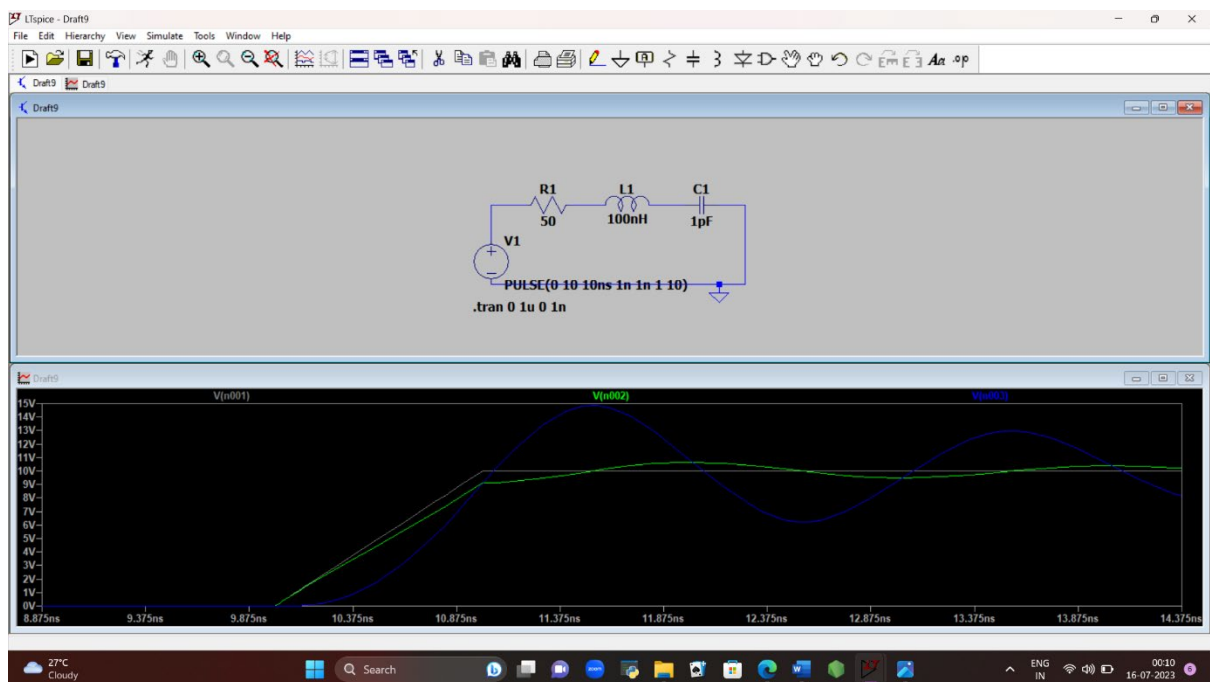
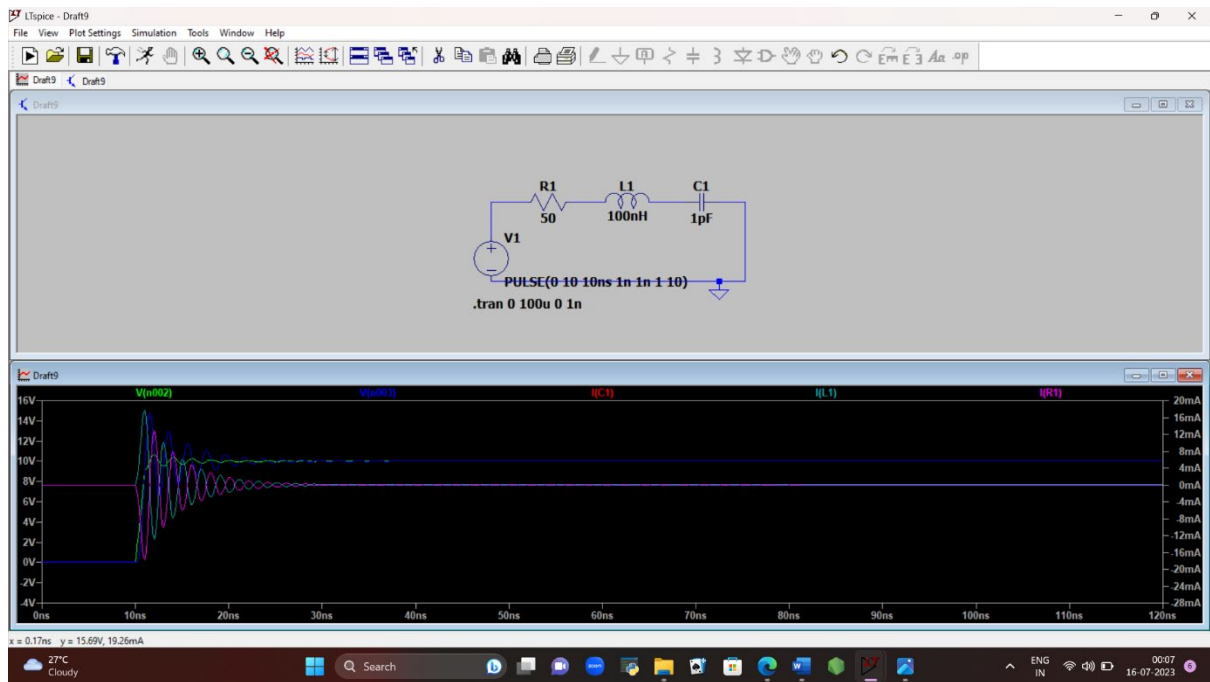
And also in signal processing to remove high frequency noise, or extract low freq. component

#### 4. RLC Circuit

Simulating steady state voltage and waveforms in a series RLC circuit.



Waveform simulation



RLC circuit comprise a resistor that dissipates energy, an inductor that stores energy in a magnetic field, and a capacitor that stores energy in an electric field.

By applying Kirchhoff's law and ohm's law and the equations governing inductors and capacitors, we derived the differential equations governing the RLC circuit behaviour.

$$V - I \cdot R - L \cdot \frac{dI}{dt} - \frac{1}{C} \cdot \left( \int I dt \right) = 0$$

### Transient Analysis:

The transient analysis allowed us to observe the circuit's response to different input stimuli over time. BY plotting the voltage and current waveforms, we could verify the theoretical predictions and analyze the circuit's behavior during transient events, such as charging and discharging of the capacitor or current buildup in the inductor.

### Practical Applications :

RLC circuits have numerous applications, including in filters, oscillators, resonant circuits, and impedance matching networks.

## 5. Differential Amplifier

Amplifies the difference between two input voltages but suppresses any voltage common to the two inputs. It is analog circuit with two inputs  $V_{in+}$  and  $V_{in-}$  and one output  $V_{out}$  in which the output is ideally proportional to the difference between the two voltages:

$$\begin{aligned} \text{Otherwise } V_{out} &= A(V_{in+} - V_{in-}) \quad \text{for ideal case, where } A \text{ is the gain of amplifier} \\ V_{out} &= A_d(V_{in+} - V_{in-}) + A_c(V_{in+} + V_{in-})/2 \\ &\quad \text{where } A_d \text{ is differential gain} \\ &\quad \quad \quad A_c \text{ is common mode gain} \end{aligned}$$

$$CMRR = 20 \log A_d / A_c$$

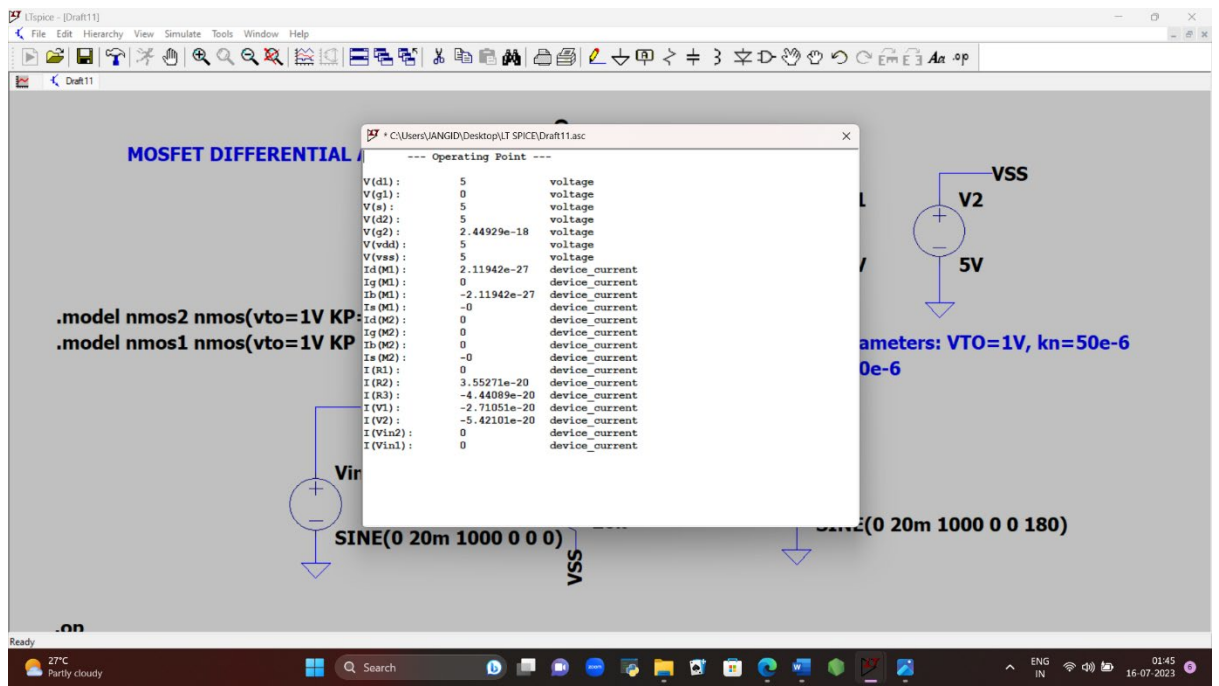
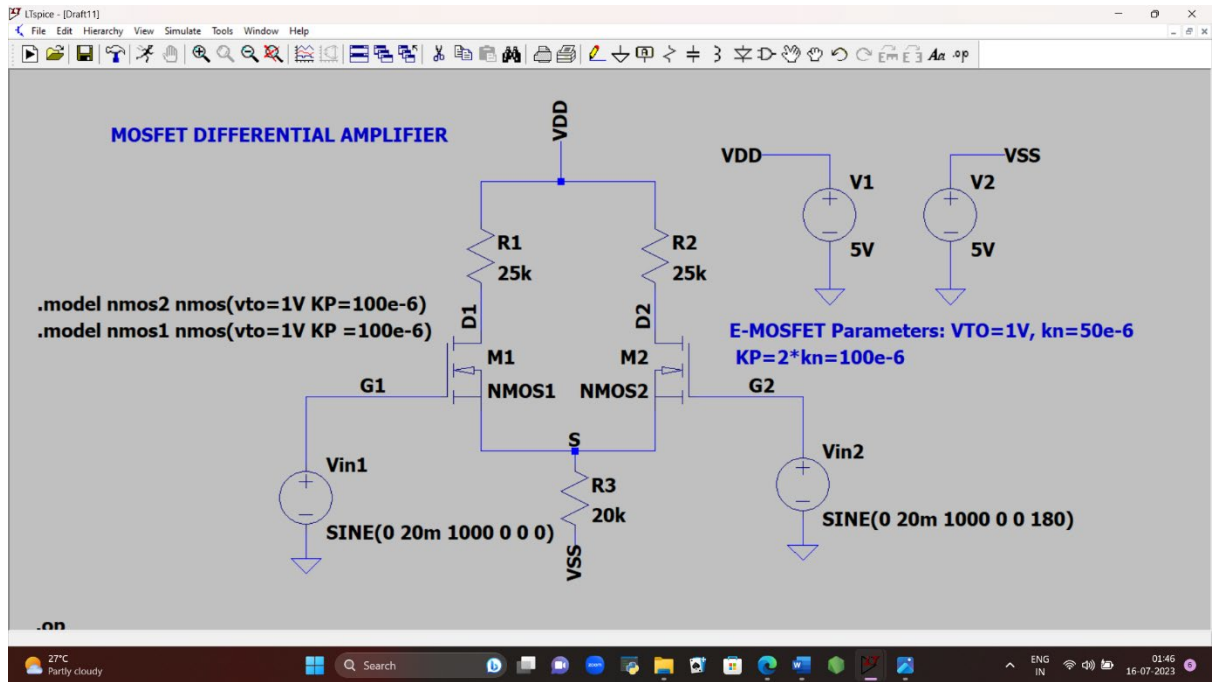
In a perfectly symmetric differential amplifier,  $A_c$  is zero, and the CMRR is infinite.

### Input/Output Impedance:

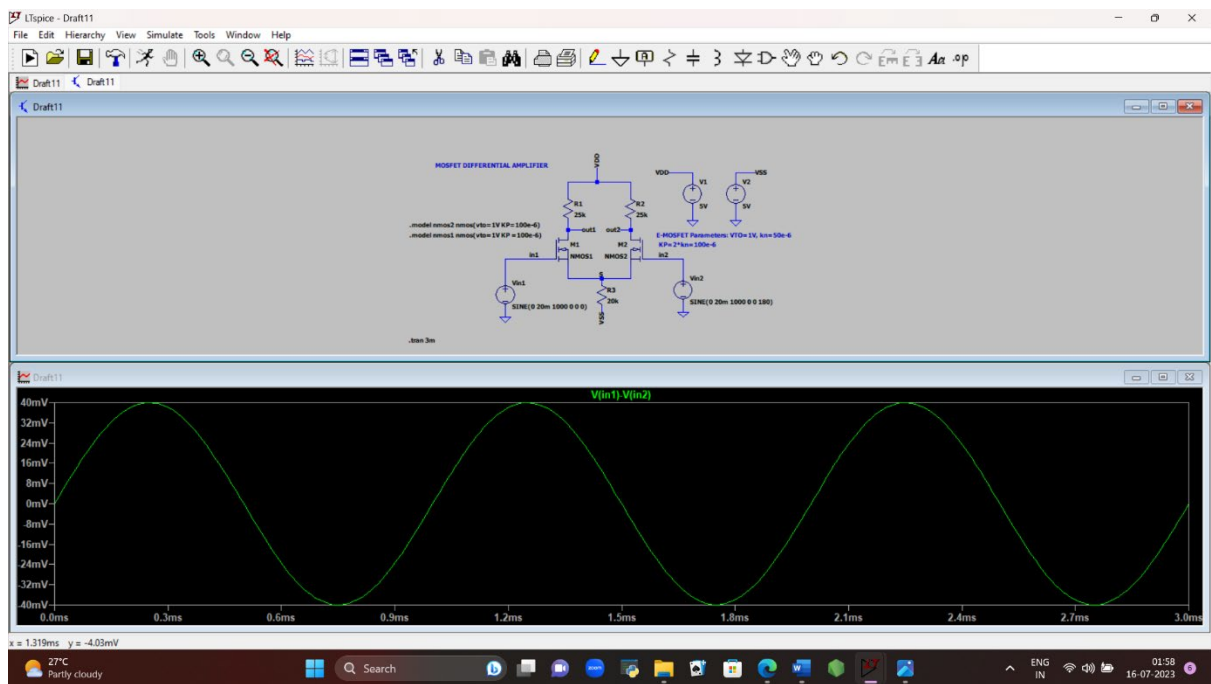
The input imp. of a differential amplifier refers to the impedance seen by the signal source connected to its input terminals. It is important to have a high input impedance to minimize loading effects. Conversely, the output impedance represents the impedance seen by the load connected to the amplifier's output. A low output impedance ensures efficient power transfer and reduces signal degradation.

### Bandwidth:

The bandwidth of a differential amplifier determines the range of frequencies over which it can amplify signals effectively. It usually defined as the frequency range where the differential voltage gain remains within a certain specified range.



DC Operating Points



AC Analysis