ANJALI GOPI

PYTHON AND MERN, CURRENTLY AT [MLCOMMONS](#)

3+ YEARS OF EXPERIENCE BUILDING DEBUG TOOLS WITH PYTHON.

I ENJOY DEVELOPING SCALABLE, QUALITY SOFTWARE WITH FAST-MOVING TEAMS.

EXPERIENCE

Apr 2020 | present

Software Engineer

Stealth Startup — San Francisco Bay Area

- Created an app grading system that enables more consumer transparency. Hosted on **AWS** at **[goodconscience](#)**. Early engineer **owning full stack** including **MongoDB, Express, React, Node.js, Bootstrap**.
- Built a robust back-end to perform CRUD operations on data in MongoDB **NoSQL database** and a responsive front-end with reusable components using React. Optimized the REST API calls to back-end.

Jun 2019 | Mar 2020

Staff Engineer

Ampere Computing — San Francisco Bay Area

- Developed **quality driver code routines in C for DDR IO** tests. Reviewer for DDR PHY and memory controller driver code including training algorithms.
- **Instrumental in the debug/identification** of bugs in the driver code. This resulted in significant **schedule impact** to Ampere's first gen ARM Altra server.
- **Led memory analog validation**. Responsible for compliance testing, post-Si validation, driver code development. Mentored junior engineers in the team.

May 2017 | Jun 2019

Analog Engineer

Intel Corporation — San Francisco Bay Area

- Expedited debug by developing HBM PHY **Python scripts** with silicon DFx features. Improved the **HBM IO AI accelerator's** health and performance using stress tuning and timing optimization Python tests. **Led** the validation/debug activities.
- Automated temperature/voltage margining, post processing **using Python that greatly reduced the data collection and analysis time**.
- **Received team recoginition for accelerated product launch and unblocking teams.**

Jun 2013 | May 2017

Hardware Engineer

Intel Corporation — Bangalore, India

- Co-authored an **invention disclosure** on USB OTG. This also resulted in significant SoC power savings. Led and declared power-on exit, ahead of schedule by a week.
- Part of memory reference code debug - unblocked the shipment of over 1000 boards, **Celeron N3000 product release**. Received **cross team recognition award**.

EDUCATION

2009 | 2013

BE Electronics and Communication Engineering

PSG College of Technology, CGPA: 9.55/10 — Coimbatore, India

CONTACT INFO

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WORK AUTHORIZATION

Green Card

SKILLS

- **Python**
- C
- **MongoDB, Express, Node.js**
- **React, JavaScript, HTML5, CSS3**
- **AWS, Docker, TDD, API Integration and Testing**
- **Flask**, Bokeh, OpenCV, Django
- Cloud Architecture, Microservices, OAuth, HTTP, SQL

- JMP, Unix Bash, C++
- Verilog, Assembly
- System Validation
- Board Design

MASSIVE OPEN ONLINE COURSES

The Complete 2020 Full Stack Web Development Bootcamp - Jun 2020

AWS Concepts and Essentials - Jun 2020

Introduction to Programming in C - Specialization - Dec 2019

Analog Design Engineering Intern Intel Corporation - Jan - May 2013

Worked on the design and validation of VR feeding memory. This greatly reduced the validation time of an Atom based Tablet silicon.