ANJALI GOPI

PYTHON AND MERN FULL STACK ENGINEER

💼 EXPERIENCE

Apr 2020
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present

Software Engineer
Stealth Startup   📍 San Francisco Bay Area

- Created an app grading system that enables more consumer transparency. Hosted on **AWS** at **goodconscience**.
- Early engineer **owning full stack** including **MongoDB, Express, React, NodeJS, Bootstrap**.
- Built a responsive front-end with reusable components using **React**. Optimized the REST API calls to back-end.
- Built a robust back-end to perform CRUD operations on data in **MongoDB NoSQL database**.

Jun 2019
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Mar 2020

Staff Engineer
Ampere Computing   📍 San Francisco Bay Area

- **Led memory analog validation** resulting in a successful power-on of Ampere's first gen ARM based Altra server products. Responsible for compliance testing, post-Si validation, assembly/bare metal code development. **Mentored** junior engineers in the team.
- Developed driver routines in **C for DDR IO** tests. Reviewer for DDR PHY and memory controller driver code including tarining algorithms.
- **Instrumental in the debug/identification** of bugs in the driver code around the DRAM reset sequence and PHY IO drive settings.

May 2017
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Jun 2019

Analog Engineer
Intel Corporation   📍 San Francisco Bay Area

- Successfully debugged and validated **HBM IO** across PVT corners for an AI accelerator. **Architected and led** the validation/debug plan for the next product.
- **Expedited debug** by developing HBM PHY Python scripts with silicon DFx features. Improved the IO health and performance using pattern generator stress tuning and timing optimization.
- **Automated** temperature/voltage margining and post processing **scripts using Python** that greatly reduced the data collection and analysis time.
- Received **team recognition** for resolving bugs that prevented the HBM PHY from coming out of reset and thereby unblocking lot of teams.

Jun 2013
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May 2017

Hardware Engineer
Intel Corporation   📍 Bangalore, India

- As a key board designer, **led the successful power-on** of critical quad core Atom SoC based platform. Completed debug and validation ahead of schedule by 1 week, and initiated HVM.
- Co-authored an **invention disclosure** on USB OTG. Proposed, designed and validated a platform workaround for a SoC bug. This also resulted in significant SoC power savings.
- Supported the debug of a critical memory reference code issue (as team of 3). This helped unblock the shipment of over 1000 boards, enable silicon validation communities and **unblock Celeron N3000 product release** . Received **cross team recognition award** for the same.

Jan 2013
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May 2013

Analog Design Engineering Intern
Intel Corporation   📍 Bangalore, India

- Developed discrete power management reference modules that greatly reduced the validation time of an Atom based Tablet silicon. Worked on the design and validation of VR feeding memory

🎓 EDUCATION

2009
|
2013

BE Electronics and Communication Engineering
PSG College of Technology   📍 Coimbatore, India

CGPA: 9.55

CONTACT INFO

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⊙ anjaligopi
📞 6502658217

WORK AUTHORIZATION

Green Card Holder

SKILLS

Python
C
MongoDB, Express, Node
React, JavaScript, HTML5, CSS3

AWS, JMP, Unix Bash, C++
Verilog, Assembly
System Validation
Board Design

MASSIVE OPEN ONLINE COURSES

The Complete 2020 Web Development Bootcamp - Udemy - Jun 2020

Full-stack web development.
HTML, CSS, Bootstrap, Javascript, Node, React, Express, MongoDB.

AWS Concepts and Essentials - Linux Academy - Jun 2020

IAM, Networking, EC2, S3, Databases.
SNS, Load Balancing, Elasticity, Scalability, Serverless.

Introduction to Prgramming in C - Specialization - Coursera - Dec 2019

Writing, Running, and Fixing Code in C.
Pointers, Arrays, Recursion.
Interacting with the System and Managing Memory.

This résumé was wholly typeset with HTML/CSS — see `git.io/vVSYL`