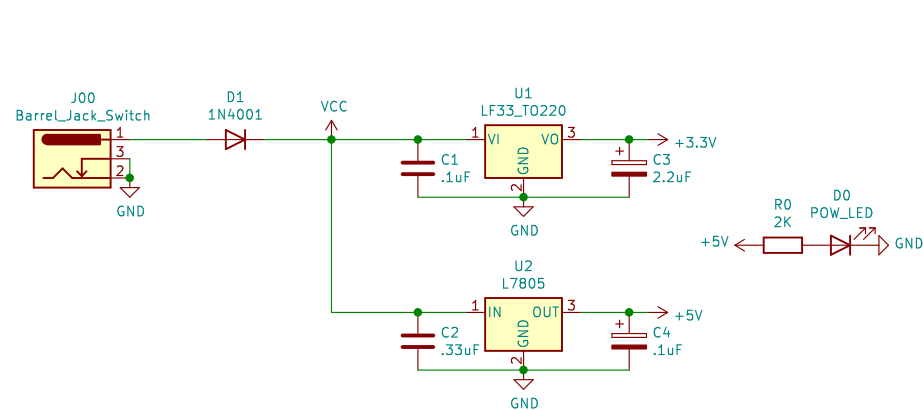
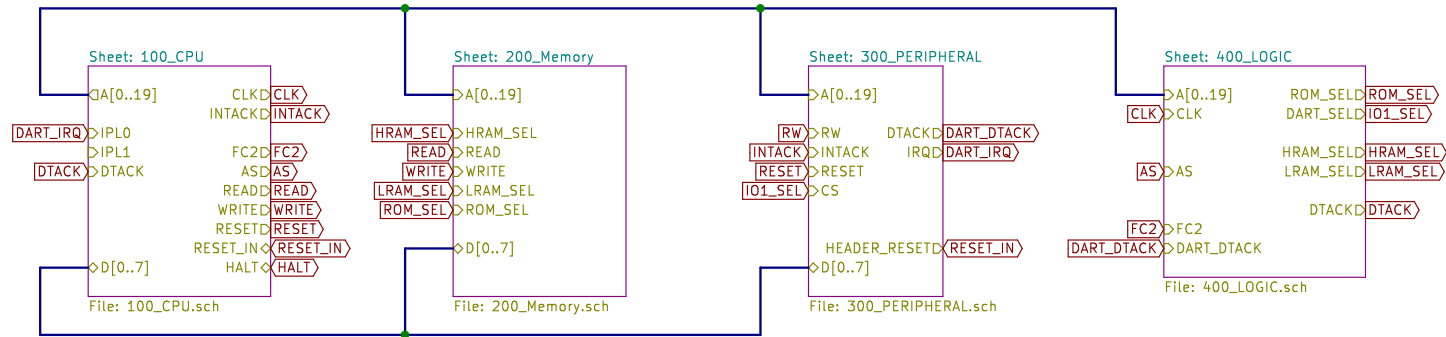


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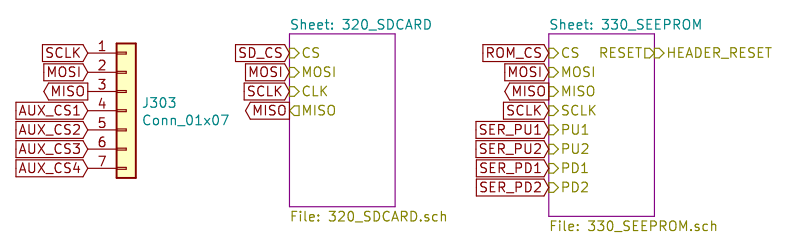
- H0 MountingHole
- H1 MountingHole
- H2 MountingHole
- H3 MountingHole

Sheet: /		
File: 68k8.sch		
Title: 68k8 SBC		
Size: A4	Date: 2021-07-16	Rev:
KiCad E.D.A. kicad (5.1.10)-1		Id: 1/8

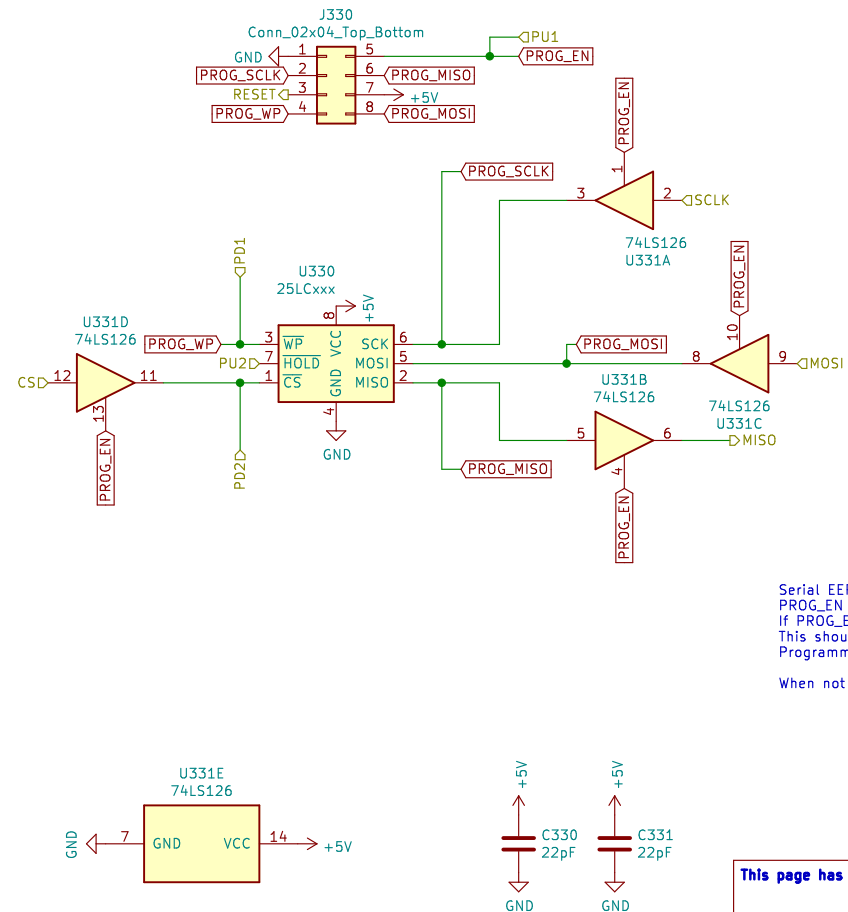


DTACK is low when RAM/ROM enabled and driven by DART_DTACK otherwise

Rev:
Id: 2/8



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Sheet: /300_PERIPHERAL/ File: 300_PERIPHERAL.sch	
Title:	
Size: A4	Rev:
KiCad E.D.A. kicad (5.1.10)–1	Id: 3/8



Serial EEPROM Programmable via SPI programmer
 PROG_EN disables all signals to/from CPU and replaces with signals from external programmer
 If PROG_EN is low, the CPU can not access chip
 This should allow for remote programming without needing to remove EEPROM
 Programmer can also force CPU reset

When not programming, lines should be disconnected or set to Hi-Z on programmer

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Sheet: /300_PERIPHERAL/330_SEEPROM/
 File: 330_SEEPROM.sch

Title:

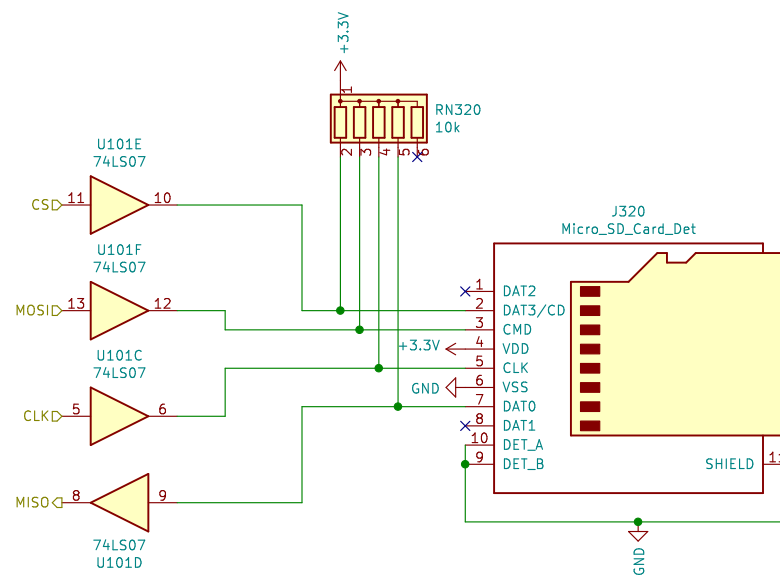
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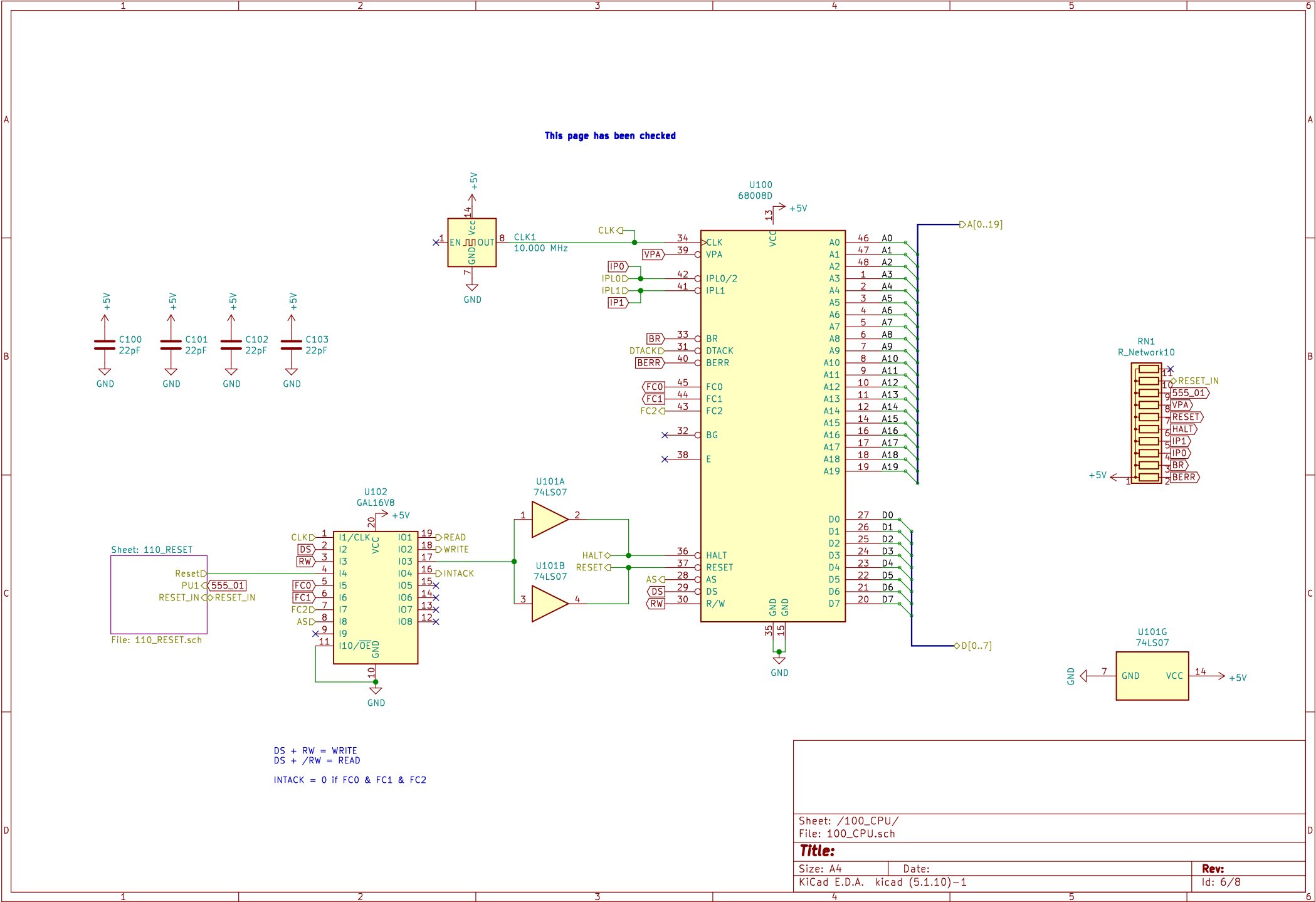
Id: 4/8



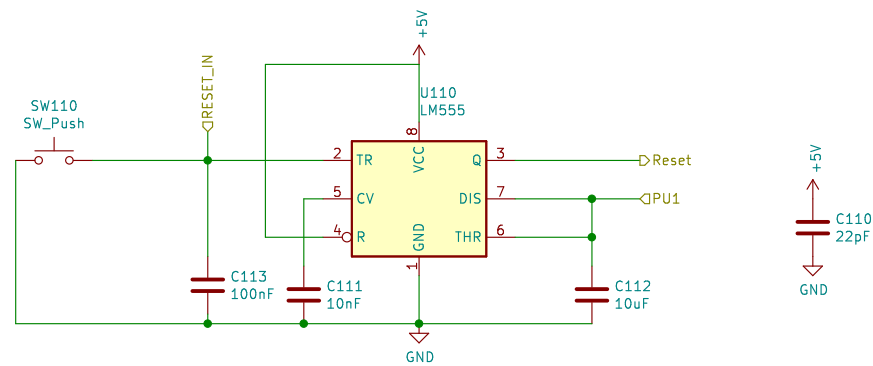
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Size: A4	Date:
KiCad E.D.A. kicad (5.1.10)–1	

Rev:
Id: 5/8

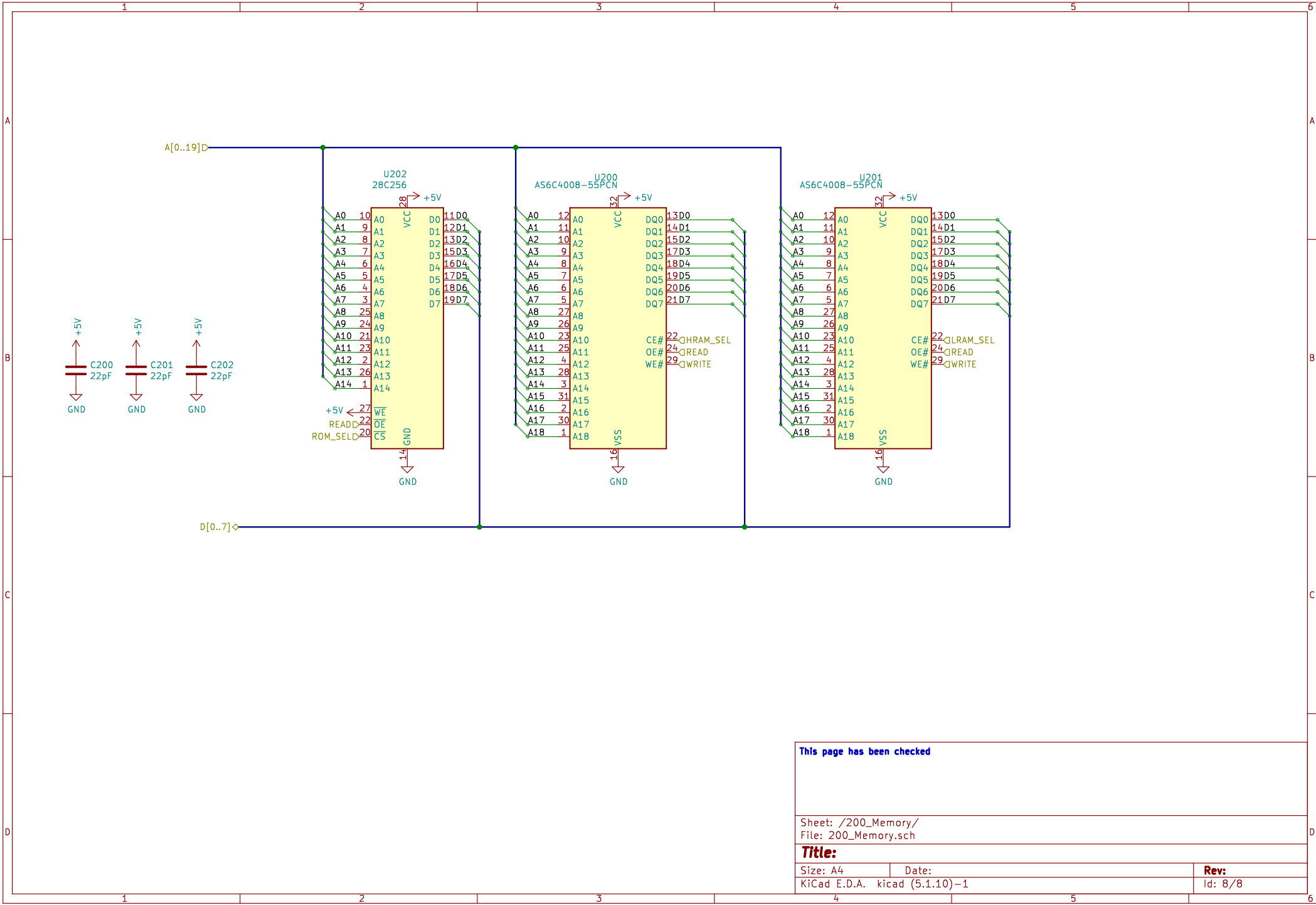


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Power on Reset
Reset signal is active high

Sheet: /100_CPU/110_RESET/ File: 110_RESET.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.10)-1		Id: 7/8



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Sheet: /200_Memory/
File: 200_Memory.sch

Title:

Size: A4
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Rev:
Id: 8/8