

**This page has been checked!**

Sheet: /3000\_Memory/  
File: 3000\_Memory.sch

**Title:**

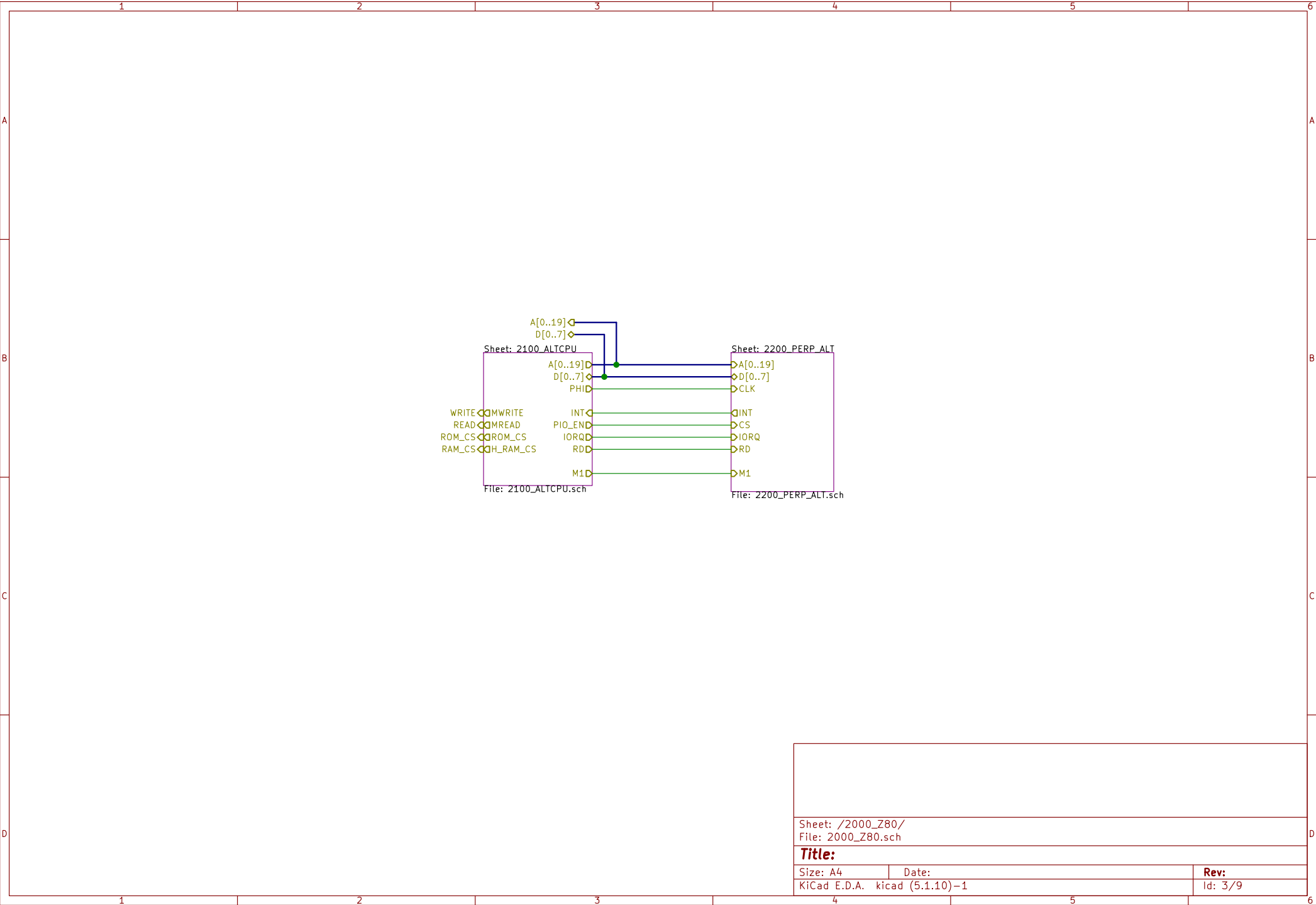
Size: A4

Date:

KiCad E.D.A. kicad (5.1.10)-1

**Rev:**

Id: 2/9







Sheet: /2000\_Z80/2200\_PERP\_ALT/2210\_SD\_Card/  
File: 2210\_SD\_Card.sch

**Title:**

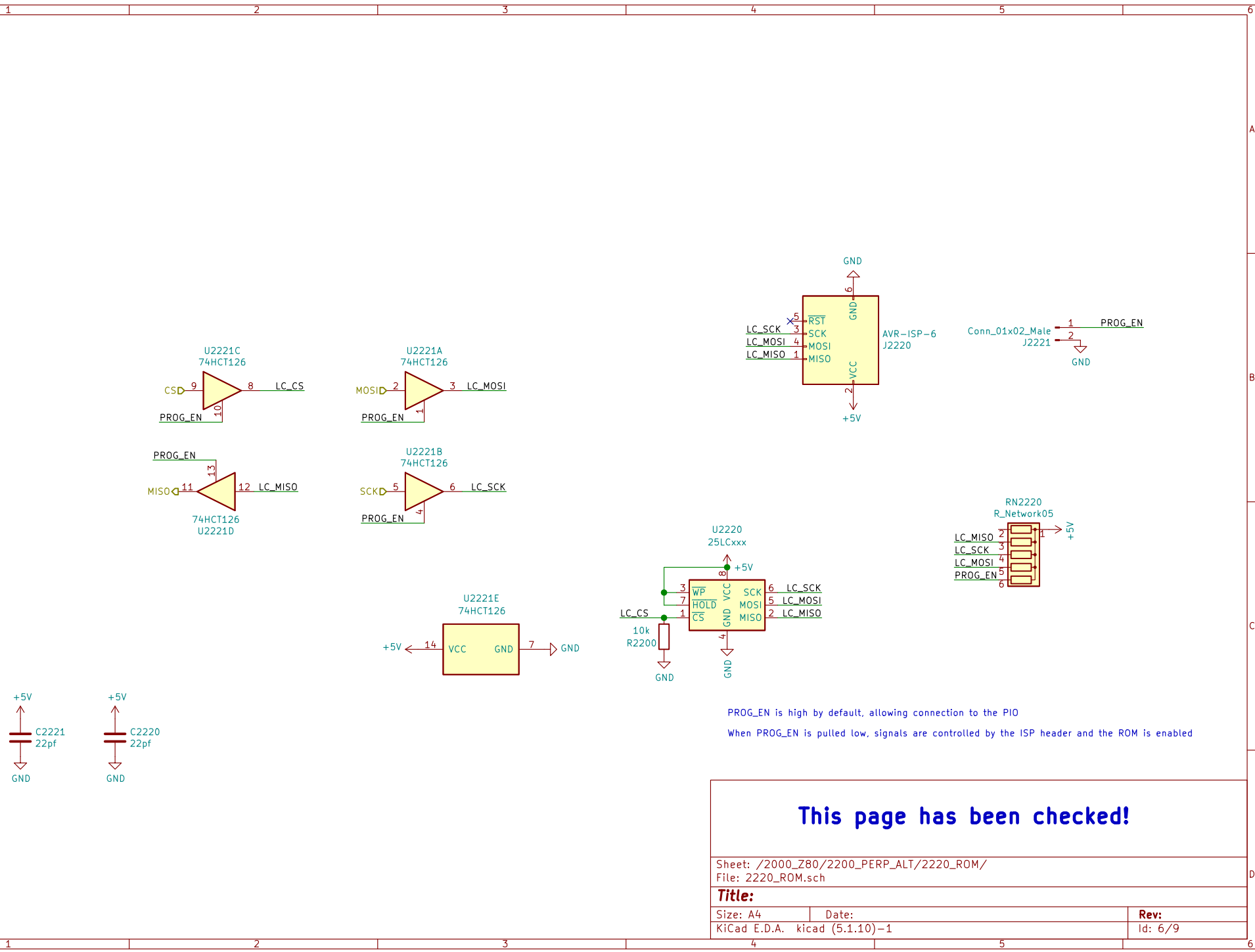
Size: A4

Date:

KiCad E.D.A. kicad (5.1.10)-1

**Rev:**

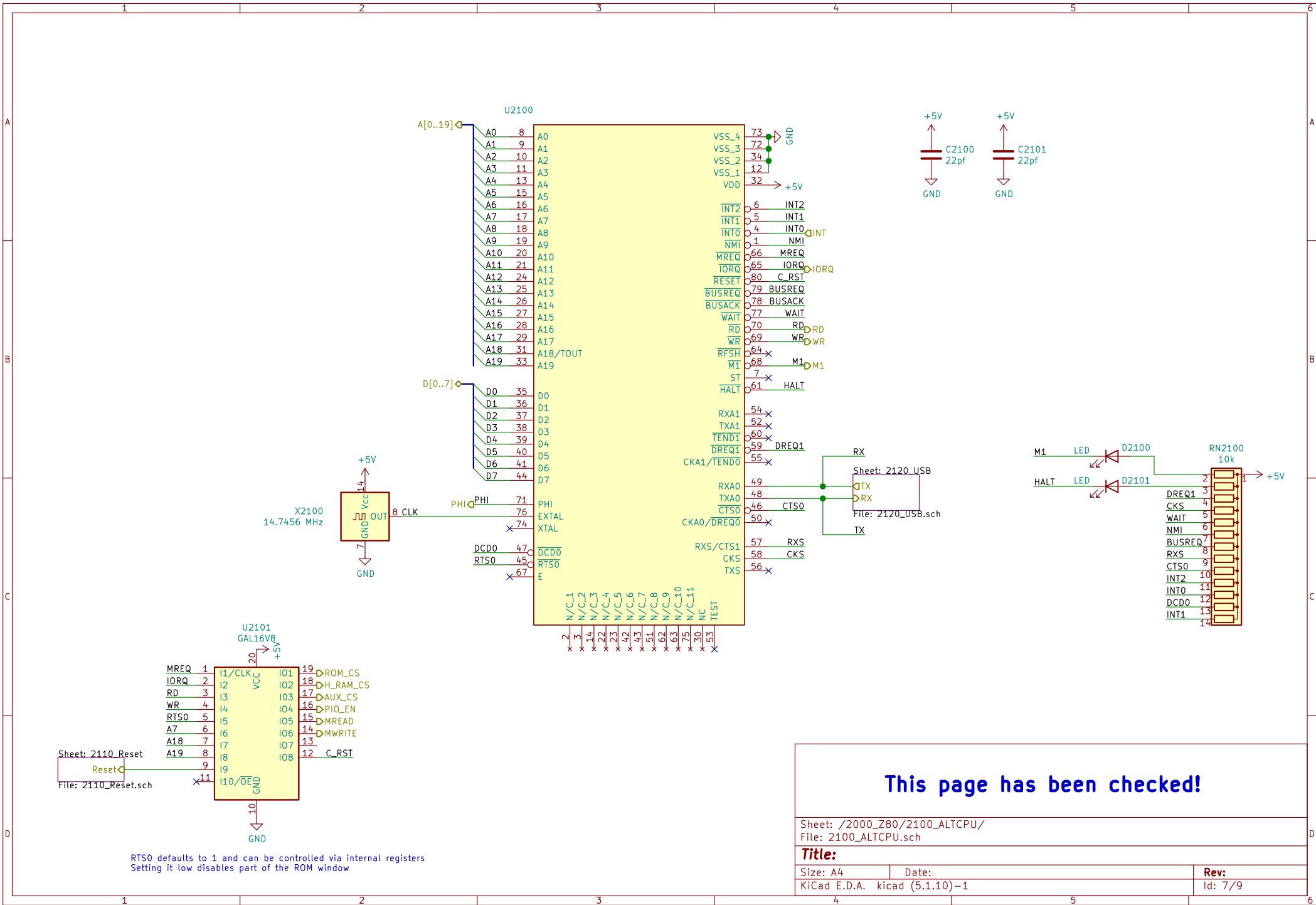
Id: 5/9



PROG\_EN is high by default, allowing connection to the PIO  
When PROG\_EN is pulled low, signals are controlled by the ISP header and the ROM is enabled

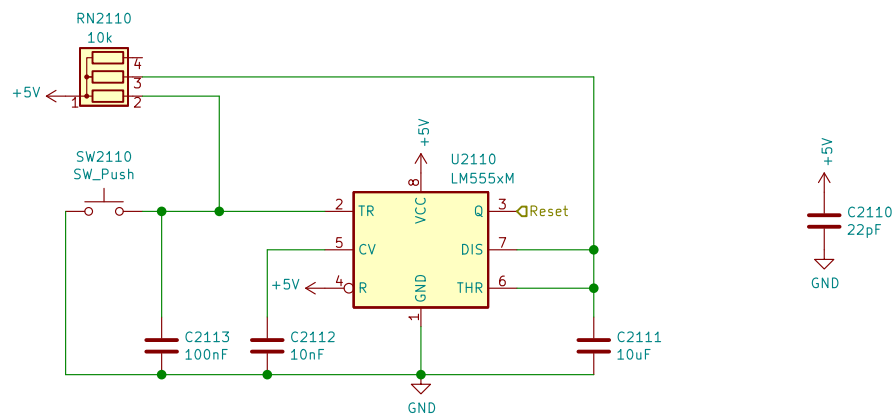
**This page has been checked!**

Sheet: /2000_Z80/2200_PERP_ALT/2220_ROM/		
File: 2220_ROM.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.10)–1		Id: 6/9



This page has been checked!

Sheet: /2000_Z80/2100_ALTCPU/ File: 2100_ALTCPU.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.10)-1		Id: 7/9



Power on Reset  
Reset signal is active high  
Needs to be inverted for CPU

**This page has been checked!**

Sheet: /2000\_Z80/2100\_ALTCPU/2110\_Reset/  
File: 2110\_Reset.sch

**Title:**

Size: A4

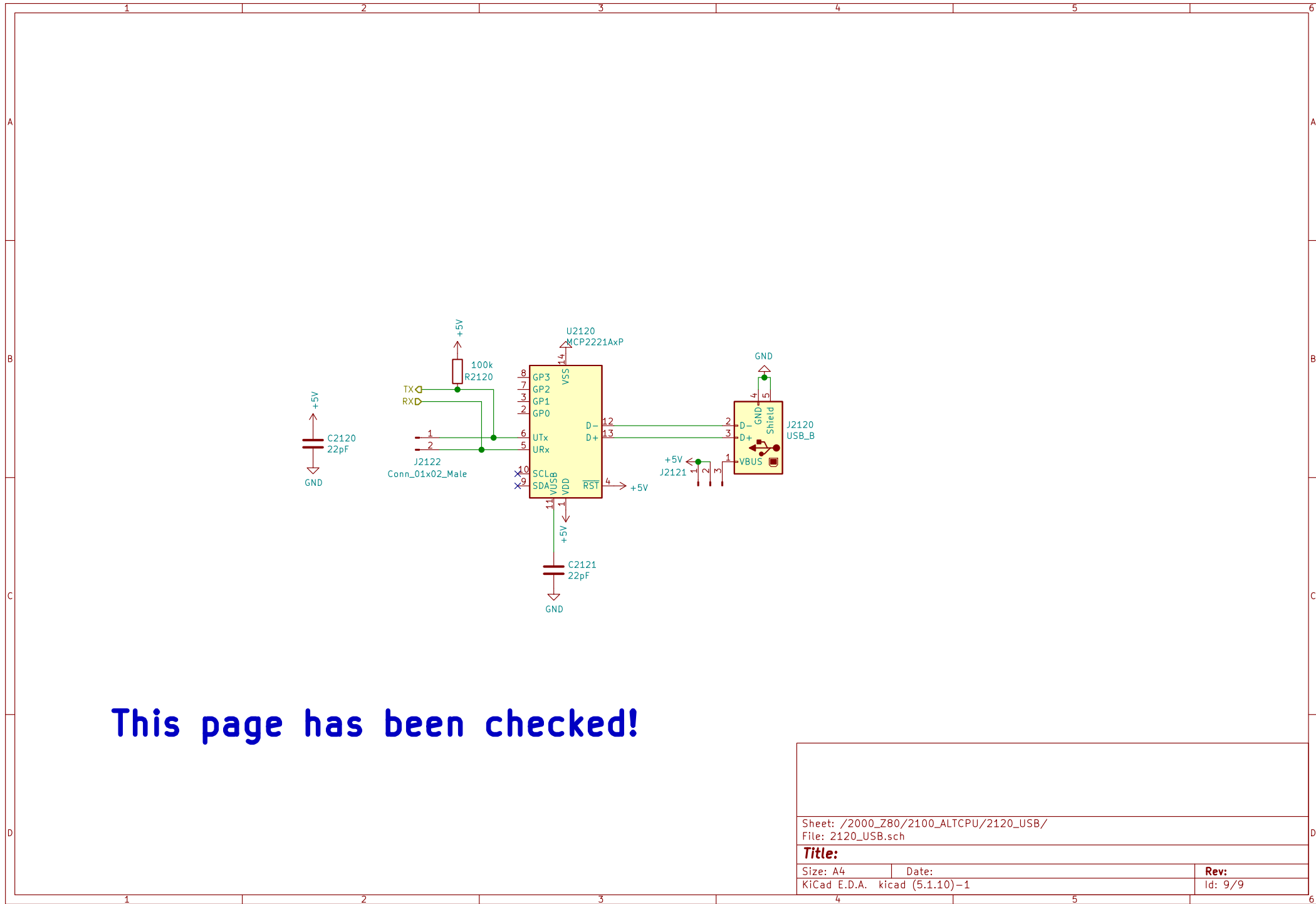
Date:

KiCad E.D.A. kicad (5.1.10)-1

**Rev:**

Id: 8/9





This page has been checked!

Sheet: /2000_Z80/2100_ALTCPU/2120_USB/ File: 2120_USB.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.10)-1		Id: 9/9