



## Table of Control Signals

Instruction	Opcode	RegWrite	ALUSrc	Branch	MemRead	MemWrite	MemToReg	JALR	ALUOp
R-type	0110011	1	0	0	0	0	0	0	2
I-type	0010011	1	1	0	0	0	0	0	3
LBU/LW	0000011	1	1	0	1	0	1	0	0
SH/SW	0100011	0	1	0	0	1	0	0	0
BNE	1100011	0	0	1	0	0	0	0	1
JALR	1100111	1	1	0	0	0	0	1	0
LUI	0110111	0	1	0	0	0	0	0	4