

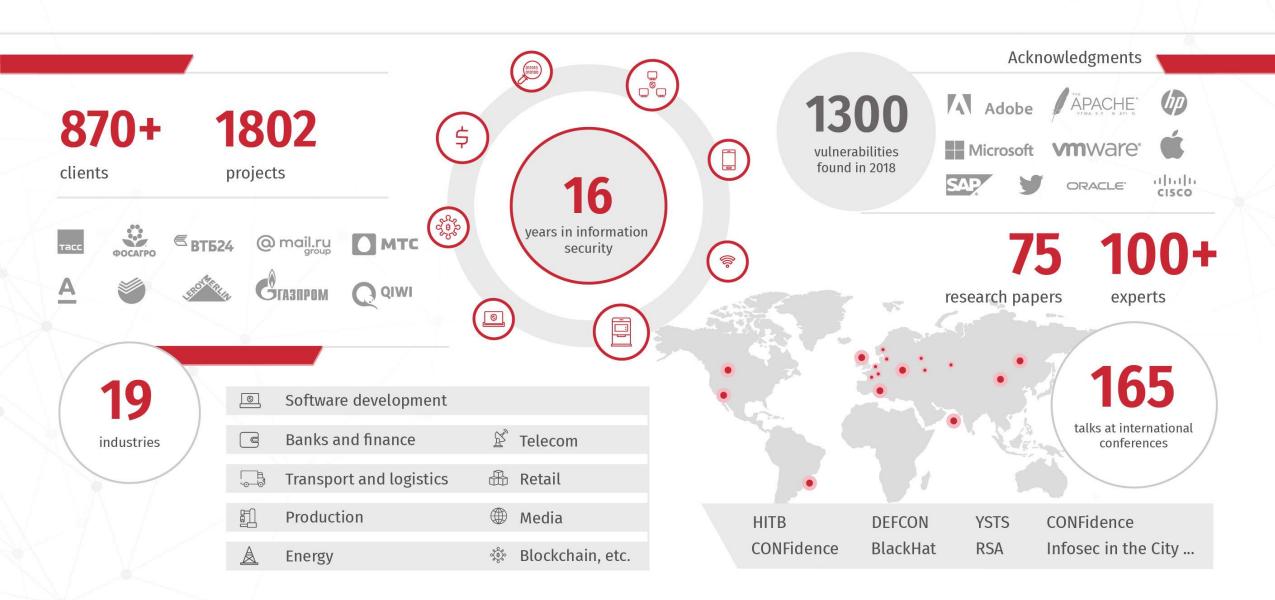
H2HC 2019

Launching feedback-driven fuzzing on TrustZone TEE

Andrey Akimov, security researcher



LEADING INFORMATION SECURITY SERVICES PROVIDER







- Samsung S8 usage of ARM TrustZone Trustonic Kinibi
- Searching for attack target
- Exploring TrustZone implementation
- Trusted applications
- Fuzzing
- Crash analysis
- Results
- Exploitation of SVE-2019-14126





Usage



GLOBALPLATFORM®

- Corporate services
- Content management
- Personal data protection
- Connectivity protection
- Mobile financial services

SAMSUNG

- Hardware secure storage
- Authentication, biometrics
- Hardware cryptographic engine
- Digital Rights Management (DRM)
- Protecting and monitoring of the Normal World by the Secure World
 - Real-Time Kernel Protection (RKP)
 - Periodic Kernel Measurement (PKM)
- Trusted user interface



TEE operating system

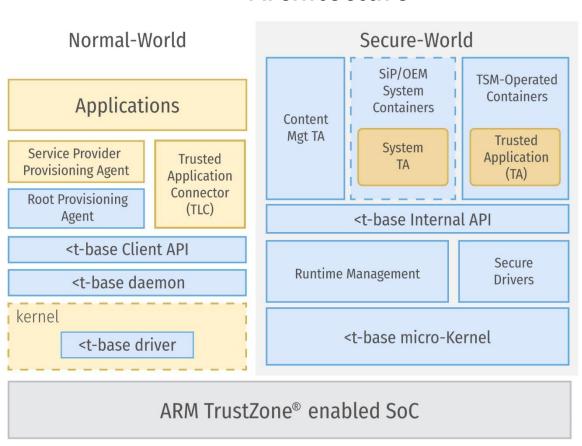
- Ex. G&D mobicore, <t-base
- Samsung Exynos SoCs: Galaxy S3 to Galaxy S9 Trustonic Kinibi
- Samsung Galaxy S10 Samsung Teegris
- github: trustonic-tee-user-space
- github: trustonic-tee-driver
- Old Qualcomm leak with Trustonic Kinibi SDK qcom_leaked_sources.zip
 - secure world headers
 - secure world static libraries
 - documentation
 - etc.

TRUSTONIC

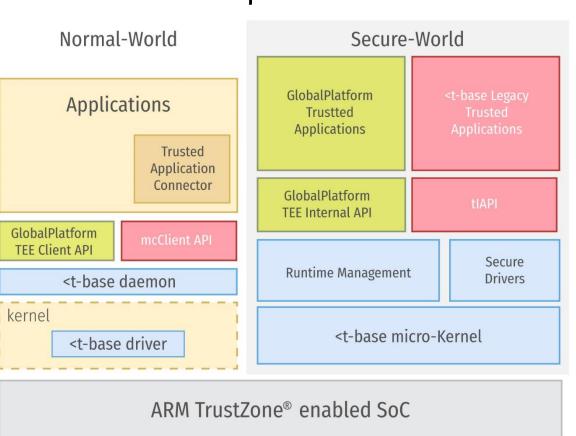


Normal World and Secure World

Architecture



Developer's view





Normal World

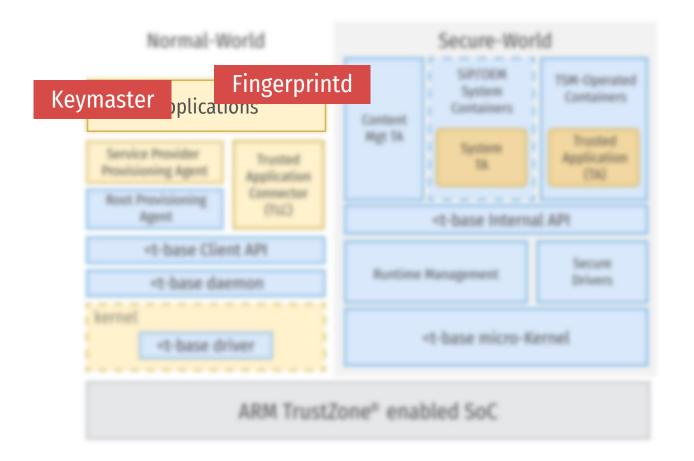
Exploring Android file system



Client applications

- Keymaster
 - access to key information
- Fingerprintd
 - biometrics
- Samsung Pay

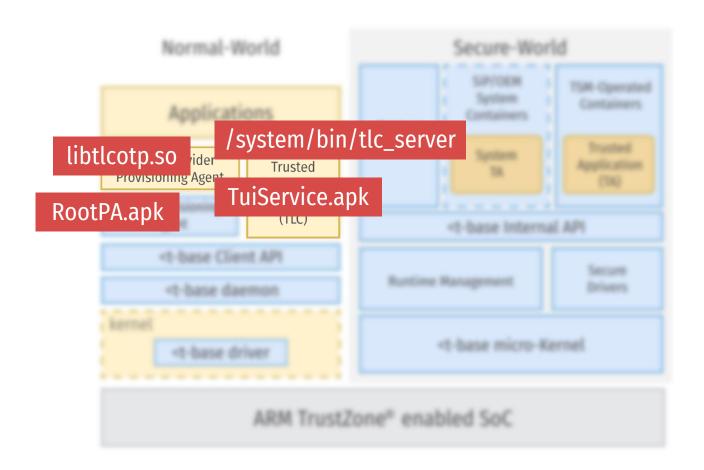
•





Trusted application connector

- Native libraries
 - libtlcotp.so
 - libtlc_direct_comm.so
 - •
- Binder
 - /system/bin/tlc_server access to trustlets via Binder interface
 - TuiService.apk access to TUI
- Service provider provisioning agent
- Root provisioning agent
 - RootPA.apk gd.mobicore.pa

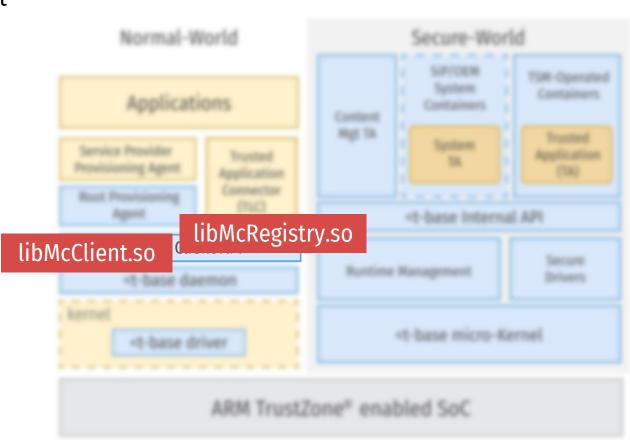






Client API

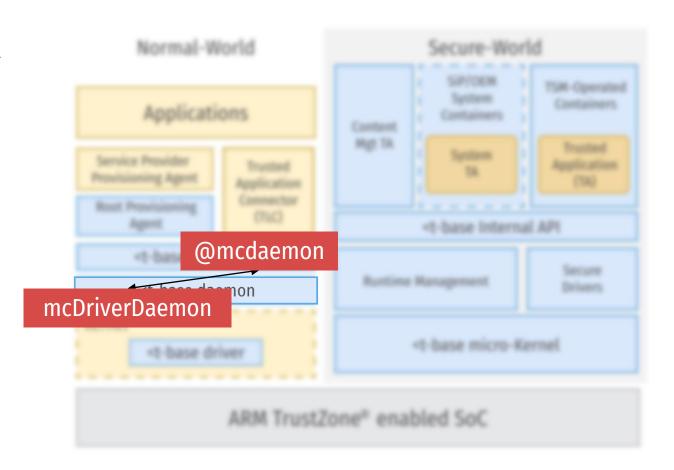
- /system/vendor/lib64/libMcClient.so trustlet communication
 - mcOpenSession
 - mcMallocWsm
 - mcNotify
 - •
- /system/vendor/lib64/libMcRegistry.so registry management
 - mcRegistryStoreAuthToken
 - mcRegistryStoreSp
 - ..





Daemon

- /system/vendor/bin/mcDriverDaemon
- Communicates through @mcdaemon socket
- SELinux
 - u:object_r:mobicoredaemon_exec:s0





Kernel components

- Official open source Android kernel
- Community builds
 - TGP Kernel
 - Xceed
 - BatStock-Kernel V1.8.0
 - •
- make menuconfig
 - TrustZone related kernel components
- Trustonic TEE Driver
 - triggers SMC to switch CPU to Secure World

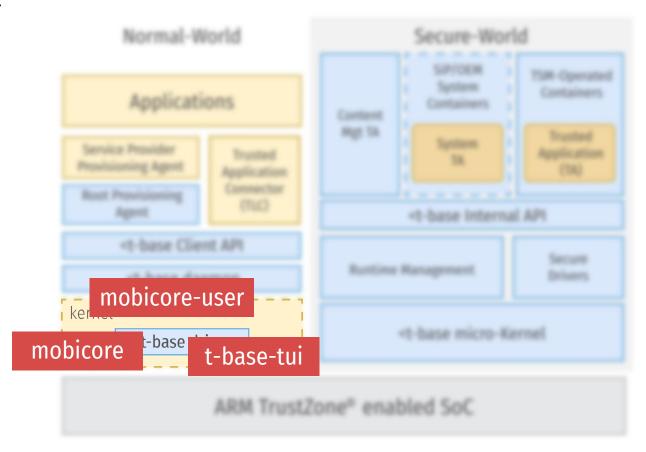
```
Kernel console over STM devices
< > Intel(R) Trace Hub controller
    FPGA Configuration Support --->
[*] BTS driver support --->
    TRACE driver support --->
   Trustonic TEE Driver
     Trustonic TEE uses LPAE
     Trustonic TEE driver debug mode
     Trustonic Trusted UI
       Trustonic Trusted UI with fb blank
       TBase Trusted UI use touch related code
     Secure OS control
     Secure OS booster API
     Seucre OS booster API supports MCT disable
   Vision Support --->
    *** CCIC configs ***
   CCIC notifier support
   CCIC S2MM003
   CCIC S2MM005
   support CCIC alternate mode
   Support LPM ENABLE
   support WATER DETECT
[*] Samsung NFC driver
     Near Field Communication (NFC) devices --->
<*> Sensors ssp
```





Kernel components

- Main kernel entry points
 - /dev/mobicore administration tasks
 - /dev/mobicore-user client application trusted application communication
 - /dev/t-base-tui trusted user interface
- SELinux enforced
 - u:object_r:mobicore_device:s0
 - u:object_r:mobicore_user_device:s0
 - u:object_r:tui_device:s0





Secure World

Exploring binary images





Secure World

- sboot.bin
- Fernand Lone Sang <u>Reverse Engineering Samsung S6 SBOOT</u>
- Alexander Tarasikov Reverse-engineering Samsung Exynos 9820 bootloader and TZ







- Based on ARM Trusted Firmware (now Trusted Firmware-A)
- Secondary bootloader AP_BL2
- EL3 Monitor AP_BL31
- Secure EL-1 Payload AP_BL32
- U-boot AP_BL33

+sboot.bin+
+> Secondary Bootloader
EL3 Monitor
+-+ Secure EL-1 Payload
Non-secure Payload
++
+>

Name	ne Start End	
AP_BL2	0000000000000000	0000000000002000
AP_BL31_IMG	0000000000002000	000000000002A000
AP_BL31_unpacker	000000000002A000	00000000005A000
AP_BL33	00000000005A000	0000000000143000
AP BL32	0000000000143000	00000000001C3110





Contains most parts of TEE

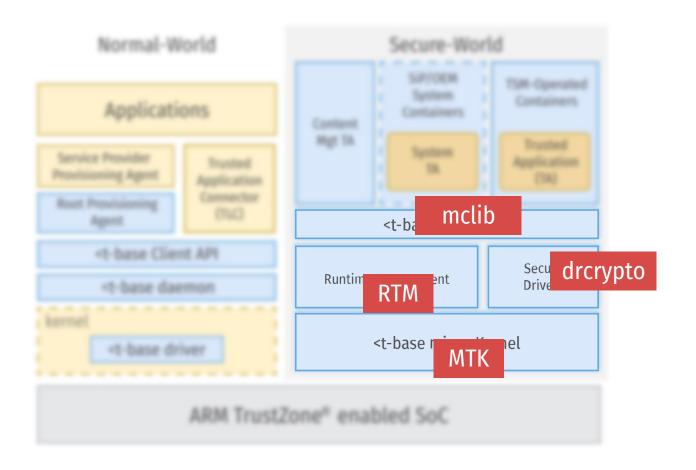
Digital Security

Na	me	Start	End
40	MTK_code	07F00000	07F08AB8
46	MTK_data	07F08AB8	07F0C000
46	IMG_HDR	07F0C000	07F0D000
46	MCLIB	07F0D000	07F24000
46	RTM	07F24000	07F36000
45	DRCRYPTO	07F36000	07F49000
46	TLPROXY	07F49000	07F4A000
45	STH2	07F4A000	07F54000
4	MCTL	07F54000	07F56000



Secure EL-1 Payload

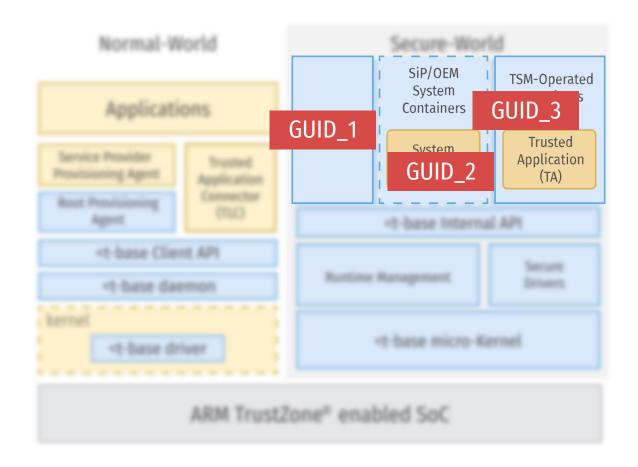
- Kinibi kernel MTK
- Runtime manager RTM
- Some trusted drivers drcrypto, ...
- Some trusted applications STH2, ...
- Internal API library mclib





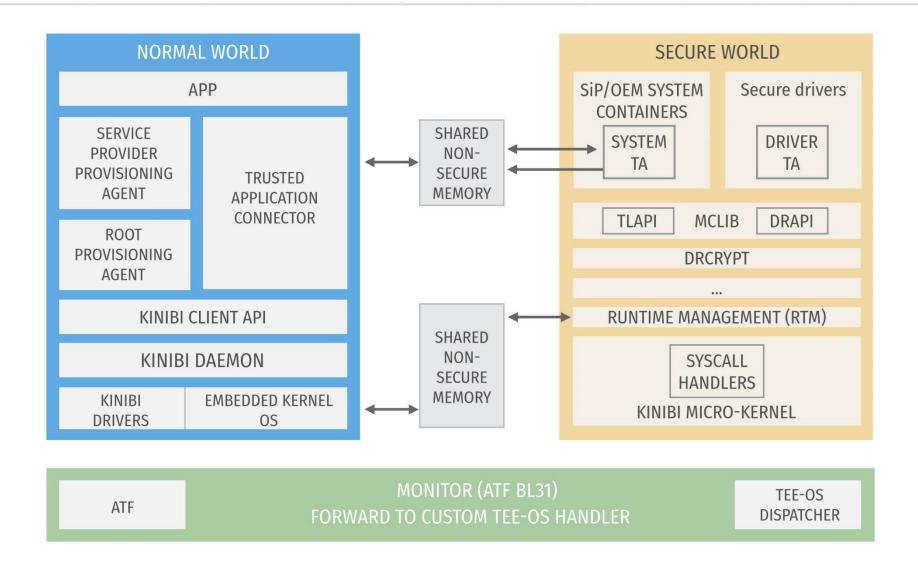
Trustlets

- Trusted applications TA, CM system TA, SP TAs
- Reside in Android file system
- Identified by GUID



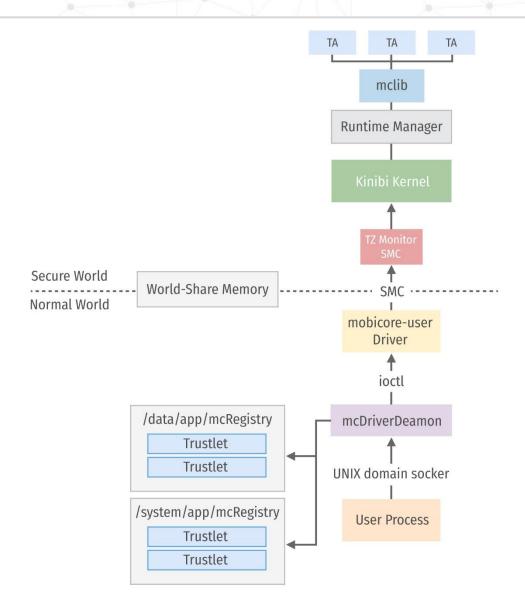


Inter-world communication





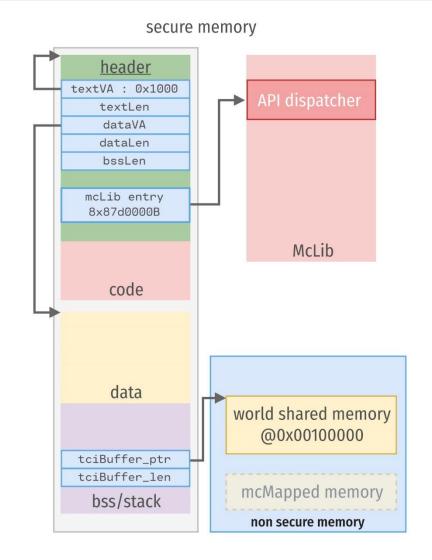
Inter-world communication flow





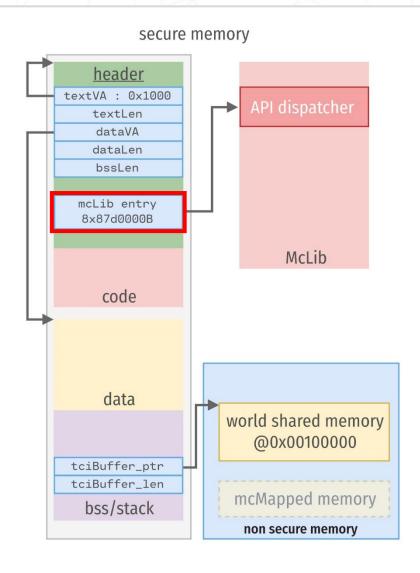
Trustlets

- MobiCore Load Format MCLF
- github: mcLoadFormat.h
 - IDA Pro loader
 - Ghidra loader
- Signed binaries
- 32-bit executables
- Uninitialized fields
 - tciBuffer_ptr
 - tciBuffer_len
 - mcLibEntry
 - ..
- Internal API via mclib









- All external calls are through mclib entry field in MCLF header
- Easy to emulate such an isolated code
- Easy to wrap in fuzzing environment



Fuzz smartly

AFL





- Straightforward approach
 - Fuzz trustlets from Normal World
 - Non-controlled environment
 - No coverage control
 - No crash information
- Smart approach
 - Controlled environment
 - Control fuzzing coverage
 - All crash information
 - Explore crashes with all tools

AFL



- AFL fuzzes applications
 - source code afl-gcc
 - binary code afl-unicorn
 - executables qemu usermode
- AFL mutates standard input (--) or file input (@@)
- Use AFL qemu usermode
 - Convert MCLF trustlet to ELF executable
 - Make a wrapper to forward standard input to the trustlet TCI
 - Fuzz it with qemu mode!



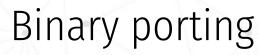
Binary porting

Transform a trustlet to a linux application

- Make an initial stub to forward input
- Make an ELF with initial stub and trustlet
- Relocate trustlet image properly
- Transfer execution to the trustlet entry point
- Mock mclib
- Automate it for all trustlets



- Make an initial stub code
- Define symbols
 - tciBuffer_ptr
 - tciBuffer_len
 - tlMain







- Compile our stub
 - gcc -c tlrun.c -o tlrun.o
- Define symbols
 - objcopy --add-symbol tlMain=\$(TLMAIN)
- Adding sections
 - objcopy --add-section .tlbin_text=.text.bin \
 --set-section-flags .tlbin_text=code,contents,alloc,load \
 tlrun.o tlrun.o.1
- Locating sections
 - gcc tlrun.o.1 --section-start=.tlbin_text=0x1000 -o tlrun



Implement mclib API



- TlApi.h
- TlApiCom.h
- TlApiCommon.h
- TlApiCrypto.h
- TlApiError.h
- TlApiHeap.h
- TlApiLogging.h
- TlApiMcSystem.h
- TlApiSecurity.h
- TlApiTime.h
- TlApiTplay.h
- TlApiTui.h

```
_TLAPI_EXTERN_C tlApiResult_t tlApiUnwrapObjectExt(
    void *src,
    size_t srcLen,
    void *dest,
    size_t *destLen,
    uint32_t flags );

_TLAPI_EXTERN_C void tlApiLogPrintf(
    const char *fmt,
    ...);
```



- Dispatch function
 - tlApiLibEntry

```
// tlrun.c
typedef void (*tlApiEntry t)(int num);
void (*tlApiLibEntry)(int num) __attribute__((weak));
void tlApiEntry(int num) __attribute__((noplt));
 _attribute__((constructor)) void init()
 tlApiLibEntry = tlApiEntry;
                                      // tllib.c
                                      void* get api(int num)
                                        return ptrs[num];
```

```
// entry.S
.syntax unified
.arch armv7a
.globl tlApiEntry
tlApiEntry:
                 {r0-r4,lr}
        push
                 get_api
        bl
                 r12, r0
        mov
                 {r0}
        pop
                 \{r0-r3,lr\}
        pop
        bx
                 r12
```



Binary porting

Automation for multiple TAs

- Trustlet porting parameters
 - Entry point
 - Sections locations
 - TCI buffer length
- Old good Makefiles

- Trustlet entry point
 - objcopy --add-symbol tlMain=\$(TLMAIN)
- Sections locations
 - gcc tlrun.o.1 --sectionstart=.tlbin_data=\$(TLDATA) -o tlrun
- TCI buffer length
 - gcc -DTCILEN=\$(TLTCI_LEN) -c tlrun.c -o tlrun.o



Automation for multiple TAs



- IDA Pro
 - batch mode
 - Idascript
- Ghidra
 - Headless mode

```
rem ida_auto.bat
                     for /r %%f in (*.idb) do (
                          idascript %%f %TOOLDIR%\tlinfo.py
# tlinfo.py
def info segments():
    ss = dict()
    for s in Segments():
         name = idc.get_segm_name(s)
         segs.update({name: [s, idc.get_segm_end(s)]})
    return segs
if __name__ == "__main__":
    try:
         kinibi_api.main()
         print "TLMAIN := 0x%x" % (locate_tlmain() + 1)
         ss = info_segments()
         env_names = {".text": "TLTEXT",
              ".data": "TLDATA",
              ".bss": "TLBSS"}
```



Launch

~ # ./tlrun < test

```
root@artik:~/targets/07010000000000000000000000000000000# ./tlrun < test
mem1 = 0x77e110
tciBuffer = 0x77e008, tciBufferLen = 40
Jump to tlMain
TlCm: Starting, 3.6, Mar 9 2015, 17:57:42.
--- tlApiGetVersion ---
--- tlApiGetSuid ---
TlCm: Waiting.
 --- tlApiWaitNotification ---
TlCm: Begin MC_CMP_CMD_BEGIN_SOC_AUTHENTICATION.
--- tlApiGetVirtMemType ---
addr = 0x77e110
TlCm: End MC_CMP_CMD_BEGIN_SOC_AUTHENTICATION.
--- tlApiNotify ---
```



Fuzzing

Poexali!

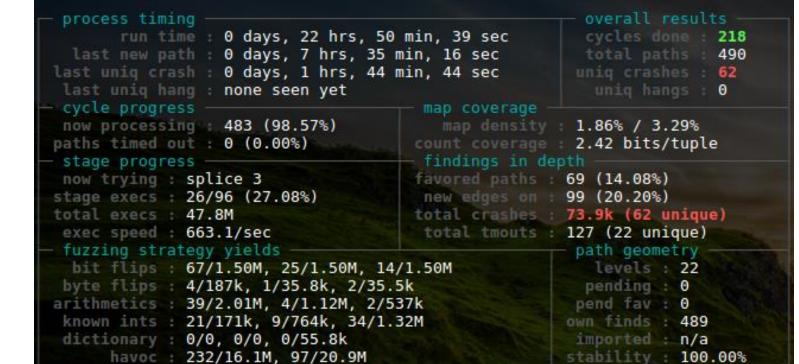




- QEMU and AFL QEMU patches issues
 - toolchain
- AFL instrumentation issues
 - Study AFL thoroughly







trim: 41.48%/57.5k, 80.27%

american fuzzy lop 2.52b (tlrun)



[cpu000: 24%]







23 trustlets – 477 crashes

afl-cmin – 225 unique cases

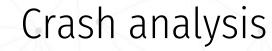


Crash analysis





- Get to ARM machine
- Dynamic analysis
 - Gdb scripts
- Dynamic Binary Instrumentation
 - DynamoRIO
 - Valgrind
- Symbolic execution
 - angr





- gdb crash analyzer
 - poor information
- DynamoRIO
 - cannot load so specifically constructed file
- Valgrind
 - callgrind
 - memcheck
 - not for automatic parsing
- angr
 - error-prone, time-consuming

gdb is the only friend



Crash analysis

gdb scripts

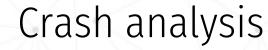
analyze.sh

- Make more logging from our mclib
- Build SQLite database

```
# stub.gdb

set logging on
set logging redirect on
target remote :5555
source catch.py
continue
```

```
for f in $(ls $1/out/crashes)
do
    echo === $f === | tee -a gdb.txt
    ../afl-qemu-trace -L /usr/arm-linux-gnueabi/ -g 5555 $1/tlrun < $1/out/crashes/$f 1>/dev/null 2>/dev/null &
    arm-none-eabi-gdb -x stub.gdb -batch 2>/dev/null
    tail -n 2 gdb.txt
    ../afl-qemu-trace -L /usr/arm-linux-gnueabi/ $1/tlrun < $1/out/crashes/$f > /tmp/1.qemu
done
```





- Non-trivial functions
 - tlApiSecSPICmd
 - tlApi_callDriver
 - tlApiWrapObjectExt
 - tlApiUnWrapObjectExt
 - •

- Exclude such cases
- Implement and get more accurate fuzzing results



Crash analysis

~ # sqlite3 analyze-cmin.db 'select * from main' | grep -v tlApiSecSPICmd

```
ffffffff000000000000000000000000e|000055|SIGILL|0|tlApiDeriveKey;tlApiWaitNotification;tlApiGetVirtMemType;tlApiGetVirtMemType;tlApiMalloc;tlApiMalloc|0|
ffffffff000000000000000000000000e|000057|SIGILL|0|tlApiDeriveKey;tlApiWaitNotification;tlApiGetVirtMemType;tlApiGetVirtMemType;tlApiMalloc;tlApiMalloc|0|
ffffffff00000000000000000000000e|000059|SIGSEGV|271744|tlApiDeriveKey;tlApiWaitNotification;tlApiGetVirtMemType;tlApiGetVirtMemType;tlApiMalloc;tlApiMalloc|0|
ffffffff000000000000000000000012 | 000001 | SIGSEGV | 456116 | tlApiWaitNotification | 1 |
ffffffff0000000000000000000000012|000002|SIGSEGV|456116|tlApiWaitNotification|1|
ffffffff000000000000000000000012|000003|SIGSEGV|456116|tlApiWaitNotification|1|
ffffffff000000000000000000000012|000006|SIGSEGV|455744|tlApiWaitNotification|1|
ffffffff0000000000000000000000012|000007|SIGSEGV|455748|tlApiWaitNotification|1|
ffffffff000000000000000000000012|000008|SIGSEGV|456116|tlApiWaitNotification|1|
ffffffff000000000000000000000002f|000000|SIGSEGV|208724|tlApiRandomGenerateData;tlApiWaitNotification;tlApiUnwrapObjectExt|1|
ffffffff0000000000000000000000038|000000|SIGILL|0|tlApiWaitNotification;tlApiSecSPICmd;tlApiMalloc;tlApiSecSPICmd|1|
ffffffff0000000000000000000000038|000001|SIGILL|0|tlApiWaitNotification;tlApiSecSPICmd;tlApiMalloc;tlApiSecSPICmd|1|
ffffffff0000000000000000000000038|000002|SIGILL|0|tlApiWaitNotification;tlApiSecSPICmd;tlApiMalloc;tlApiSecSPICmd|1|
ffffffff000000000000000000000038|000006|SIGSEGV|81498|tlApiWaitNotification|1|
ffffffff000000000000000000000038|000007|SIGSEGV|443988|tlApiWaitNotification|1|
ffffffff0000000000000000000000008|SIGSEGV|443988|tlApiWaitNotification|1|
ffffffff000000000000000000000000000009|SIGSEGV|443988|tlApiWaitNotification|1|
ffffffff000000000000000000000038|000011|SIGSEGV|443620|tlApiWaitNotification|1|
ffffffff000000000000000000000038|000012|SIGSEGV|443624|tlApiWaitNotification|1|
ffffffff0000000000000000000000038|000013|SIGILL|0|tlApiWaitNotification;tlApiSecSPICmd;tlApiMalloc;tlApiSecSPICmd|1|
ffffffff000000000000000000000038|000014|SIGSEGV|443624|tlApiWaitNotification|1|
```



- https://security.samsungmobile.com/securityUpdate.smsb
 - SVE-2019-13958
 - SVE-2019-14126

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- Gruskovnjak Jordan: SVE-2019-13921
- Slava Makkaveev of Check Point: SVE-2019-13949, SVE-2019-13950, SVE-2019-13952
- Zero Day Initiative: SVE-2019-14008
- Julian Jackson: SVE-2019-14031
- Artyom Skrobov of Check Point: SVE-2019-14073

SMR-MAY-2019



Samsung Mobile is releasing a maintenance release for major flagship models as part of monthly Security Maintenance Release (SMR) process. This SMR package includes patches from Google and Samsung.



SVE-2019-14126

Heap overflow in keymaster trusted application



- Parsing DER-encoded ASN.1
- malloc size 1 little endian
- memcpy size 2 big endian

TCI buffer

```
....>,[¦.j..".яА
.....яА.....
00000000: 04 01 00 00 9B 2C 5B A6|10 BC 0A 00 22 00 FF C0
00000020: 00 00 00 03 83 00 00 77 10 AC 0A 00 00 00 00 00
00000030: 00 00 00 6C 6C
                          ...111111111111111
11111111111111111
11111111111111111
11111111111111111
11111111111111111
111111111111111111
```



Security mitigations

- Trusted applications
 - Per TA virtual memory
 - Unable to access kernel or physical memory
 - Divided into sections with different memory attributes
 - TCI buffers are non-executable
 - No ASLR
 - only in future plans (Adding ASLR to a microkernel-based operating system)

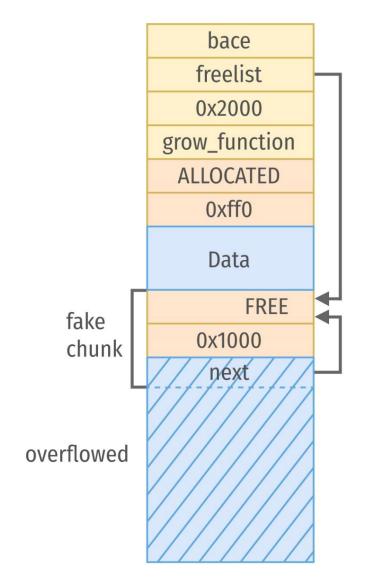


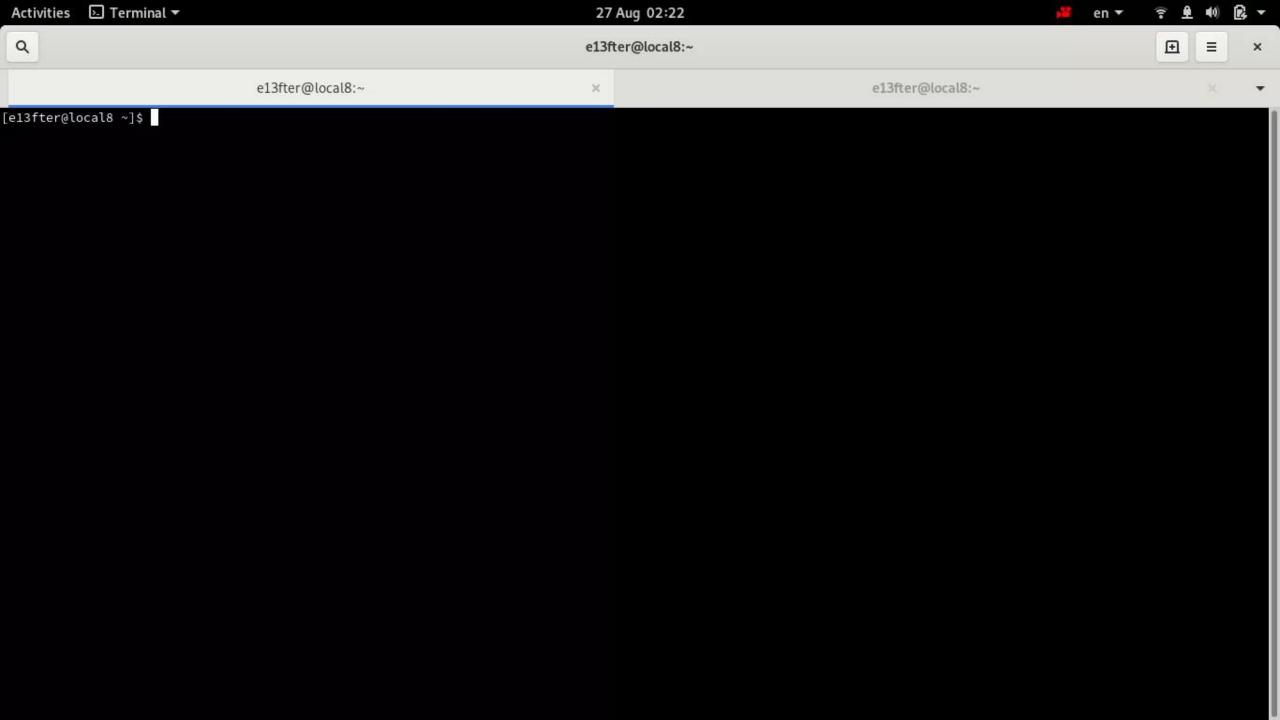
Exploitation overview

- Strategy
 - 1. Find a function pointer in .bss;
 - 2. Relocate a heap chunk before the pointer;
 - 3. Trigger memory allocation and copying at this chunk to overwrite the pointer;
 - 4. Call overwritten pointer.
- Heap exploitation in Kinibi
 - Eloi Sanfelix TEE Exploitation



- Brute force
 - In heap
 - create a fake chunk, pointing to .bss
 - In .bss
 - create one more fake chunk, pointing to itself
 - next allocations loop infinitely?
 - Yes suitable address
 - No, the trustlet crashed the relocation failed







- What we have
 - Calling an arbitrary executable code
 - No chances to execute a shellcode
 - Code-reuse is possible
 - Canaries in the stack



JOP (Jump Oriented Programming)

ROP gadget

LDR	R2,	[R1]
STRB.W	R0,	[R2],#1
STR	R2,	[R1]
BX	LR	

ROP gadget

MOV	RØ, R4
POP	{R3-R7,PC}

JOP gadget

ADDS	R7,	R7,	#1
ORR.W	R4,	R4,	#0x200
BLX	R1		





- ROPgadget --binary tlrun --thumb --range 0x1000-0xbeb44
- grep -E "; b.+ r[0-9]+\$"

```
0x000b1a984 : subs r1, #0x2d : movs r0, #0x3d : ldr r7, [r6, #0x1d] : ldr r4, [r6, #4] : bx r4
0x000b1sc : subs r1, #0x2d : movs r0, #0x3d : ldr r5, [pc, #0x120] : muls r1, r0, r1 : bx r4
0x000974e8 : subs r1, r0, #1 : ldr r0, [sp, #0x2c] : blx r2
0x000974e8 : subs r1, r0, #1 : ldr r0, [sp, #0x2c] : blx r2 : b #0x97500 : adds r6, r6, #1 : ldr r1, [r4, #0x18] : ldr r0, [sp, #0x34] : blx r1
0x000974e8 : subs r1, r0, #1 : ldr r0, [sp, #0x2c] : blx r2 : b #0x97504 : adds r6, r6, #1 : ldr r1, [r4, #0x18] : ldr r0, [sp, #0x34] : blx r1
0x000954de : subs r1, r0, r4 : ldr r0, [sp, #0x2c] : blx r2 : b #0x97504 : adds r6, r6, #1 : ldr r1, [r4, #0x18] : ldr r0, [sp, #0x34] : blx r1 : ldr r1, [r4, #0x00]
0x000056b2 : subs r2, #0x20 : ldrh r0, [r4, r4] : strb r5, [r4, #0x14] : movs r0, #0x50d : strb r5, [r4, #0x10] : adds r6, r6, #1 : blx r1
0x0001b732 : subs r2, r0, r4 : subs r7, r7, #1 : ldr r1, [r4, #0x18] : ldr r0, [sp, #0x10] : adds r6, r6, #1 : blx r1
0x0001b732 : subs r2, r0, r1 : movs r3, #0 : mov r0, sb : ldr.w r4, [r8, #0x1c] : blx r4
0x0001b6132 : subs r2, r7, r1 : movs r3, #0 : mov r0, sb : ldr.w r4, [r8, #0x1c] : blx r4
0x0001b615 : subs r4, #0x3a : ldr r1, [r5, #0x64] : str r6, [r6, #0x14] : ldr r4, [r5, #0x64] : subs r6, #0x64 : bx r0
0x0002151e : subs r4, #0x3a : ldr r1, [r5, #0x64] : str r6, [r6, #0x14] : ldr r4, [r5, #0x80] : ldr.w r3, [r2, #0x80] : mov r2, r1 : mov r1, r0 : movs r0, #0xb3 : bx r3
0x0009516e : subs r4, r0, r4 : mov r0, r4 : add.w r2, r4, r7, ls! *2 : str r1, [r2, #0x80] : ldr.w r3, [r2, #0x80] : blx r1
0x0009675c : subs r6, r6, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : subs r6, f0, #1 : ldr r0, [sp, #0x20] : blx r1
0x0009675c : sub
```



- JOP (Jump Oriented Programming)
 - Jump table in memory
 - One super gadget as a dispatcher

5.1.5 LDMIA and STMIA

Load and store multiple registers.

Syntax

op Rn!, {reglist}

where:

op is either:

LDMIA Load multiple, increment after STMIA Store multiple, increment after.

Rn is the register containing the base address. Rn mustbe in the range r0-r7.

reglist is a comma-separated list of low registers or low-register ranges.

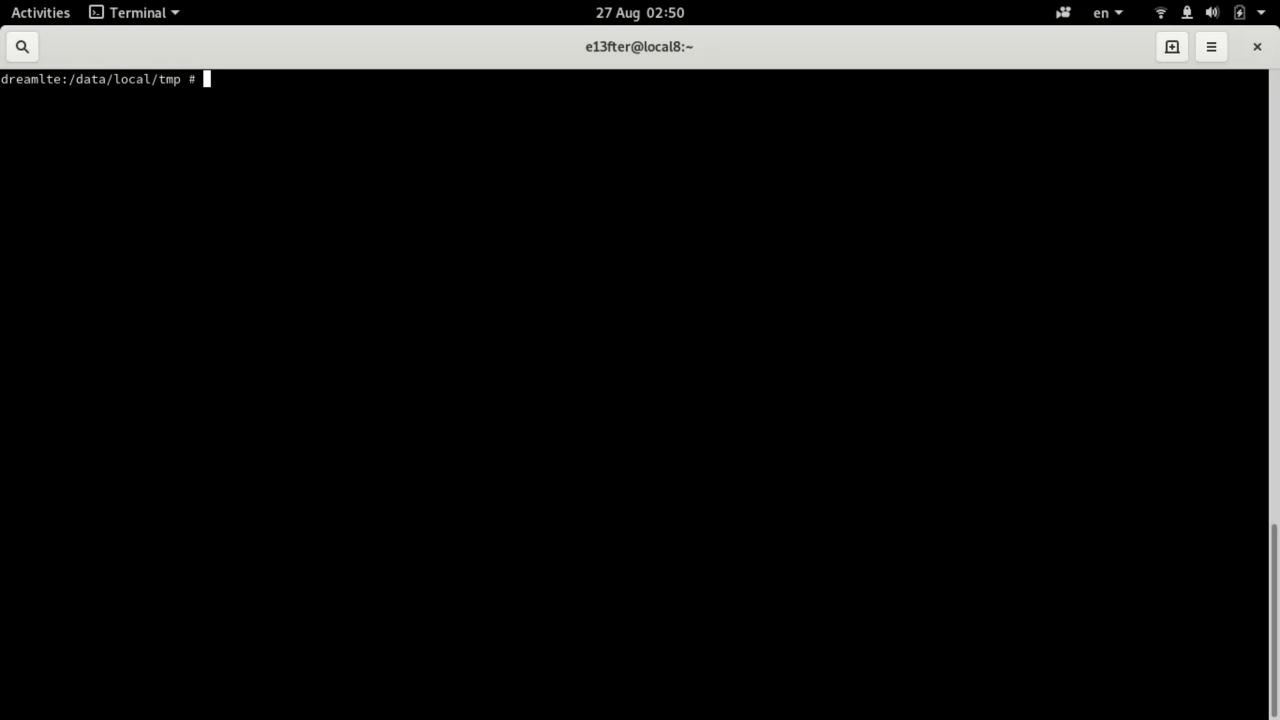




```
Digital
Security
```

- ROPgadget --binary tlrun --thumb --range 0x1000-0xbeb44
- grep -E "; b.+ r[0-9]+\$"
- grep -E "ldm.."

```
el3fter@mint-vm ~/afl/targets $ ROPgadget --binary tlrun --thumb --range 0x1000-0xbeb44 | grep -E "; b.+ r[0-9]+$" | grep "ldm.."
0x000a368c : add r1, sp, #0x340 ; str r1, [sp, #0x8c] ; ldrb r6, [r3, #0x18] ; add r5, sp, #0x40 ; strb r3, [r5, #7] ; cbz r3, #0xa3704 ; ldr r2, [sp,
{r2, r3, r4, r5, r7}; bx r7
0x000a3692 : add r5, sp, #0x40 ; strb r3, [r5, #7] ; cbz r3, #0xa36fe ; ldr r2, [sp, #0x264] ; it lo ; ldmlo r6!, {r2, r3, r4, r5, r7} ; bx r7
0x0009827c : adr r0, #0xec ; movs r6, #1 ; ldm r0, {r0, r1, r2} ; stm.w sp, {r0, r1, r2} ; ldr r1, [r5, #0x18] ; ldr r0, [sp, #0x18] ; adds r4, r4, #1
0x0009827a : b #0x98328 ; adr r0, #0xec ; movs r6, #1 ; ldm r0, {r0, r1, r2} ; stm.w sp, {r0, r1, r2} ; ldr r1, [r5, #0x18] ; ldr r0, [sp, #0x18] ; add
0x000a3696 : cbz r3, #0xa36fa ; ldr r2, [sp, #0x264] ; it lo ; ldmlo r6!, {r2, r3, r4, r5, r7} ; bx r7
0x000a369a : it lo ; ldmlo r6!, {r2, r3, r4, r5, r7} ; bx r7
0x00098280 : ldm r0, {r0, r1, r2} ; stm.w sp, {r0, r1, r2} ; ldr r1, [r5, #0x18] ; ldr r0, [sp, #0x18] ; adds r4, r4, #1 ; blx r1
0x000a369c : ldm r6!, {r2, r3, r4, r5, r7} ; bx r7
0x000a3698 : ldr r2, [sp, #0x264] ; it lo ; ldmlo r6!, {r2, r3, r4, r5, r7} ; bx r7
0x000a3690 : ldrb r6, [r3, #0x18] ; add r5, sp, #0x40 ; strb r3, [r5, #7] ; cbz r3, #0xa3700 ; ldr r2, [sp, #0x264] ; it lo ; ldmlo r6!, {r2, r3, r4, r
0x0009827e : movs r6, #1 ; ldm r0, {r0, r1, r2} ; stm.w sp, {r0, r1, r2} ; ldr r1, [r5, #0x18] ; ldr r0, [sp, #0x18] ; adds r4, r4, #1 ; blx r1
0x000a368e : str r1, [sp, #0x8c] ; ldrb r6, [r3, #0x18] ; add r5, sp, #0x40 ; strb r3, [r5, #7] ; cbz r3, #0xa3702 ; ldr r2, [sp, #0x264] ; it lo ; ldm
 ; bx r7
0x000a3694 : strb r3, [r5, #7] ; cbz r3, #0xa36fc ; ldr r2, [sp, #0x264] ; it lo ; ldmlo r6!, {r2, r3, r4, r5, r7} ; bx r7
```





Breaking keymaster

- Demo
- Break Android FDE through keymaster
 - Extracting Qualcomm's KeyMaster Keys Breaking Android Full Disk Encryption
- Post-Exploitation
 - Escalate to Trusted Drivers
 - Escalate to TEE kernel
 - Escalate to EL3 Monitor
 - Do anything you want





- Porting a binary to get all available toolset
 - Easy
 - Portable
- Fuzzing with AFL qemu mode
 - Fast
 - Reliable
- Exploiting vulnerabilities in Kinibi trustlets
 - No ASLR
 - A starting point for pwning TrustZone
 - One more way to pwn Android kernel





- Reverse Engineering Samsung S6 SBOOT
- Unbox Your Phone
- Trust Issues: Exploiting TrustZone TEEs
- <u>TEE Exploitation: Exploiting Trusted Apps on Samsung's TEE</u> at Zer0con 2019
- BREAKING SAMSUNG'S ARM TRUSTZONE at BlackHat USA 2019
- The road to Qualcomm TrustZone apps fuzzing at Recon Montreal 2019
- Reverse-engineering Samsung Exynos 9820 bootloader and TZ



Thanks for your attention!

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