


vMBPortTimersEnable



```
graph LR; A[vMBPortTimersEnable] --> B[Chip_TIMER_Reset]
```

A diagram showing a flow from a gray box labeled 'vMBPortTimersEnable' to a white box labeled 'Chip_TIMER_Reset'. A blue arrow points from the right side of the gray box to the left side of the white box.

Chip_TIMER_Reset