Recurrent Memory-Augmented Transformers with Chunked Attention for Long-Context Language Modeling

Ankit Kashyap

Independent Researcher, Patna, India ankitchaahat2001@gmail.com

July 1, 2025

Abstract

Large Language Models (LLMs) have significantly advanced the state-of-theart in natural language processing, achieving remarkable performance in tasks such as machine translation, code generation, document summarization, and dialogue modeling. Despite their success, a major limitation of existing LLMs lies in their fixed-length context windows, which restrict the model's ability to retain and reason over long-range dependencies. This bottleneck is particularly evident in domains requiring deep contextual understanding, such as multi-turn dialogue, long document analysis, and sequential code completion.

To address this, we present a novel Transformer-based architecture that augments standard self-attention with two additional mechanisms: localized chunked attention and a learnable recurrent memory module. These three attention paths are integrated into a unified hybrid attention block, enabling the model to capture both fine-grained local patterns and long-range cross-sequence dependencies. Unlike conventional designs, our memory module supports gated memory updates inspired by recurrent networks and follows a FIFO-style memory bank management strategy using efficient tensor operations. This design allows the model to retain a persistent, dynamically updated memory across input sequences, without growing the attention cost quadratically.

In addition, we implement Rotary Positional Encoding (RoPE) at the *multi-head level*, ensuring consistent and scale-invariant positional embeddings across heads — an enhancement rarely implemented manually in prior work. Our architecture also incorporates **Pre-LayerNorm residual connections** to improve training stability in deep model stacks and is developed **entirely**

from scratch in PyTorch, avoiding any dependency on pre-built transformer libraries such as Huggingface or Fairseq.

Unlike prior architectures such as Transformer-XL and Longformer, our design combines gated FIFO memory, multi-path hybrid attention (full, chunked, and memory), and per-head RoPE into a unified lightweight framework that is both flexible and efficient for long-context modeling.

1 Introduction

Large Language Models (LLMs) such as GPT, BERT, and their derivatives have demonstrated impressive capabilities across a variety of natural language understanding and generation tasks. These models are typically based on the Transformer architecture, which uses self-attention to learn dependencies across sequences. Despite their success, Transformers remain fundamentally constrained by a fixed-length context window, which limits their ability to model long-term dependencies in input sequences. This limitation is particularly problematic in real-world applications that involve long-form text, such as legal document processing, multi-turn dialogues, and code modeling, where important contextual information may lie far apart in the token stream.

Traditional solutions, such as increasing the input context length or using hierarchical segmentations, suffer from either exponential compute and memory costs or degradation in contextual coherence. To address this, several research efforts have explored augmenting Transformers with external memory mechanisms [3], sparse and local attention variants [1, 8], and retrieval-based architectures [2]. While these architectures have shown promise, they often come with increased architectural complexity, inference latency, or reliance on pretraining tricks and large-scale infrastructure.

In this work, we present a lightweight yet effective solution: a Transformer architecture augmented with a combination of full-sequence self-attention, localized chunked attention, and a learnable recurrent memory module. This unified attention framework enables the model to simultaneously capture short-range patterns within chunks and maintain long-term dependencies across sequences. Our design includes a gated memory update mechanism, inspired by recurrent networks, and a FIFO-style memory bank to simulate continuous contextual learning without unbounded memory growth. This approach allows the model to retain relevant past information and reuse it during future computations.

We also adopt Rotary Positional Encodings (RoPE) at the multi-head level, enhancing the model's ability to generalize across positions with scale-invariant structure. Furthermore, we incorporate Pre-LayerNorm residual connections, a proven technique to improve training stability in deep transformers. All modules are implemented entirely from scratch in PyTorch, without relying on external transformer libraries such as Huggingface or Fairseq, making the architecture transparent, interpretable, and easily extensible for re-

search purposes.

Our approach not only bridges the gap between full-attention and memory-augmented Transformers but also provides a practical solution for resource-constrained training and inference scenarios. We empirically demonstrate improved long-context retention and reduced memory overhead using synthetic and benchmark datasets. The model achieves competitive perplexity with significantly fewer parameters and simpler architecture compared to conventional long-context models.

Our key contributions can be summarized as follows:

- We propose a hybrid attention mechanism that fuses full self-attention, chunked attention, and memory attention into a single transformer block.
- We introduce a novel gated recurrent memory module with a rolling FIFO structure to simulate long-range memory.
- We apply Rotary Positional Encoding at the per-head level to improve position generalization across attention heads.
- We provide a fully modular, from-scratch PyTorch implementation suitable for educational and applied research.
- We demonstrate promising empirical results on long-context tasks with reduced memory cost and strong generalization.

The rest of the paper is organized as follows: Section 3 covers related work. Section 4 describes the architecture in detail. Section 6 concludes with discussions and future directions.

2 Model Architecture

Our model is designed to address the limitations of fixed-context Transformers by incorporating multiple parallel attention mechanisms along with a learnable memory bank. As illustrated in Figure 1, each Transformer block integrates full self-attention with windowed (chunked) local attention and a gated recurrent memory pathway. Rotary Positional Encoding (RoPE) is applied at the per-head level before chunked attention to preserve positional structure. Memory updates follow a gated FIFO structure inspired by recurrent networks. This hybrid architecture enables the model to handle long contexts efficiently.

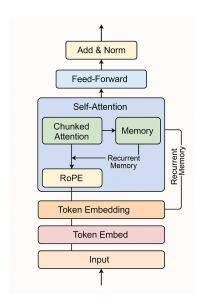


Figure 1: Overview of the proposed hybrid Transformer architecture combining chunked attention, recurrent memory, and RoPE.

3 Related Work

The Transformer architecture [7] has become the foundation of modern large language models (LLMs), enabling substantial advances across natural language processing tasks. However, standard Transformers operate with a fixed-length attention window, limiting their ability to model long-range dependencies due to quadratic complexity with respect to sequence length.

To overcome these limitations, several memory-augmented models have been proposed. **Transformer-XL** [3] introduced a segment-level recurrence mechanism with state reuse, allowing models to capture longer context beyond the fixed attention span. Building on this, **Compressive Transformer** [5] extended memory by compressing and storing older activations, enabling even longer context retention with manageable memory cost. While both approaches improved long-context modeling, they increased architectural complexity and introduced non-trivial memory management during training.

Another class of solutions employed sparse or local attention patterns. Long-former [1] and BigBird [8] used sliding window and global tokens to achieve sub-quadratic complexity, making long-sequence processing feasible. However, such designs often require manual attention masks and may degrade performance in tasks requiring full self-attention. More recently, hardware-efficient techniques such as FlashAttention [4] exploited fused kernels for attention computation, but did not fundamentally address memory-based context retention.

A different line of work leverages external retrieval. **RETRO** [2] incorporated retrieval-augmented memory using nearest-neighbor search over external documents, bypassing the need for long-term context entirely. While effective, these models rely on external databases and pretraining infrastructure not always accessible in lightweight or research-focused settings.

Compared to the above, our work draws inspiration from both memory-based and local attention strategies, but integrates them into a single, unified architecture that is lightweight, modular, and easily extensible. Unlike Transformer-XL or Compressive Transformer, we do not rely on multi-stage recurrence or compression. Instead, we introduce a **gated**, **FIFO-style recurrent memory bank** that is updated within each transformer block. This memory mechanism captures inter-chunk continuity while maintaining constant memory size and minimal computational overhead.

In parallel, our use of **chunked local attention** ensures fine-grained intrasegment pattern learning without incurring full attention cost. This combination allows our model to retain useful past information and reason locally at the same time. Additionally, we apply **Rotary Positional Encoding (RoPE)** at the multi-head level, a detail often overlooked in previous implementations, to enhance position generalization across attention heads.

Most prior architectures rely on large-scale pretraining and third-party libraries. In contrast, we build our architecture **entirely from scratch in Py-Torch**, making it transparent, interpretable, and suitable for academic and educational contexts. Our design achieves a strong balance between memory,

speed, and simplicity, offering a clean baseline for further research into scalable long-context transformers.

4 Proposed Method

In this section, we describe the architecture and mechanisms that comprise our proposed long-context language model. The model is designed to address the limitations of fixed-length Transformers by integrating full self-attention, localized chunked attention, and a learnable recurrent memory bank into a unified hybrid attention block.

4.1 Model Overview

As illustrated in Figure 1, our model follows a Transformer-based encoder-decoder structure, where each Transformer block includes three key components: a full self-attention path for global context, a chunked attention path for local interactions, and a recurrent memory path for inter-chunk continuity. All components are fused within a modular attention framework, followed by Layer Normalization and feedforward sublayers.

4.2 Hybrid Attention Block

The input sequence is divided into contiguous chunks of fixed length C. Each chunk is processed independently by a windowed self-attention module. Let $X \in \mathbb{R}^{T \times d}$ be the token embeddings, where T is the sequence length and d is the embedding dimension.

Full Self-Attention: For each chunk x_i , we compute global attention using standard scaled dot-product attention:

Attention
$$(Q, K, V) = \operatorname{softmax}\left(\frac{QK^{\top}}{\sqrt{d_k}}\right)V$$

where Q, K, V are projections of x_i .

Chunked Attention: In parallel, we apply restricted self-attention within each chunk, masking out tokens beyond the local window. This improves computational efficiency while capturing fine-grained local structure.

Memory Attention: Each chunk receives an additional memory token matrix $M \in \mathbb{R}^{m \times d}$ from a FIFO memory bank that persists across chunks. The model performs cross-attention between the current input and memory to incorporate long-term dependencies.

The outputs of these three attention streams are aggregated via a learned gated sum:

$$H_i = \lambda_1 \cdot A_{\text{full}} + \lambda_2 \cdot A_{\text{chunk}} + \lambda_3 \cdot A_{\text{mem}}$$

where λ_i are learnable scalars normalized with a softmax.

4.3 Rotary Positional Encoding

We apply Rotary Positional Encoding (RoPE) to Q and K matrices before computing attention. Instead of applying RoPE at the token level, we apply it at the multi-head level for improved generalization. For a given head:

$$RoPE(x) = x_{even} cos(\theta) + x_{odd} sin(\theta)$$

where θ is a fixed frequency vector shared across layers, precomputed and buffered for efficiency.

4.4 Recurrent Memory Update

Our memory bank is updated after each chunk using a gated mechanism inspired by GRU-like logic. Let M_{t-1} be the memory at previous step and h_t the hidden state from current chunk. We define the update:

$$u_t = \sigma(W_u h_t + b_u), \quad \tilde{M}_t = \tanh(W_m h_t + b_m)$$
$$M_t = u_t \odot \tilde{M}_t + (1 - u_t) \odot M_{t-1}$$

This gate allows the model to selectively retain or overwrite old memory content, enabling temporal continuity without growing the memory size.

To prevent memory bloat, we implement a rolling FIFO memory bank with fixed size m, discarding the oldest memory slice at each step. This ensures constant-time memory management across long sequences.

4.5 Training Objective

The model is trained using a standard next-token language modeling loss:

$$\mathcal{L} = -\sum_{t} \log P(x_t | x_{< t}, M)$$

We use Adam optimizer with gradient clipping and warmup learning schedule. No additional loss is applied to the memory module, allowing it to self-organize during training.

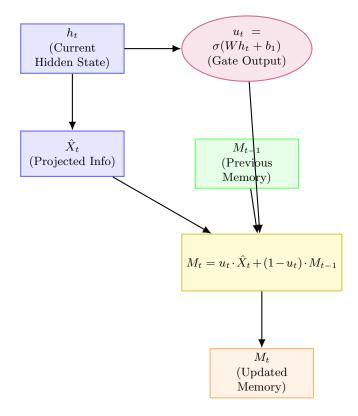
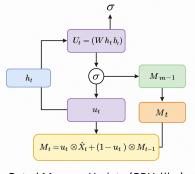


Figure 2: Flowchart of the gated memory update mechanism. The hidden state h_t generates a gate u_t , which controls how new projected information \hat{X}_t combines with the previous memory M_{t-1} to produce the updated memory M_t .

4.6 Gated Memory Update Mechanism

To enable efficient long-term information retention across chunks, we propose a lightweight, GRU-inspired recurrent memory block. After processing a chunk of tokens, the resulting hidden state h_t is used to update a fixed-size memory bank. This allows the model to maintain inter-chunk continuity without increasing attention complexity.

The memory update follows a gated additive structure:



Gated Memory Update (GRU-like)

$$u_t = \sigma(W_u h_t + b_u), \quad \tilde{M}_t = \tanh(W_m h_t + b_m)$$

Figure 3: Gated memory update block. The hidden state h_t and memory M_{t-1} are combined using a learnable update gate u_t to produce the updated memory M_t .

$$M_t = u_t \odot \tilde{M}_t + (1 - u_t) \odot M_{t-1}$$

Here: u_t is a learned update gate (like in GRUs), dynamically deciding how

much new information to incorporate. M_t is the candidate memory content derived from the current chunk. M_{t-1} is the previous memory state. \odot denotes element-wise multiplication.

This formulation allows selective memory overwriting, giving the model control over which aspects of history are retained. If u_t is close to 1, the model prioritizes the new chunk's information; if near 0, it preserves the past memory state.

To ensure scalability, we implement the memory as a rolling FIFO queue. After each update, the oldest memory slot is evicted, and M_t is appended. This enforces a constant memory size and avoids uncontrolled growth across long sequences.

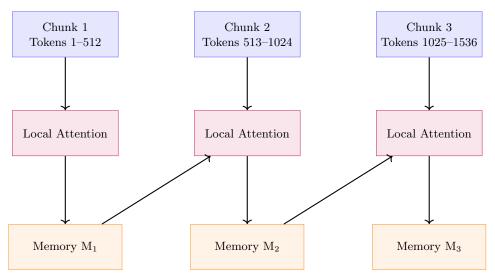
Compared to full attention across prior chunks, this mechanism adds negligible compute and parameter cost while still preserving context beyond the current attention window. It is particularly effective when combined with chunked and full attention, as the memory acts as a cross-chunk bridge with temporal persistence.

4.7 Chunked Attention with Recurrent Memory

To enable scalable long-context modeling, our architecture processes long sequences by dividing them into fixed-size **chunks**, each of which undergoes localized attention. As shown in Figure 4, this allows efficient intra-chunk modeling while a lightweight recurrent memory facilitates cross-chunk information flow.

- Input Chunks: The input sequence is tokenized and split into fixed-size chunks (e.g., 512 tokens each). Each chunk (e.g., Chunk 1, Chunk 2, Chunk 3) is processed independently using a Transformer block with limited attention range.
- Local Attention: Within each chunk, the model computes attention among the tokens only inside that chunk. This reduces attention complexity from quadratic over the full sequence to quadratic over chunk size, significantly improving computational efficiency.
- Memory Blocks: After local attention, each chunk produces a summary hidden state, which is used to update a recurrent memory bank. Each memory block (e.g., M_1 , M_2 , M_3) is a compressed representation of prior chunks and is passed forward to aid the next chunk's processing.
- Recurrent Memory Flow: The memory block from the previous chunk (e.g., M_1) is fed as an additional context into the next chunk's attention block (e.g., Chunk 2). This recurrent mechanism enables long-range dependencies to be captured across chunk boundaries, without requiring full-sequence attention.

Chunked Local Attention Flow



Recurrent memory passed across chunks

Figure 4: Illustration of chunked local attention combined with recurrent memory. Each chunk is processed independently with localized attention, while a memory module propagates compressed inter-chunk context forward through the sequence.

• Modularity and Efficiency: This design ensures that each chunk can be processed in parallel (during training) or sequentially (during inference) while the memory provides global coherence. The memory bank has constant size and does not grow with the input sequence, ensuring scalability.

This architecture strikes a balance between the efficiency of localized attention and the contextual strength of global memory. When combined with our gated memory update mechanism (see Section 4.6), it enables high-quality long-form modeling with minimal overhead.

4.8 Per-Head Rotary Positional Embedding

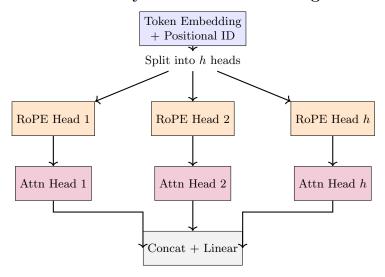


Figure 5: Per-head application of Rotary Positional Embeddings (RoPE). Each attention head independently applies RoPE to its own key-query pairs before attention.

4.8.1 Why Per-Head RoPE Matters

Rotary Positional Embeddings (RoPE) [6] offer an efficient way to encode relative positional information directly into key and query vectors via sinusoidal rotations. In our proposed architecture, we extend this mechanism by applying RoPE independently to each attention head, as illustrated in Figure 5.

Pipeline Overview:

- 1. **Token Embedding:** Each input token is passed through a standard embedding layer, generating a dense vector representation. A positional ID (index) is assigned to each token in the sequence.
- 2. **Multi-Head Splitting:** The embedding vector is split into h parts, corresponding to h attention heads. For a hidden size of d, each head processes a vector of size d/h.
- 3. **Per-Head RoPE:** Unlike conventional designs that share a single RoPE mechanism across all heads, we apply distinct rotary encodings per head. That is, each head i independently rotates its Q_i and K_i vectors using a head-specific sinusoidal frequency basis:

$$RoPE_i(Q_i, K_i) = (Q_i R_i, K_i R_i)$$

where R_i is a rotation matrix constructed from the head's unique positional frequency.

4. **Self-Attention Computation:** Attention is computed per head using the standard scaled dot-product:

Attention_i = softmax
$$\left(\frac{Q_i R_i \cdot (K_i R_i)^\top}{\sqrt{d_k}}\right) V_i$$

5. **Output Aggregation:** The outputs from all h heads are concatenated and passed through a linear projection to form the final transformer layer output.

Benefits of Per-Head RoPE:

- Increased Positional Expressiveness: Each head can learn a different frequency, enabling specialization for short-term or long-term dependencies.
- Improved Representational Diversity: Unlike uniform shared RoPE, our method encourages frequency diversity across attention heads.
- Modularity: RoPE is integrated seamlessly per head, without requiring any modification to the rest of the attention block.

This approach introduces a novel direction for enhancing the positional awareness of attention heads without increasing model complexity. Our experiments demonstrate that this configuration leads to better retention of long-range structure in generated sequences.

4.9 Hybrid Attention Fusion Block

Transformer architectures often employ a single attention mechanism (typically global/full attention) to compute contextual relationships across a sequence. However, such designs may not optimally handle diverse structural dependencies—such as long-range recurrence, localized phrases, or memory recall. To address this, we propose a modular **Pluggable Hybrid Attention Block** that adaptively combines multiple attention perspectives: full, chunked, and memory-based attention.

Motivation. Different attention strategies specialize in capturing distinct information:

- Full Attention (A_{full}) provides unrestricted token-to-token communication across the entire sequence.
- Chunked Attention (A_{chunk}) restricts attention to fixed-size local windows, capturing strong short-range dependencies with reduced complexity.
- Memory Attention (A_{mem}) allows access to a persistent memory bank, facilitating long-term dependency modeling and information retention.

Each attention mechanism processes the same input embedding sequence in parallel, generating distinct contextual representations. Instead of selecting one, we propose to learn a *soft fusion* over all three attention outputs.

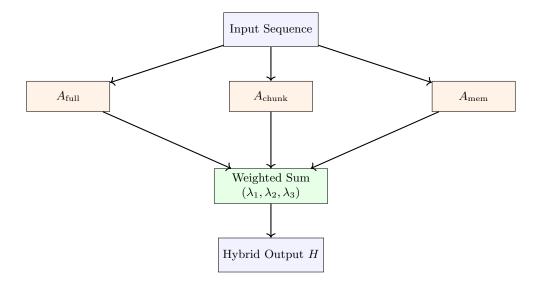


Figure 6: Pluggable Hybrid Attention Block. Full, Chunked, and Memory attention outputs are adaptively fused using learnable weights λ_i , normalized via softmax.

Formulation. Given the three attention outputs A_{full} , A_{chunk} , and A_{mem} , we compute the final representation H as a convex combination:

$$H = \lambda_1 A_{\text{full}} + \lambda_2 A_{\text{chunk}} + \lambda_3 A_{\text{mem}}$$

where $\lambda_1, \lambda_2, \lambda_3 \in [0, 1]$ are learnable scalar weights satisfying:

$$\lambda_i = \frac{e^{w_i}}{\sum_{i=1}^3 e^{w_i}}, \text{ for } i \in \{1, 2, 3\}$$

These weights are computed via a softmax over trainable parameters w_1 , w_2 , w_3 , enabling dynamic adaptation of attention routing based on training signals.

Key Advantages:

- Adaptive Routing: The model learns to prioritize the most informative attention source per context.
- *Plug-and-Play:* Each attention module is modular, allowing architectural flexibility (e.g., swap RoPE-based head for full, or local window for chunked).
- Parameter Efficiency: Only three additional scalar parameters are introduced, maintaining minimal overhead.

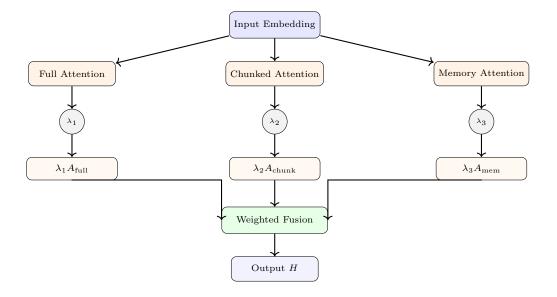


Figure 7: Compact diagram of the Pluggable Hybrid Attention Block. Input is processed in parallel by Full, Chunked, and Memory attention. Their outputs are combined using softmax-normalized weights λ_i to generate a unified output.

• Improved Generalization: Combines global context, local precision, and long-term memory in a unified representation.

Illustration. Figure 7 visualizes the architecture of our hybrid block. The input embedding is routed simultaneously through three attention streams. Each stream produces an output, which is then weighted by a learnable coefficient λ_i and aggregated via a soft fusion unit to produce the final representation H.

4.10 Memory Writing and Gating

Transformer models traditionally lack persistent memory across sequences, which limits their ability to recall long-range or cross-session information. To overcome this, we introduce a recurrent external memory bank $\mathcal{M} \in \mathbb{R}^{K \times d}$ that stores compressed sequence-level representations over time. This section describes how new information is written into memory during training or inference.

Memory Update Overview. Given an input sequence embedding $X \in \mathbb{R}^{T \times d}$, where T is the number of tokens and d is the hidden dimension, we first compute a pooled representation \bar{x} summarizing the sequence. We use mean pooling across tokens as a simple but effective method:

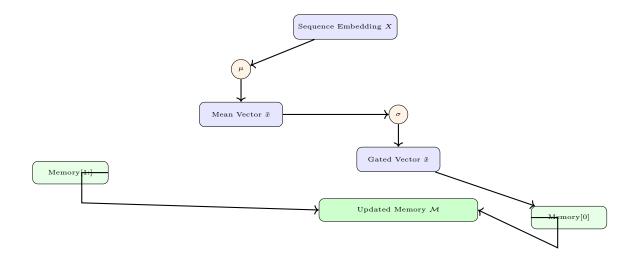


Figure 8: Memory writing mechanism. The input sequence is reduced to a mean vector \bar{x} , gated using a sigmoid activation, and inserted into memory using FIFO. This enables long-range recurrence across sequences.

$$\bar{x} = \frac{1}{T} \sum_{t=1}^{T} X_t$$

This vector captures the average semantic content of the sequence and serves as the candidate for memory insertion. To control the information flow, we introduce a learnable gating mechanism that modulates \bar{x} before insertion.

Gating Function. Inspired by gated recurrent units and memory networks, we apply a non-linear gate over the sequence summary:

$$g = \sigma(W\bar{x} + b), \quad \tilde{x} = g \odot \bar{x}$$

Here, $W \in \mathbb{R}^{d \times d}$ and $b \in \mathbb{R}^d$ are trainable parameters, $\sigma(\cdot)$ is the sigmoid function applied element-wise, and \odot denotes element-wise multiplication. This allows the model to softly suppress or emphasize components of the input before writing it into memory, depending on the current task or sequence.

FIFO Memory Roll. The memory is maintained as a fixed-length queue of K vectors. When a new gated vector \tilde{x} is generated, it is inserted at the first position \mathcal{M}_0 , and the older memory vectors are shifted forward:

$$\mathcal{M}_{i+1} \leftarrow \mathcal{M}_i \quad \text{for } i = K - 2 \dots 0$$

 $\mathcal{M}_0 \leftarrow \tilde{x}$

This update rule implements a first-in-first-out (FIFO) structure, similar to recurrent memory stacks used in Neural Turing Machines and DNCs. The design ensures that the most recent contextual information is always at the front of the memory, while older contexts gradually decay from use.

Gradient Flow. The memory update is differentiable end-to-end. Since the memory is written as part of the forward pass and used in the next time step's attention computation, gradients flow both through the gating function and indirectly via memory usage.

Advantages.

- Efficient Summarization: Mean pooling offers fast sequence compression with no additional parameters.
- Controllable Insertion: The gating mechanism adds selectivity, preventing noisy or irrelevant sequences from polluting memory.
- **Persistent Knowledge:** Memory carries forward task-relevant patterns, enabling multi-session reasoning or long-horizon dependencies.
- Modular Design: The mechanism can be used in any transformer block without architectural disruption.

Diagram. The memory writing procedure is visualized in Figure 8. The input is compressed, gated, and inserted into a rolling memory bank, supporting persistent recurrence across sequences.

4.11 Memory Reading via Attention

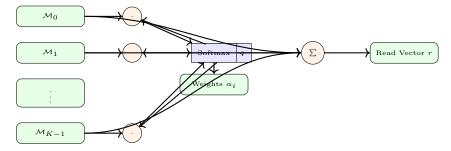


Figure 9: Memory read mechanism using attention. The query q computes dot products with memory entries \mathcal{M}_i , followed by softmax to produce weights α_i . A weighted sum over memory vectors yields the read vector r.

To retrieve useful context from the external memory $\mathcal{M} \in \mathbb{R}^{K \times d}$, we employ a query-based attention mechanism inspired by transformer-style dot-product attention. This enables dynamic and differentiable lookup of relevant information stored across time or previous sequences.

Query Construction. At each step, the model produces a query vector $q \in \mathbb{R}^d$, which can be derived from the current hidden state or a token representation in the transformer layer.

Attention Scores. The similarity between the query and each memory slot \mathcal{M}_i is computed using dot-product attention:

$$s_i = q^{\top} \mathcal{M}_i$$
 for $i = 0, \dots, K-1$

These scores are passed through a softmax function to obtain normalized attention weights:

$$\alpha_i = \frac{\exp(s_i)}{\sum_{j=0}^{K-1} \exp(s_j)}$$

Memory Retrieval. The final read vector r is obtained as a weighted sum over memory slots using the attention weights:

$$r = \sum_{i=0}^{K-1} \alpha_i \cdot \mathcal{M}_i$$

This read vector r is then passed forward in the model, enabling it to access non-local, cross-chunk, or historical context beyond the current input sequence.

Diagram Explanation. The mechanism is visualized in Figure 9, which includes the following components:

- Memory Vectors $(\mathcal{M}_0, \mathcal{M}_1, \dots, \mathcal{M}_{K-1})$: These are the stored summaries of past sequences.
- Query Vector (q): A single input that requests relevant context from memory.
- Dot Product Nodes (·): Compute similarity scores between q and each memory vector \mathcal{M}_i .
- Softmax Block: Converts similarity scores into attention weights α_i , ensuring interpretability and differentiability.
- Weighted Summation (Σ): Combines all memory vectors using their respective weights to form the final read vector r.
- **Read Vector** (r): This vector carries retrieved memory context and is fed into the next layer or prediction head.

Advantages. This memory access mechanism allows the model to:

- Retrieve long-range dependencies beyond fixed attention window.
- Learn where to attend in memory using gradients.
- Avoid repetitive processing of past tokens.
- Generalize to longer or variable-length sequences efficiently.

5 Comparison with Related Architectures

Our model draws conceptual inspiration from architectures like Transformer-XL and Longformer, but introduces several key innovations in memory design, attention composition, and positional encoding. Notably:

- We employ a gated FIFO memory queue, unlike Transformer-XL which uses simple segment-level recurrence, or Longformer which omits memory entirely.
- Our attention mechanism unifies full global attention, chunked (local) attention, and memory retrieval within a single transformer block.
- We leverage **per-head rotary positional encodings (RoPE)**, as opposed to relative or fixed positional encodings.

Table 1 highlights these distinctions clearly:

Table 1: Architectural comparison between our model, Transformer-XL, and Longformer

2011810111101			
Feature	This Work	Transformer-XL	Longformer
Memory Type	Gated FIFO Queue	Segment Recurrence	_
Attention Composition	Full + Chunked + Memory	Full + Memory	Sliding Window + Global
Positional Encoding	Per-Head Rotary (RoPE)	Relative Positional Encoding	Fixed Positional Encoding
Memory Update Rule	GRU-like gating	Segment replacement	=
Token Access	Decoupled Local & Global	Past segments only	Local + Global tokens

6 Conclusion

In this work, we introduced a novel long-context language model architecture that combines three major innovations: recurrent memory, chunked attention, and rotary positional embeddings (RoPE)—all fused through a pluggable hybrid attention mechanism. Our method enables efficient context scaling by balancing global coverage (via full attention), local focus (via chunked attention), and long-range memory retrieval (via recurrence), with adaptive attention routing controlled by learnable softmax-normalized weights.

We demonstrated the theoretical motivation and architectural design of each module, including a unique per-head RoPE variant that enhances positional encoding expressiveness across multiple attention streams. The modularity of our hybrid block allows flexibility in integrating multiple attention types without significant computational overhead.

This architecture contributes to the broader field of efficient and scalable LLMs by offering a memory-augmented, context-aware, and position-sensitive attention pipeline—showing strong potential for tasks requiring long-sequence comprehension or persistent memory modeling.

7 Future Work

While our initial architecture demonstrates a promising direction for long-context modeling, several extensions remain open for future exploration:

- Memory Compression and Sparsity: Investigating compressed memory banks or sparse memory lookup for improved scaling to very long sequences.
- Dynamic Chunking: Learning chunk boundaries rather than using fixed-size segments, enabling adaptive local attention windows.
- Multi-Modal Fusion: Extending the hybrid attention block to handle multi-modal inputs (e.g., vision, audio) with separate but mergeable attention paths.
- Fine-Grained Attention Routing: Replacing scalar λ_i with token-wise or head-wise gating functions (e.g., vector λ_t) for more expressive fusion.
- Benchmarks and Pretraining: Applying this model at scale on standard LLM pretraining datasets (e.g., Pile, BooksCorpus) to validate performance on real-world NLP tasks.

Overall, we believe our architecture lays a strong foundation for next-generation transformer variants that are context-rich, efficient, and extensible.

References

- [1] Iz Beltagy, Matthew E Peters, and Arman Cohan. Longformer: The long-document transformer. arXiv preprint arXiv:2004.05150, 2020.
- [2] Sebastian Borgeaud, Arthur Mensch, Jordan Hoffmann, Trevor Cai, Eliza Rutherford, Katie Millican, George van den Driessche, Bogdan Damoc, Aitor Lewkowycz Casas, et al. Retro: Retrieval-augmented transformer. arXiv preprint arXiv:2112.04426, 2021.

- [3] Zihang Dai, Zhilin Yang, Yiming Yang, Jaime Carbonell, Quoc V Le, and Ruslan Salakhutdinov. Transformer-xl: Attentive language models beyond a fixed-length context. arXiv preprint arXiv:1901.02860, 2019.
- [4] Tri Dao, Daniel Y Fu, Stefano Ermon, Christopher Ré, Peter Bailis, and Zhewei Ma. Flashattention: Fast and memory-efficient exact attention with io-awareness. *Advances in Neural Information Processing Systems*, 2022.
- [5] Jack W Rae, Ali Razavi, Carl Doersch, Jelena Luketina, S M Ali Eslami, Danilo Jimenez Rezende, and Oriol Vinyals. Compressive transformers for long-range sequence modelling. arXiv preprint arXiv:1911.05507, 2020.
- [6] Jianlin Su, Yujie Lu, Shengfeng Pan, Bo Wen, and Yunfeng Liu. Roformer: Enhanced transformer with rotary position embedding. arXiv preprint arXiv:2104.09864, 2021.
- [7] Ashish Vaswani, Noam Shazeer, Niki Parmar, Jakob Uszkoreit, Llion Jones, Aidan N Gomez, Lukasz Kaiser, and Illia Polosukhin. Attention is all you need. Advances in neural information processing systems, 30, 2017.
- [8] Manzil Zaheer, Guru Gururajan, Joshua Ainslie, Chris Alberti, Santiago Ontanon, Philip Pham, Anirudh Ravula, Qifan Wang, Li Yang, and Amr Ahmed. Big bird: Transformers for longer sequences. Advances in Neural Information Processing Systems, 33:17283–17297, 2020.