Lab8 Digital system lab

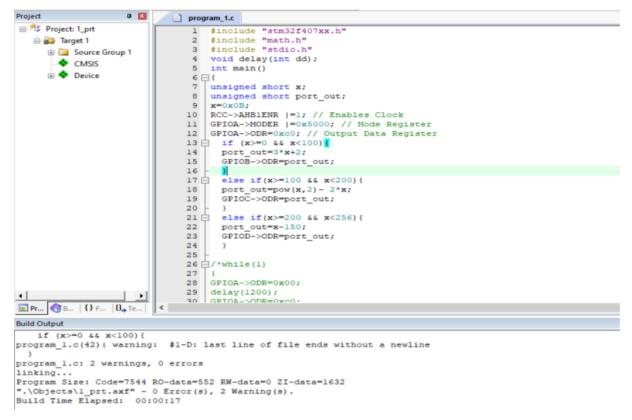
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Part1: Using GPIO

Task1: An unsigned 8-bit value is sensed/inputted at one of the MCU's ports and saved in a variable called x. Now, depending on the numerical value of x, it should be conditioned first before being transferred to ports as follows:

```
If 0 \le x < 100, Send 3x + 2 to port-B.
If 100 \le x < 200, Send x 2 - 2x to port-C.
If 200 \le x < 256, Send x - 150 to port-D.
```

Solution:



Explanation: As stated in the question, we declare x to be an unsigned integer, then use the if/else condition to apply certain conditions based on the value of x, and then send a specific output through each condition. This output is constructed by manipulating the value of x, so it should also be an unsigned integer. Then, for each condition, the appropriate output is transmitted to the port in accordance with the condition.

Part2: : Using SysTick Timer

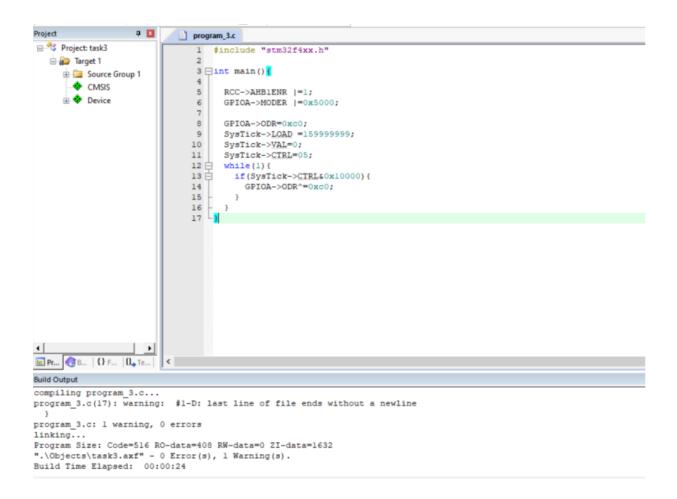
Task2: Write a program to provide a delay of 10 seconds using the system timer. Assume input clock frequency as 16MHz. (Hint: use a timer in a loop).

```
Project
                                                                                                                                                                                                                                            2_program.c

☐ Project: 2_prt
                             ⊟ 😜 Target 1
                                                                                                                                                                                                                                                                                                   4 🖂 (
                                                     ⊕ 🦲 Source Group 1
                                                                                                                                                                                                                                                                                                                                              RCC->AHB1ENR |= 1; // Enables Clock
                                                            CMSIS
                                                       ⊕ ◆ Device
                                                                                                                                                                                                                                                                                                                                          GPIOA->MODER |= 0x5000; // Mode Register
GPIOA->ODR = 0xc0; // Output Data Register
                                                                                                                                                                                                                                                                                                                                        SysTick->LOAD = 160000000; // Timer period of one clock cycle * Load value
                                                                                                                                                                                                                                                                                                                                          SysTick->VAL = 0;
SysTick->CTRL = 5;
                                                                                                                                                                                                                                                                                     11
                                                                                                                                                                                                                                                                                        13
                                                                                                                                                                                                                                                                                      14
                                                                                                                                                                                                                                                                                                                                              int flag = 0;
                                                                                                                                                                                                                                                                                                                                        while(1){
                                                                                                                                                                                                                                                                                                                                                             nale(1) {
   if(SysTick->CTRL & 0x10000) {
      // Generating 10 seconds delay
      if(flag == 10) {
            GPIOA->ODR ^= 0xc0;
            flag = 0;
            fl
                                                                                                                                                                                                                                                                                      16
                                                                                                                                                                                                                                                                                        19 🖨
                                                                                                                                                                                                                                                                                                                                                                           flag++;
}
                                                                                                                                                                                                                                                                                      23
                                                                                                                                                                                                                                                                                     25
                                                                                                                                                                                                                                                                                     26
                                                                                                                                                                                                                                                                                                                                              return 1;
     I Pr... | 1 Pr.
 Build Output
 Build target 'Target 1'
compiling 2_program.c...
2_program.c(28): warning: #111-D: statement is unreachable
                                    return 1;
return 1;
linking...
Program Size: Code=528 RO-data=408 RW-data=0 ZI-data=1632
".\Objects\2_prt.axf" - 0 Error(s), 1 Warning(s).
Build Time Elapsed: 00:00:16
```

Explanation: The frequency is set to 16 MHz, and we know that the SysTick Load register can only hold up to 24 bits, so we only save 16x10. Because load value 6 equates to a one-second delay, we loop roughly 10 times to achieve a ten-second delay.

Task 3: Write a program to toggle LED using System Timer (Sys_Tick). Assume that You need to toggle LEDs at PA6 and PA7.

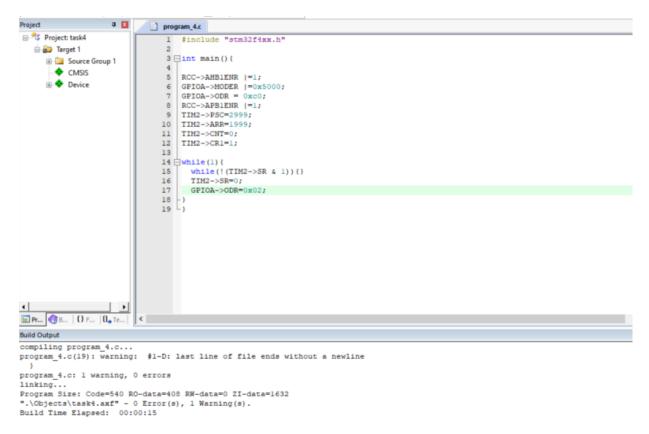


Explanation: Given that we must toggle the LED at PA6 and PA7, we must use port A and set the GPIO Moder and ODR values to set pin 6 and pin 7 to high. To toggle the LED, we utilise Binary XOR, and the load value in SysTick is adjusted to

match the delay we require. =delay*system clock is the SysTick Reload Value.

Part 3: Using General Timer:

Task 4: Generate a square wave of 05 KHz frequency using Timer 2. Obtain this square wave on PA1 (GPIO A, pin 1).



Explanation:

To generate the square wave we need prescaler and Reload value, for this let us consider the clock frequency to be 30,000 MHz,

Now, timer frequency: Timer frequency=clock frequency/(prescaler+1)*(ARR+1)

So, 5kHz=30,000 MHz/(prescaler+1)*(ARR+1)

Then, prescaler=2999, ARR=1999

Task 5: Generate a triangular wave of 50 KHz frequency using Timer 5. Obtain this triangular wave on PA7 (GPIO A, pin 7).

This triangle wave cannot be obtained with GPIO. We can, however, get square waves. This is because we need to alter the value from 0 to 1 for a triangular wave, but we can set the value to either 0 or 1 in the code.

To generate the triangle wave, we'll need different hardware. It could be possible to make it with an integrator machine that integrates the rectangular waveform.

Task 6: Configure ADC1 Channel IN 0 to read data at its respective GPIO pin

```
Project
                            program_6.c

☐ ♣ Project: task_6

                                     #include "stm32f4xx.h"
   □ Iarget 1
                                 3 ☐ int main() {
      Source Group 1
                                       RCC->AHB1ENR |=1;
       ...... CMSIS
                                       RCC->APB2ENR |=1;
      ⊕ ◆ Device
                                       ADC1->CR2 |=(1<<1);
                                       ADC1->CR2 |=(1<<10);
                                 8
                                       GPIOA->MODER |=(3<<0);
                                 9 \( \bar{\pi} \) while (1) {
                                 10
                                         ADC1->CR2 |=1;
                                         ADC1->SQR3 |=0;
                                11
                                 12
                                         ADC1->SR |=0;
                                13
                                         ADC1->CR2 |=(1<<30);
                                         while(!(ADC1->SR &(1<<1)));
                                14
                                15 - }
                                16
📠 Pr... | 😚 B... | { } F... | 🛈 → Te... |
Build Output
compiling program_6.c...
program_6.c(16): warning:
                            #1-D: last line of file ends without a newline
program_6.c: 1 warning, 0 errors
linking...
Program Size: Code=560 RO-data=408 RW-data=0 ZI-data=1632
".\Objects\task_6.axf" - 0 Error(s), 1 Warning(s).
Build Time Elapsed: 00:00:23
```

Explanation:

At first, we enabled the clock for GPIO and the clock for ADC1 was enabled.

Then, using line 6 of the code, we activate the continuous conversation mode, in which the ADC analogue input is continuously written to the ADC result data register, and finally, using line 7 of the code, we exit the conversation mode.

Then, for GPIO port-A, the MODER value is configured to use the analogue mode for the PA-0 pin.

The ADC-1 module is now activated inside the endless loop, and the normal sequence register and the status register are both set to 0.

This conversion is restarted with the loop's start, and the condition inside the loop is checked once more until the conversion is complete.