

Annette-Kolb-Anger 2, Ramersdorf-Perlach, 81737 Monchen, Germany

□ (+49) 017655155443 | 🗷 ankitkalbande@gmail.com | 🛅 posquit0 | 😘 ankitkalbande

"Be the change that you want to see in the world."

### Summary.

Current Embedded Design and Security Engineer (Team Lead) in a small company EFR (Europ⊠ische Funk-Rundsteuerung) GmbH. 2+ years experience specializing in Embedded Linux based application development and security. Super nerd who loves Vim, Linux and enjoys to play with Linux Kernal. Interested in devising a better problem-solving method for challenging tasks, and learning new technologies and tools if the need arises

### Education

#### TUM(Technical University of Munich)

MASTERS OF SCIENCE IN COMMUNICATION ENGINEERING

- Embedded System and Security
- Wireless Sensor Network
- · Network Planning

- Software Defined Networking
- System on Chip Design
- HW-SW Co-design System

- Oct. 2013 Oct. 2015
- VLSI Design
- Electronic Design Automation
- Broadband Communication

#### VNIT(Visvesvaraya National Institute of Technology)

BACHELOR OF TECHNOLOGY IN ELECTRONICS AND COMMUNICATION ENGINEERING

- Aug. 2009 Sept. 2013

Nagpur, India

Munich, Germany

- Digital and Analog Circuit Designing
- Embedded System

- Analog and Digital Communications
- Mobile Communication
- Network Planning
- · Real-time Operating System

# Work Experience \_\_

#### Europ⊠ische Funk-Rundsteuerung(EFR) GmbH

Munich, Germany

EMBEDDED DESIGN ENGINEER & SECURITY EXPERT (TEAM LEAD)

Jun. 2016 - PRESENT

- Designed BSI (Bundesamt f
   Mr Sicherheit in der Informationstechnik) complient secured software architecture for SMGW (Smart Meter Gateway).
- Implemented secured Linux boot workflow for Atmel's SAM5D35 MCU (ARM cortex A5).
- Integrated smartcard with SMGW and ported crypto related functionalities to smartcard.
- Developed RESTful API client to communicate with energy management server.
- Making the SMGW CC(Common Criteria) compliant (ISO/IEC 15408).
- Tools:-Openssl, Buildroot, Wireshark, Eclipse, CMake, Doxygen etc.
- Protocol:-TLS/SSL 1.2, HTTP, ISO7816, NTP, SOCKSV5 etc.
- Platform: Atmel's SAM5D35 (ARM cortex A5), Broadcom's BCM2837 (ARM Cortex A53).

#### **Department for Integrated Systems, TUM**

Munich, Germany Dec. 2015 - May. 2016

DIGITAL DESIGN AND RESEARCH ENGINEER

- Ported the digital design from Chipit to ProFPGA hardware.
- · Debugging the bugs in digital design of Network Adapter for InvasIC micro-architecture.
- Tools:-Synplify Pro, Chipitpro, Chipscope, Modelsim and grmon.
- Protocol:- AHB-AMBA, APB-AMBA.
- Platform:- Chipit (Xilinx Vertex-5), ProFPGA (Xilinx Vertex-7).

### Department for Real-Time Systems, TUM

**EMBEDDED DESIGN WERKSTUDENT** 

Munich, Germany

- Developed RSSI based local positioning system for BLE module NRF51822 using iBeacon.
- Integrated the developed module with existing modules on ARM cortex-M0+ of NRF51822.
- Tools:-GCC, Eclipse, Keil, NRF Studio, GDB etc.
- Protocol:- BLE 3.0 (Bluetooth Low Energy), iBeacon.
- Platform: NRF51822 (ARM Cortex M0), iBeacon.

Aug. 2015 - Oct. 2015

#### **Department for Integrated Systems, TUM**

zepartinent ior integrated by steins, i or

 Lab Tutor
 Apr. 2015 - Jul. 2015

- Lab tutor for HW-SW Co-design lab.
- Tools:- Vivado, ISE, Modelsim, GCC etc.
- Protocol:- Xillybus, AHB-AMBA, AXI-AMBA etc.
- Platform:-Xilinx Zynq-7000.

#### **Department for Integrated Systems, TUM**

DIGITAL DESIGN WERKSTUDENT

- Developed a new module for HW-SW Co-design lab and wrote the lab manual for the same.
- Developed a video processing accelerator on FPGA (Zynq-7000) and its software counterpart on ARM cortex-A9 processor.
- Tools:-Vivado, ISE, Modelsim, GCC etc.
- Protocol:-Xillybus, AHB-AMBA, AXI-AMBA etc.
- Platform :- Xilinx Zynq-7000.

#### Siemens (Corporate Research)

WIRELESS RESEARCH WERKSTUDENT

- Developed and implemented algorithm for motion sensing using RSSI values of Wi-Fi communication.
- Worked on distance measurement using time of arrival of Wi-Fi packets.
- Evaluated communication power of Atmel's Wi-Fi module.
- Tools: Matlab, Wireshark, Tshark, GCC etc.
- Protocol:-IEEE 802.11 n (WiFi).

## Skills\_

**Programming Skills** 

- C, C++, Assembly
- Shell, Perl, TCL Scripting
- MATLAB, Octave

- · VHDL, Verilog, SystemC
- · Android, JAVA
- Omnet++

Job-related Skills

- Embedded Linux Development
- Embedded Security
- Embedded System Architecture

- · Linux Kernel Module and Driver Development
- Network Architecture and Protocol Development

Munich, Germany

Munich, Germany

Nov. 2014 - Mar. 2015

Munich, Germany

Jan. 2014 - Jun. 2015

• Digital Designing and Verification

**Tools Skills** 

- · Buildroot, Yocto
- GCC (cross compiler), Make, CMake, Automake, Doxygen
- Wireshark, Tshark

- Openssl, MbedTLS, OpenPGP, OpenSC
- Eclipse, Netbean, Keil, Dave
- Vivado, ISE, Modelsim, Synplify Pro

**Language Skills** 

- English (High Proficiency)
- German (Beginner)

- Hindi (High Proficiency)
- Marathi (Mother Tongue)

# **Publications**

- Ankit Kalbande and Divyesh Dixit \( \text{\textit{M}}\) Guardian Protocol for Mobile Wireless Sensor Networks \( \text{\text{\text{International Conference on Electronics}}\), Communication and Computational Engineering (ICECCE 2014), Tamil Nadu, India.
- Ankit Kalbande, Divyesh Dixit and K.M Bhurchandi MRFID Based Health Assistance and Monitoring System through a Handmounted Embedded Device MIEEE 4th ICCCNT 2013, Tamil Nadu, India.
- Ankit Kalbande \( \text{MGuardian Protocol for Wireless Sensor Networks \( \text{M IEEE International Conference on Emerging Trends in Electronics, Communication and Networking 2012(Dec), SVNIT, Surat.
- Ankit Kalbande 🛮 Laser-based, Low cost and High Accuracy Local Positioning System (LPS) 🖾 ICECE(International Conference On Electronics and Communication Engineering) 2012, Chennai.
- Ankit Kalbande, Divyesh Dixit, Prof. Eswar Rao Moptimized, Low cost and Low Power Flash ADC Design National Conference for VLSI Design and Embedded System 2011, CEERI, Pilani.

NOVEMBER 9, 2017 CLAUD D. PARK · RÉSUMÉ