

Ankit Kataria

Web Developer - Security Enthusiast

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EDUCATION

INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE

BTECH IN ECE

Expected May 2020

CGPA: 9.10 as of II Year, Semester I

ST. MARY'S SCHOOL

Passed April 2016 | New Delhi, India

Percentage: 95.8

LINKS

Github:// [ankitkataria](#)

LinkedIn:// [ankitkataria28](#)

Gmail:// [ankitkataria28@gmail.com](#)

Portfolio:// [ankitkataria.co](#)

COURSEWORK

UNDERGRADUATE

Data Structures

ECN-102 | 10 GPA

Fundamentals of Object Oriented Programming

ECN-103 | 10 GPA

Digital Logic Design

ECN-104 | 10 GPA

Computer Architecture and Microprocessor

ECN-221 | 8 GPA

SKILLS

PROGRAMMING

Java • C++ • Python • Ruby

WEB

HTML • CSS • Javascript • NodeJS

PHP • MySQL • MongoDB

FRAMEWORKS

Laravel • Django • Jekyll • Express
SASS/SCSS

DEVELOPEMENT TOOLS

Linux • Sublime Text • Git • Vim

PROJECTS

CODEVILLAGE | UNDER SDSLABS, IITR

May 2017 – July 2017 | <https://codevillage.sdslabs.co>

- A competitive programming platform made in Lumen, a PHP Micro-Framework
- Worked on adding an admin panel to aid competition and problem handling

HOWL | UNDER SDSLABS, IITR

Nov 2017 – Dec 2017 | <https://muzi.sdslabs.co>

- A Laravel based API that powers Muzi, the in-campus music streaming web app.
- Migrated Laravel from 4.2 to 5.5 and upgraded Elastic Search to v5.6

A.V.A.D.H.I | MICROSOFT CODE.FUN.DO 2018

Feb 2018 – Present | <https://github.com/ydlamba/A.V.A.D.H.I>

- An Absence Verification and Detection Helping Instrument
- A complete Web, Mobile and IOT based solution aimed at solving the problem of false attendance at government offices
- Shortlisted among the Top 3 ideas in the online phase

STC OFFICIAL WEBSITE | UNDER STC AND SDSLABS

Feb 2018 – Present

- The Students' Technical Council(STC) is the official body representing all the technical groups present in the institute currently 11 in number
- Working on making their official website on Jekyll using HTML, SASS and JS

RISC PROCESSOR | COURSE PROJECT

Nov 2017 | <https://github.com/pandeykartikey/RISCY-Processor/>

- Implemented a 32-bit RISC processor in verilog using a self-designed ISA
- Worked on implementing the ALU component and completing the datapath

POSITIONS OF RESPONSIBILITY

SDSLABS | DEVELOPER

Jan 2017 – Present | <https://sdslabs.co/>

- Software Development Section(SDS)Labs is a student run group of software development and computer science enthusiasts.
- Work involves developing and maintaining web apps and managing servers

WONA | WEB DEVELOPER

Sept 2016 – Present | <http://watchout.iitr.ac.in/>

- Watch Out! News Agency (WONA) is the official campus magazine
- Work involves maintaining and regularly updating their website

ACHIEVEMENTS

2016 AIR 1061 Joint Entrance Examination

2016 AIR 1119 JEE Mains

2015 AIR 811 Kishore Vaigyanik Protsahan Yojana