

VLSI Programming Internship Task

Task Name : Design and Simulate a 4-Bit ALU Using Verilog/VHDL

This internship is designed to help you apply your skills in a real-world project. Each intern is required to complete only one final project task, which will serve as your main deliverable for the entire internship duration.

Each intern must upload their complete project code, screenshots, and related files to a GitHub repository.

Then, copy the GitHub link and submit it through the official submission form provided.

Ensure the repository is public and well-organized before the submission deadline.

You will receive the submission link through the official group, so keep checking regularly.

Make sure to submit your GitHub project link using that form before the deadline.

Your internship certificate will be issued on the final day of the internship.

All the best to everyone! 🌟

Thank you for being a part of Snestron Internships – we wish you a great learning journey ahead.

Let's build, learn, and grow together! 🖥️🚀