```
simulator lang=spectre
global 0 vdd!

V0 (net037 0) vsource dc=0 type=pwl wave=[ 0 0.0 50p 2 ]
C1 (net078 0) capacitor c=100a

I30 (vdd! net037 net078 _net0) CNT diameter=1e-9 angle=0
tins=10e-9 \
eins=16 tback=130e-9 eback=3.9 types=-1 L=115e-9 phisb=0.1 rs=0 \
rd=0 beta=16 Cc=7e-12 mob=1 Csubfit=0.4 Cp=40e-12

I31 (net078 net037 0 _net0) CNT diameter=1e-9 angle=0 tins=10e-9
eins=16 \
tback=130e-9 eback=3.9 types=1 L=115e-9 phisb=0.1 rs=0 rd=0 \
beta=16 Cc=7e-12 mob=1 Csubfit=0.4 Cp=40e-12

V1 (vdd! 0) vsource dc=2 type=dc
V2 (_net0 0) vsource dc=5 type=dc
```