

Computer System Security

Unit	Topic	Proposed Lecture
I	Computer System Security Introduction: Introduction, What is computer security and what to learn? , Sample Attacks, The Marketplace for vulnerabilities, Error 404 Hacking digital India part 1 chase. Hijacking & Defense: Control Hijacking ,More Control Hijacking attacks integer overflow ,More Control Hijacking attacks format string vulnerabilities, Defense against Control Hijacking - Platform Defenses, Defense against Control Hijacking - Run-time Defenses, Advanced Control Hijacking attacks.	08
II	Confidentiality Policies: Confinement Principle ,Detour Unix user IDs process IDs and privileges , More on confinement techniques ,System call interposition ,Error 404 digital Hacking in India part 2 chase , VM based isolation ,Confinement principle ,Software fault isolation , Rootkits ,Intrusion Detection Systems	08
III	Secure architecture principles isolation and lease: Access Control Concepts , Unix and windows access control summary ,Other issues in access control ,Introduction to browser isolation . Web security landscape : Web security definitions goals and threat models , HTTP content rendering .Browser isolation .Security interface , Cookies frames and frame busting, Major web server threats ,Cross site request forgery ,Cross site scripting ,Defenses and protections against XSS , Finding vulnerabilities ,Secure development.	08
IV	Basic cryptography: Public key cryptography ,RSA public key crypto ,Digital signature Hash functions ,Public key distribution ,Real world protocols ,Basic terminologies ,Email security certificates ,Transport Layer security TLS ,IP security , DNS security.	08
V	Internet Infrastructure: Basic security problems , Routing security ,DNS revisited ,Summary of weaknesses of internet security ,Link layer connectivity and TCP IP connectivity , Packet filtering firewall ,Intrusion detection.	08

Electronics Engineering

Electronics Engineering	3L:1T:0P	4 Credits
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Unit	Topics	Lectures
I	PN junction diode: Introduction of semiconductor materials; Semiconductor diode: Depletion layer, V-I characteristics, ideal and practical, diode resistance, capacitance, diode equivalent circuits, transition and diffusion capacitance, Zener diodes breakdown mechanism (Zener and avalanche).	8
II	Diode application: Series, parallel and series, parallel diode configuration, half and full wave rectification, clippers, clampers, Zener diode as shunt regulator, voltage-multiplier circuits special purpose two terminal devices : light-emitting diodes, Varactor (Varicap) diodes, tunnel diodes, liquid-crystal displays.	8
III	Bipolar junction transistors and field effect transistor: Bipolar junction transistor: Transistor construction, operation, amplification action, common base, common emitter, common collector configuration dc biasing BJTs: operating point, fixed-bias, emitter bias, voltage-divider bias configuration. Collector feedback, emitter-follower configuration. Bias stabilization. CE, CB, CC amplifiers and AC analysis of single stage CE amplifier (re Model). Field effect transistor: Construction and characteristic of JFETs. AC analysis of CS amplifier, MOSFET (depletion and enhancement) type, transfer characteristic.	8
IV	Operational amplifiers: Introduction and block diagram of Op-Amp, ideal & practical characteristics of Op-Amp, differential amplifier circuits, practical Op-Amp circuits (inverting amplifier, non-inverting amplifier, unity gain amplifier, summing amplifier, integrator, differentiator), Op-Amp parameters: input offset voltage, output offset voltage, input biased current, input offset current differential and common-mode operation.	8
V	Electronic instrumentation and measurements: Digital voltmeter: Introduction, RAMP techniques digital multimeters: Introduction Oscilloscope: introduction, basic principle, CRT, block diagram of oscilloscope, simple, measurement of voltage, current phase and frequency using CRO, introduction of digital storage oscilloscope and comparison of DSO with analog oscilloscope.	8

Text /Reference Books:

1. Robert L. Boylestand / Louis Nashelsky, "Electronic Devices and Circuit Theory," Latest Edition, Pearson Education.
2. H S Kalsi, "Electronic Instrumentation", Latest Edition, TMH Publication.
3. Meetidehran/ A.K. singh "fundamental of electronics Engineering", New age international publisher.

Course Outcomes:

At the end of this course students will demonstrate the ability to:

DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	Introduction: Functional units of digital system and their interconnections, buses, bus architecture, types of buses and bus arbitration. Register, bus and memory transfer. Processor organization, general registers organization, stack organization and addressing modes.	08
II	Arithmetic and logic unit: Look ahead carries adders. Multiplication: Signed operand multiplication, Booths algorithm and array multiplier. Division and logic operations. Floating point arithmetic operation, Arithmetic & logic unit design. IEEE Standard for Floating Point Numbers	08
III	Control Unit: Instruction types, formats, instruction cycles and sub cycles (fetch and execute etc), micro operations, execution of a complete instruction. Program Control, Reduced Instruction Set Computer, Pipelining. Hardwire and micro programmed control: micro programme sequencing, concept of horizontal and vertical microprogramming.	08
IV	Memory: Basic concept and hierarchy, semiconductor RAM memories, 2D & 2 1/2D memory organization. ROM memories. Cache memories: concept and design issues & performance, address mapping and replacement Auxiliary memories: magnetic disk, magnetic tape and optical disks Virtual memory: concept implementation.	08
V	Input / Output: Peripheral devices, I/O interface, I/O ports, Interrupts: interrupt hardware, types of interrupts and exceptions. Modes of Data Transfer: Programmed I/O, interrupt initiated I/O and Direct Memory Access., I/O channels and processors. Serial Communication: Synchronous & asynchronous communication, standard communication interfaces.	08

DSTL

Unit	Topic	Proposed Lecture
I	Set Theory: Introduction, Combination of sets, Multisets, Ordered pairs. Proofs of some general identities on sets. Relations: Definition, Operations on relations, Properties of relations, Composite Relations, Equality of relations, Recursive definition of relation, Order of relations. Functions: Definition, Classification of functions, Operations on functions, Recursively defined functions. Growth of Functions. Natural Numbers: Introduction, Mathematical Induction, Variants of Induction, Induction with Nonzero Base cases. Proof Methods, Proof by counter – example, Proof by contradiction.	08
II	Algebraic Structures: Definition, Groups, Subgroups and order, Cyclic Groups, Cosets, Lagrange's theorem, Normal Subgroups, Permutation and Symmetric groups, Group Homomorphisms, Definition and elementary properties of Rings and Fields.	08
III	Lattices: Definition, Properties of lattices – Bounded, Complemented, Modular and Complete lattice. Boolean Algebra: Introduction, Axioms and Theorems of Boolean algebra, Algebraic manipulation of Boolean expressions. Simplification of Boolean Functions, Karnaugh maps, Logic gates, Digital circuits and Boolean algebra.	08
IV	Propositional Logic: Proposition, well formed formula, Truth tables, Tautology, Satisfiability, Contradiction, Algebra of proposition, Theory of Inference. (8) Predicate Logic: First order predicate, well formed formula of predicate, quantifiers, Inference theory of predicate logic.	08
V	Trees: Definition, Binary tree, Binary tree traversal, Binary search tree. Graphs: Definition and terminology, Representation of graphs, Multigraphs, Bipartite graphs, Planar graphs, Isomorphism and Homeomorphism of graphs, Euler and Hamiltonian paths, Graph coloring, Recurrence Relation & Generating function: Recursive definition of functions, Recursive algorithms, Method of solving recurrences. Combinatorics: Introduction, Counting Techniques, Pigeonhole Principle	08

Data Structures

Unit	Topic	Proposed Lecture
I	<p>Introduction: Basic Terminology, Elementary Data Organization, Built in Data Types in C. Algorithm, Efficiency of an Algorithm, Time and Space Complexity, Asymptotic notations: Big Oh, Big Theta and Big Omega, Time-Space trade-off. Abstract Data Types (ADT)</p> <p>Arrays: Definition, Single and Multidimensional Arrays, Representation of Arrays: Row Major Order, and Column Major Order, Derivation of Index Formulae for 1-D,2-D,3-D and n-D Array Application of arrays, Sparse Matrices and their representations.</p> <p>Linked lists: Array Implementation and Pointer Implementation of Singly Linked Lists, Doubly Linked List, Circularly Linked List, Operations on a Linked List. Insertion, Deletion, Traversal, Polynomial Representation and Addition Subtraction & Multiplications of Single variable & Two variables Polynomial.</p>	08
II	<p>Stacks: Abstract Data Type, Primitive Stack operations: Push & Pop, Array and Linked Implementation of Stack in C, Application of stack: Prefix and Postfix Expressions, Evaluation of postfix expression, Iteration and Recursion- Principles of recursion, Tail recursion, Removal of recursion Problem solving using iteration and recursion with examples such as binary search, Fibonacci numbers, and Hanoi towers. Tradeoffs between iteration and recursion.</p> <p>Queues: Operations on Queue: Create, Add, Delete, Full and Empty, Circular queues, Array and linked implementation of queues in C, Dequeue and Priority Queue.</p>	08
III	<p>Searching: Concept of Searching, Sequential search, Index Sequential Search, Binary Search. Concept of Hashing & Collision resolution Techniques used in Hashing. Sorting: Insertion Sort, Selection, Bubble Sort, Quick Sort, Merge Sort, Heap Sort and Radix Sort.</p>	08
IV	<p>Graphs: Terminology used with Graph, Data Structure for Graph Representations: Adjacency Matrices, Adjacency List, Adjacency. Graph Traversal: Depth First Search and Breadth First Search, Connected Component, Spanning Trees, Minimum Cost Spanning Trees: Prims and Kruskal algorithm. Transitive Closure and Shortest Path algorithm: Warshal Algorithm and Dijkstra Algorithm.</p>	08
V	<p>Trees: Basic terminology used with Tree, Binary Trees, Binary Tree Representation: Array Representation and Pointer(Linked List) Representation, Binary Search Tree, Strictly Binary Tree ,Complete Binary Tree . A Extended Binary Trees, Tree Traversal algorithms: Inorder, Preorder and Postorder, Constructing Binary Tree from given Tree Traversal, Operation of Insertation , Deletion, Searching & Modification of data in Binary Search . Threaded Binary trees, Traversing Threaded Binary trees. Huffman coding using Binary Tree. Concept & Basic Operations for AVL Tree , B Tree & Binary Heaps</p>	08
Text books:		