Aggarwal Richa

Senior Design Engineer
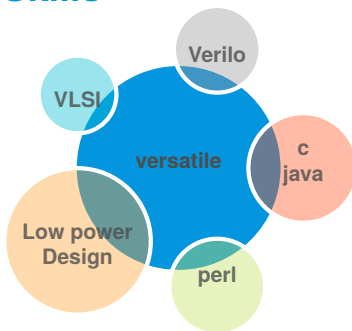
Gender : Female

Age : 28 years

**Email:
richaaggar-
wal90@gmail.com**

Country: U.S.A

Skills

Verilo
VLSI
versatile
c java
Low power Design
perl

Experience

Richa Aggarwal is a senior Design Engineer at Samsung Austin Research and Development Center . she has done her Master in Integrated Circuits Systems from The University of Texas at Austin .she did Graduate tecnical internship at Intel Corporation. After Masters , she joined Oracle as a Hardware Developer, where my routine task were to Microarchitecture development, assessment and Verilog coding of next generation SPARC core for instruction fetch unit (IFU) that includes instruction prefetch and branch prediction. For the current time she is working with the Samsung Austin Research and Development Center as a Senior Design Engineer .

Interest

she has her intrest in the electronics and communication field .She is technically skilled in various programing languages such as :- Java , c ,Matlab,Verlog etc . she likes to learn new languages . she likes to seek new opportunities to learn new things .

Likes|Dislikes

She Likes to work on the simple GUI . Doesnt like to much of the things at a time. Want to have aclear look ti things . Wants thing to be simple to understand . Doesnt like end moments of work . Like things to be organized . Does not like late work

Business Values

Wants the calculator to be simple and effective .Wants to have hiatory attached to it for the purpose of ease . wants calculator to be very specific about the number only .Want the arrangement of the buttons every specific as easy and fast to be used daily . Wants display to only show results not the calculation .