

Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in Electrical Engineering
 (Applicable from the academic session 2018-2019)

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| Name of the course | ELECTRICAL AND ELECTRONICS DESIGN LABORATORY |
| Course Code: PC-EE 681 | Semester: 6th |
| Duration: 6 months | Maximum marks:100 |
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| Teaching Scheme | Examination scheme: |
| Theory: 1hr/week | Continuous Internal Assessment:40 |
| Tutorial: 0 hr/week | External Assessment: 60 |
| Practical: 4 hrs/week | |
| Credit Points:3 | |
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| | GROUP A |
| 1. | Designing a heating element with specified wattage, voltage and ambient temperature. |
| 2. | Designing an aircore grounding reactor with specified operating voltage, nominal current and fault current |
| 3. | Designing the power distribution system for a small township |
| 4. | Designing a double circuit transmission line for a given voltage level and power (MVA) transfer. |
| 5. | Wiring and installation design of a multistoried residential building (G+4,not less than 16 dwelling flats with a lift and common pump) |
| | GROUP B |
| 6. | Designing an ONAN distribution transformer. |
| 7. | Designing a three phase squirrel cage induction motor. |
| 8. | Designing a three phase wound rotor induction motor. |
| 9. | Designing a split phase squirrel cage induction motor for a ceiling fan or a domestic pump. |
| 10. | Designing a permanent magnet fractional hp servo motor . |
| | GROUP C |

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| 11. | Design the control circuit of a Lift mechanism |
| 12. | Design a controller for speed control of DC machine. |
| 13. | Design a controller for speed control of AC machine. |
| 14. | Electronic system design employing electronic hardware (Analog, Digital, Mixed signal), microcontrollers, CPLDs, and FPGAs, PCB design and layout leading to implementation of an application |

Topics to be covered in the Lecture class:

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| 1. | Basic concepts on measurements; Noise in electronic systems; Sensors and signal conditioning circuits; Introduction to electronic instrumentation and PC based data acquisition; Electronic system design, Analog system design, Interfacing of analog and digital systems, Embedded systems,; System assembly considerations.. | 01 |
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Evaluation Method:

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| <ol style="list-style-type: none"> 1. The students would INDIVIDUALLY design the equipment and systems as per specifications provided by the class teacher following established procedures. 2. For each student, one item from each of the three groups would be chosen. 3. For unspecified items of specification and or specifications of wires, cables etc., data should be taken by students from handbooks and Indian standard. 4. Students should spend the allotted periods for carrying out design computations. 5. Their attendance shall be recorded. 6. Students should maintain a dedicated bound notebook for recording design activities like calculations, formulae used, sketches, flowcharts etc. The notebook should be regularly submitted to the class teacher for review and signature. 7. Evaluation would be based on (i) Class attendance (20%), (ii) Design Note Book (30%) (iii) Design Report (30%) (iv) End of semester viva (20%,) |
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Institute may develop experiments based on the theory taught in addition to experiments mentioned.

Course outcome: After completion of this course, the learners will be able to

1. explain basic concept of measurement, noise in electronic system, sensor and signal conditioning circuits
2. implement PC based data acquisition systems
3. construct circuits with appropriate instruments and safety precautions
4. design heating elements, air core grounding reactor, power distribution system for small township, double circuit transmission line and Electric machines
5. do wiring and installation design of a multistoried residential building with lift and pump
6. design electronic hardware for controller of lift, speed of AC/DC motor, and for an application with analog, digital, mixed signal, microcontroller and PCB

Special Remarks: The above-mentioned outcomes are not limited. Institute may redefine outcomes based their program educational objective.