Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in Computer Science & Engineering
(Applicable from the academic session 2018-2019)
PRACTICAL SYLLABUS
Semester IV

Computer Architecture Lab
Code: PCC-CS492
Contacts: 4P

Name of the Course:	**Computer Architecture Lab**
Course Code: PCC-CS492	Semester: IV
Duration: 6 months	Maximum Marks:100
Teaching Scheme:	
Theory: hrs./week	Continuous Internal Assessment
Tutorial: NIL	External Assesement: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2
Course Outcomes:	
1	PCC-CS402.1
2	PCC-CS402.2
3	PCC-CS402.3
Pre-Requisite:	
1	The hardware based design has been done in 1.the Analog & Digital Electronics laboratory
2	Computer Organisation laboratory

Laboratory Experiments:	
1	HDL introduction.
2	Basic digital logic base programming with HDL
3	8-bit Addition, Multiplication, Division
4	8-bit Register design
5	Memory unit design and perform memory operations.
6	8-bit simple ALU design
7	8-bit simple CPU design
8	Interfacing of CPU and Memory.

Any experiment specially designed by the college
(Detailed instructions for Laboratory Manual to be followed for further guidance)