Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in Computer Science & Engineering
(Applicable from the academic session 2018-2019)

Advanced Computer Architecture
Code: PEC-IT501C
Contacts: 3L

Name of the Course:	**Advanced Computer Architecture**	
Course Code: PEC-IT501C	Semester: V	
Duration: 6 months	Maximum Marks:100	
Teaching Scheme	**Examination Scheme**	
Theory:3 hrs./week	Mid Semester exam: 15	
Tutorial: NIL	Assignment and Quiz: 10 marks	
	Attendance : 5 marks	
Practical: NIL	End Semester Exam :70 Marks	
Credit Points:	3	

Unit	Content	Hrs/Unit	Marks/Unit
1	Computer Architecture and Organization-Review, Fundamentals of Computer Design, Technology Trends Cost Performance Analysis (3L) Parallel Processing Architectures- Taxonomy- SISD, MISD, SIMD,MIMD, PRAM models (3L)	6	
2.	Data and Resource Dependencies, Program Partitioning and Scheduling, Control Flow vs. Data Flow (3L) Network topologies-Static, Dynamic, Types of Networks (3L) RISC vs. CISC, Memory Hierarchy, Virtual Memory (4L)	10	
3	Concepts of Pipelining, Instruction Pipelining, dynamic pipelining, arithmetic pipelines. (4L) Multiprocessors- Multistage Networks, Cache Coherence, Synchronization, Message- passing (4L) Vector Processing Principles- Instruction types, Compound, Vector Loops, Chaining (4L)	12	
4	Array Processors- Structure, Algorithms (3L) Data Flow Architecture- Graphs. Petri Nets, Static and Dynamic DFA, VLSI Computations (4L) Parallel Programming Models, Languages, Compilers (4L)	11	

Text book and Reference books:

1. Computer Architecture and Parallel Processing- Kai Hwang and A. .Brigggs International Edition, McGraw Hill
2. Advanced Computer Architecture: D. Sima, T. fountain, P. Kacsuk, Pearson
3. Parallel Computer Architecture: D. Culler, J.P.Singh, A.Gupta, Elsevier