Ankush Saha

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SUMMARY

Computer Science undergraduate with strong foundations in Data Structures, Algorithms, and core CS concepts. Experienced in building end-to-end applications using Python and NLP techniques, including TF-IDF–based text similarity, resume parsing, and web deployment with Streamlit. Seeking software or data-driven roles where problem-solving and backend skills can create real-world impact.

EDUCATION

Bachelor of Technology in Computer Science Engineering — **2023 − 2027**
Kalinga Institute of Industrial Technology, Bhubaneswar — CGPA: 8.55 / 10.00

PROJECTS

Resume Screening Web Application

- Developed an **NLP-based resume screening application** using **Python, Streamlit, and spaCy**, enabling automated candidate ranking against job descriptions.
- Implemented **TF-IDF vectorization**, **fuzzy string matching**, and **keyword-based bonus scoring** to evaluate resume–JD similarity.
- Integrated **PDF/DOCX/TXT parsing** with fallback OCR for scanned resumes, ensuring robust text extraction across formats.
- Designed a **user-friendly web interface** with Streamlit for uploading resumes, pasting job descriptions, and viewing ranked results.
- Applied **data preprocessing, entity recognition (names, organizations, dates)**, and scoring aggregation to generate structured candidate insights.
- Demonstrated **end-to-end project development** from backend logic to frontend visualization, with potential for cloud deployment.

SKILLS

- **Programming Languages:** Java, Python, C
- **Web Development:** HTML, CSS, JavaScript, Streamlit
- **Databases:** SQL, MySQL, SAP HANA
- **Core CS Concepts:** Data Structures & Algorithms, Computer Networks, OOP, DBMS, Operating Systems, Socket Programming
- **NLP & Text Processing:** spaCy, scikit-learn, TF-IDF vectorization, Fuzzy string matching, Keyword scoring
- **Data Extraction & Processing:** OCR (pdfplumber, Tesseract), Data preprocessing, Entity recognition
- **Tools & IDEs:** Linux (WSL), Git, GitHub, IntelliJ IDEA, VS Code, Eclipse
- **Hardware & FPGA Tools:** Xilinx Vivado – Implemented basic digital circuits (encoder, decoder, multiplexer) with RTL schematics and timing diagrams