

# DM7446A, DM5447A/DM7447A BCD to 7-Segment Decoders/Drivers

## **General Description**

The 46A and 47A feature active-low outputs designed for driving common-anode LEDs or incandescent indicators directly. All of the circuits have full ripple-blanking input/output controls and a lamp test input. Segment identification and resultant displays are shown on a following page. Display patterns for BCD input counts above nine are unique symbols to authenticate input conditions.

All of the circuits incorporate automatic leading and/or trailing-edge, zero-blanking control (RBI and RBO). Lamp test (LT) of these devices may be performed at any time when the BI/RBO node is at a high logic level. All types contain

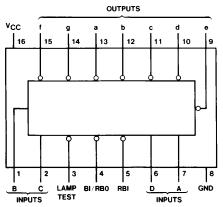
an overriding blanking input (BI) which can be used to control the lamp intensity (by pulsing) or to inhibit the outputs.

### **Features**

- All circuit types feature lamp intensity modulation capability
- Open-collector outputs drive indicators directly
- Lamp-test provision
- Leading/trailing zero suppression

# **Connection Diagram**





TL/F/6518-1

Order Number DM5447AJ, DM7446AN or DM7447AN See NS Package Number J16A or N16E

## **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range

 Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter		Units		
Cymbol	T drameter	Min	Nom	Max	Oilles
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
$V_{IH}$	High Level Input Voltage	2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8	V
V <sub>OH</sub>	High Level Output Voltage (a thru g)			30	V
ГОН	High Level Output Current (BI/RBO)			-0.2	μΑ
l <sub>OL</sub>	Low Level Output Current (a thru g)			40	mA
l <sub>OL</sub>	Low Level Output Current (BI/RBO)			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

## '46A Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Cone	ditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> =	= -12 mA			-1.5	V
V <sub>OH</sub>	High Level Output Voltage (BI/RBO)	$V_{CC} = Min$ $I_{OH} = Max$		2.4	3.7		V
I <sub>CEX</sub>	High Level Output Current (a thru g)	$V_{CC} = Max, V_{C}$ $V_{IL} = Max, V_{IH}$	•			250	μΑ
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL}$ $V_{IH} = Min, V_{IL}$			0.3	0.4	V
II	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> (Except BI/RBC				1	mA
l <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> (Except BI/RBC				40	μΑ
	Low Level Input	V <sub>CC</sub> = Max	BI/RBO			-4	mA
	Current	$V_I = 0.4V$	Others			-1.6	1 111/2
los	Short Circuit Output Current	V <sub>CC</sub> = Max (BI	/RBO)			-4	mA
Icc	Supply Current	V <sub>CC</sub> = Max (Note 2)			60	103	mA

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 2:  $I_{\mbox{\footnotesize CC}}$  is measured with all outputs open and all inputs at 4.5V.

'46A Switching Characteristics at  $V_{CC}=5V$  and  $T_A=25^{\circ}C$  (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	$C_L = 15 \text{ pF}$ $R_L = 120\Omega$		100	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output			100	ns

# **Recommended Operating Conditions**

Symbol	Parameter		DM5447A			Units		
Зушьог	raiametei	Min	Nom	Max	Min	Nom	Max	Office
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	٧
V <sub>IH</sub>	High Level Input Voltage	2			2			٧
$V_{IL}$	Low Level Input Voltage			0.8			0.8	٧
V <sub>OH</sub>	High Level Output Voltage (a thru g)			15			15	V
I <sub>OH</sub>	High Level Output Current (BI/RBO)			-0.2			-0.2	μΑ
I <sub>OL</sub>	Low Level Output Current (a thru g)			40			40	mA
I <sub>OL</sub>	Low Level Output Current (BI/RBO)			8			8	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

## '47A Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Cond	ditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> =	= -12 mA			-1.5	V
V <sub>OH</sub>	High Level Output Voltage (BI/RBO)	$V_{CC} = Min$ $I_{OH} = Max$		2.4	3.7		V
I <sub>CEX</sub>	High Level Output Current (a thru g)	$V_{CC} = Max, V_{CC}$ $V_{IL} = Max, V_{IH}$	•			250	μΑ
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{Ol}$ $V_{IH} = Min, V_{IL}$	-		0.3	0.4	V
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I$			1	mA	
I <sub>IH</sub>	High Level Input Current	$V_{CC} = Max, V_I$	= 2.4V			40	μΑ
I <sub>IL</sub>	Low Level Input	V <sub>CC</sub> = Max	BI/RBO			-4	mA
	Current	$V_I = 0.4V$	Others			-1.6	1117
los	Short Circuit Output Current	V <sub>CC</sub> = Max (BI	/RBO)			-4	mA
Icc	Supply Current	V <sub>CC</sub> = Max	DM54		60	85	mA
		(Note 2)	DM74		60	103	

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 2:  $I_{\text{CC}}$  is measured with all outputs open and all inputs at 4.5V.

'47A Switching Characteristics at  $V_{CC}=5V$  and  $T_A=25^{\circ}C$  (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 120\Omega$		100	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output			100	ns

## **Function Table**

### 46A, 47A

Decimal or			Inpu	ts			BI/RBO	Outputs			Note				
Function	LT	RBI	D	С	В	Α	(Note 1)	а	b	С	d	е	f	g	Note
0	Н	Н	L	L	L	L	Н	L	L	L	L	L	L	Н	
1	Н	Х	L	L	L	Н	Н	Н	L	L	Н	Н	Н	Н	
2	Н	×	L	L	Н	L	Н	L	L	Н	L	L	Н	L	
3	Н	Х	L	L	Н	Н	Н	L	L	L	L	Н	Н	L	
4	Н	×	L	Н	L	L	Н	Н	L	L	Н	Н	L	L	
5	Н	X	L	Н	L	Н	Н	L	Н	L	L	Н	L	L	
6	Н	×	L	Н	Н	L	н	Н	Н	L	L	L	L	L	
7	Н	Х	L	Н	Н	Н	Н	L	L	L	Н	Н	Н	Н	(2)
8	Н	×	Н	L	L	L	н	L	L	L	L	L	L	L	(2)
9	Н	Х	Н	L	L	Н	Н	L	L	L	Н	Н	L	L	
10	Н	×	Н	L	Н	L	Н	Н	Н	Н	L	L	Н	L	
11	Н	Х	Н	L	Н	Н	Н	Н	Н	L	L	Н	Н	L	
12	Н	×	Н	Н	L	L	н	Н	L	Н	Н	Н	L	L	
13	Н	Х	Н	Н	L	Н	Н	L	Н	Н	L	Н	L	L	
14	Н	Х	Н	Н	Н	L	Н	Н	Н	Н	L	L	L	L	
15	Н	Х	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	
ВІ	Х	Х	Х	Х	Х	Х	L	Н	Н	Н	Н	Н	Н	Н	(3)
RBI	Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	(4)
LT	L	Х	Х	Х	Х	Х	Н	L	L	L	L	L	L	L	(5)

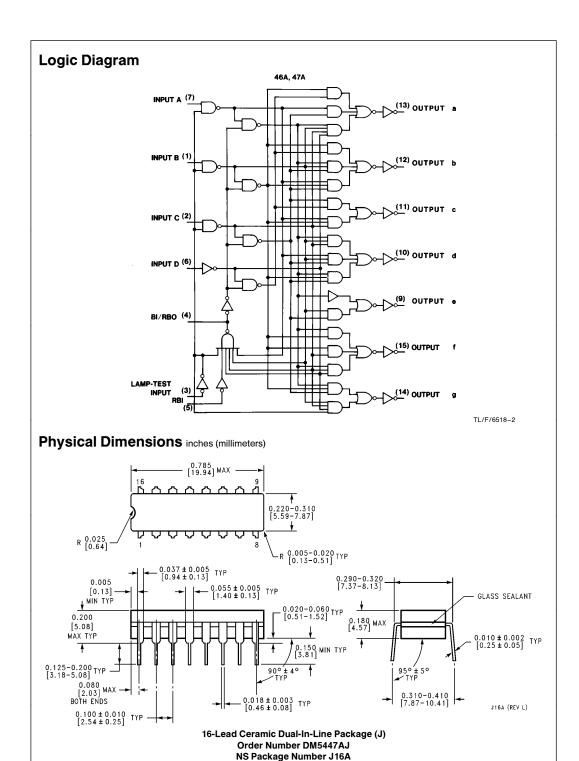
Note 1: BI/RBO is a wire-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).

Note 2: The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The ripple-blanking input (RBI) must be open or high if blanking of a decimal zero is not desired.

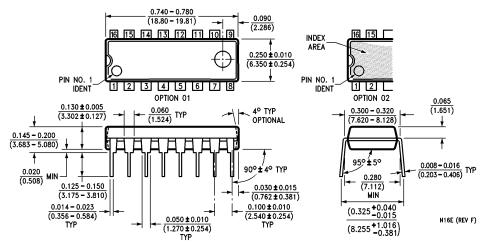
Note 3: When a low logic level is applied directly to the blanking input (BI), all segment outputs are high regardless of the level of any other input.

Note 4: When ripple-blanking input (RBI) and inputs A, B, C, and D are at a low level with the lamp test input high, all segment outputs go H and the ripple-blanking output (RBO) goes to a low level (response condition).

 $\textbf{Note 5:} \ When the \ blanking \ input/ripple-blanking \ output \ (Bl/RBO) \ is \ open \ or \ held \ high \ and \ a \ low \ is \ applied \ to \ the \ lamp-test \ input, \ all \ segment \ outputs \ are \ L \ .$ H = High level, L = Low level, X = Don't Care



# Physical Dimensions inches (millimeters) (Continued)



16-Lead Molded Dual-In-Line Package (N) Order Number DM7446AN or DM7447AN NS Package Number N16E

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