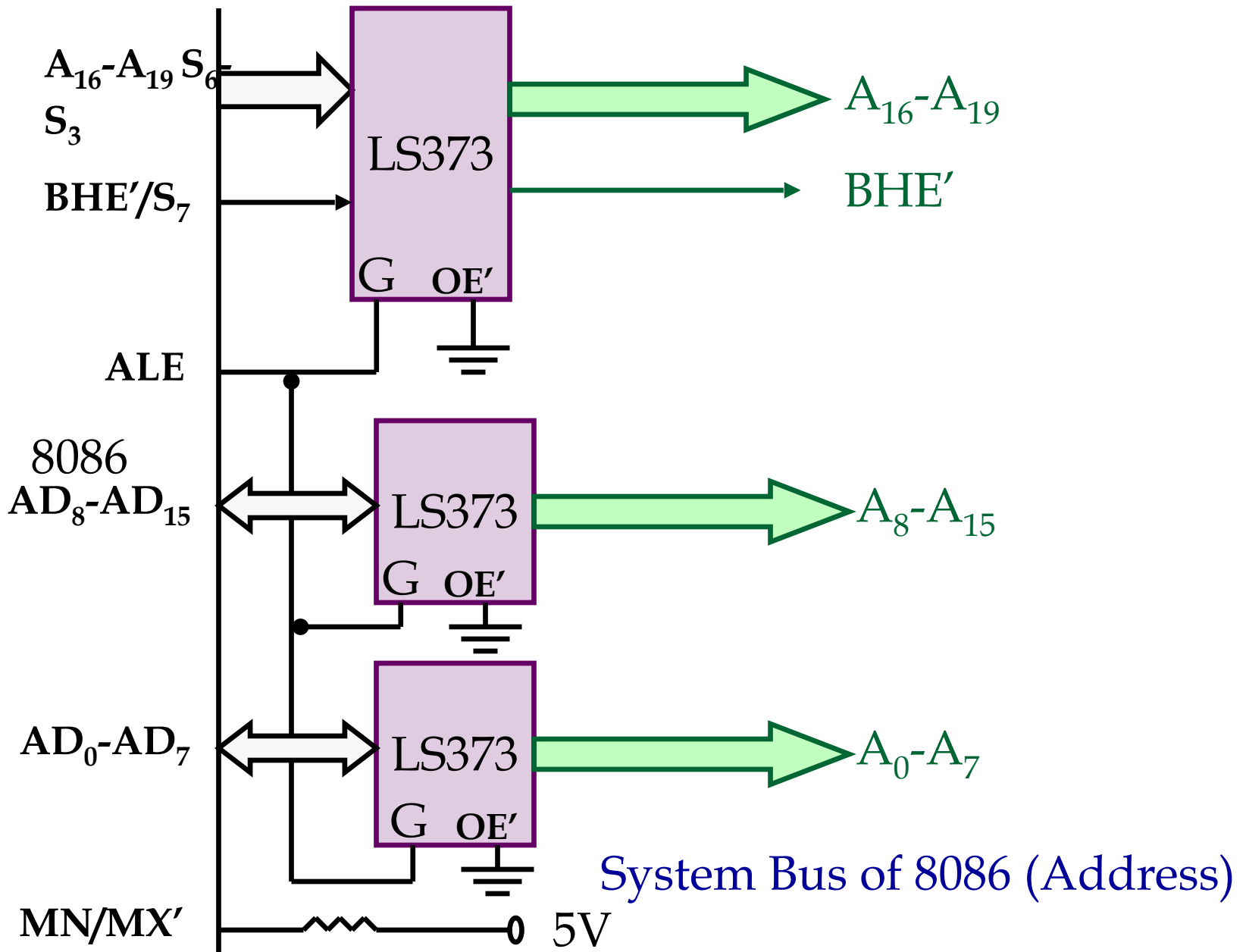
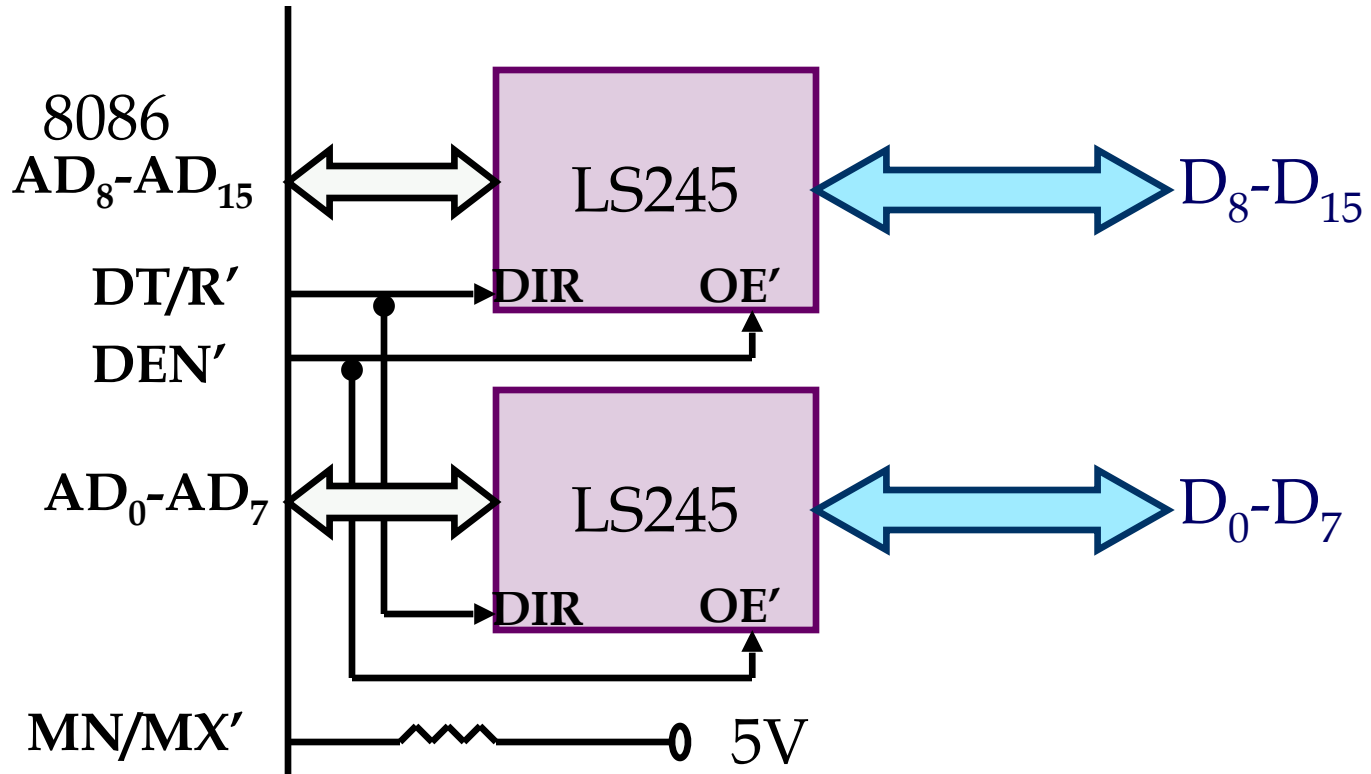


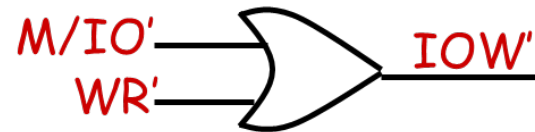
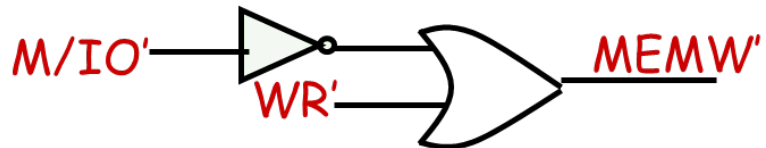
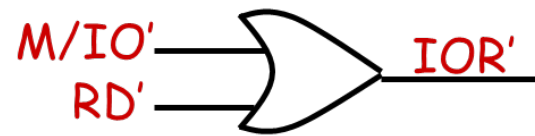
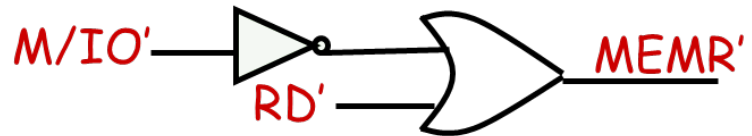
8086 Inputs

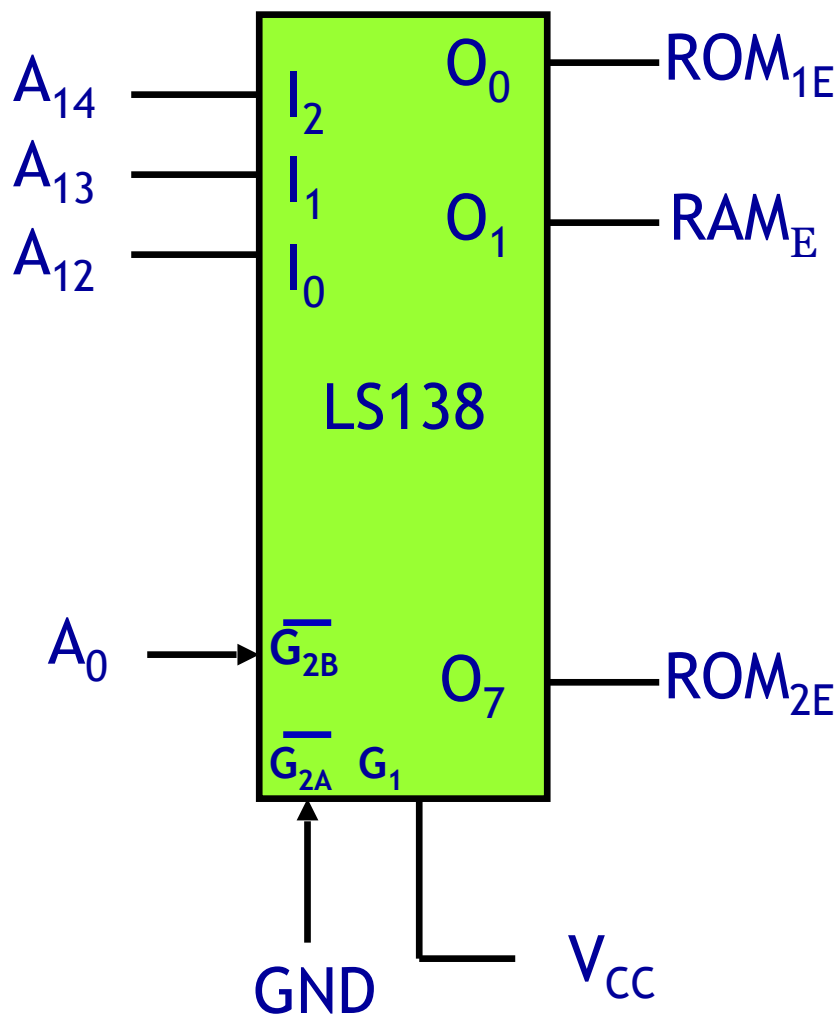




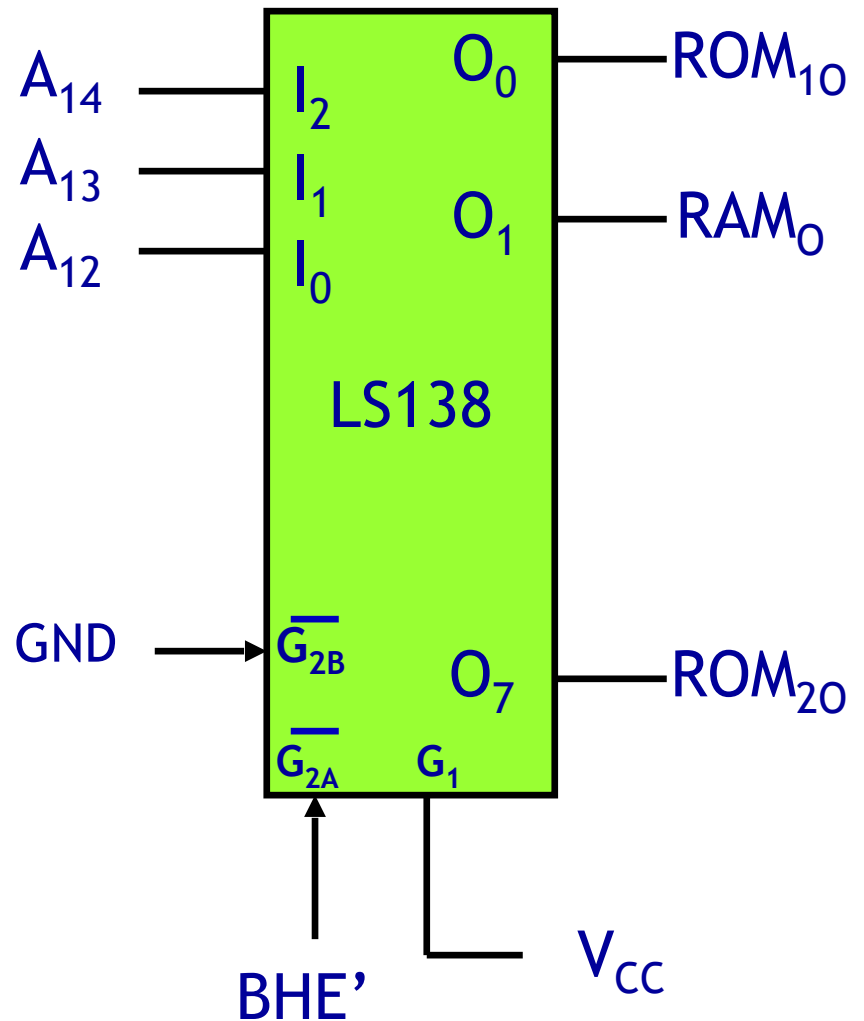
System Bus of 8086(Data)

M/IO'	RD'	WR'	Bus cycle
1	0	1	MEMR'
1	1	0	MEMW'
0	0	1	IOR'
0	1	0	IOW'



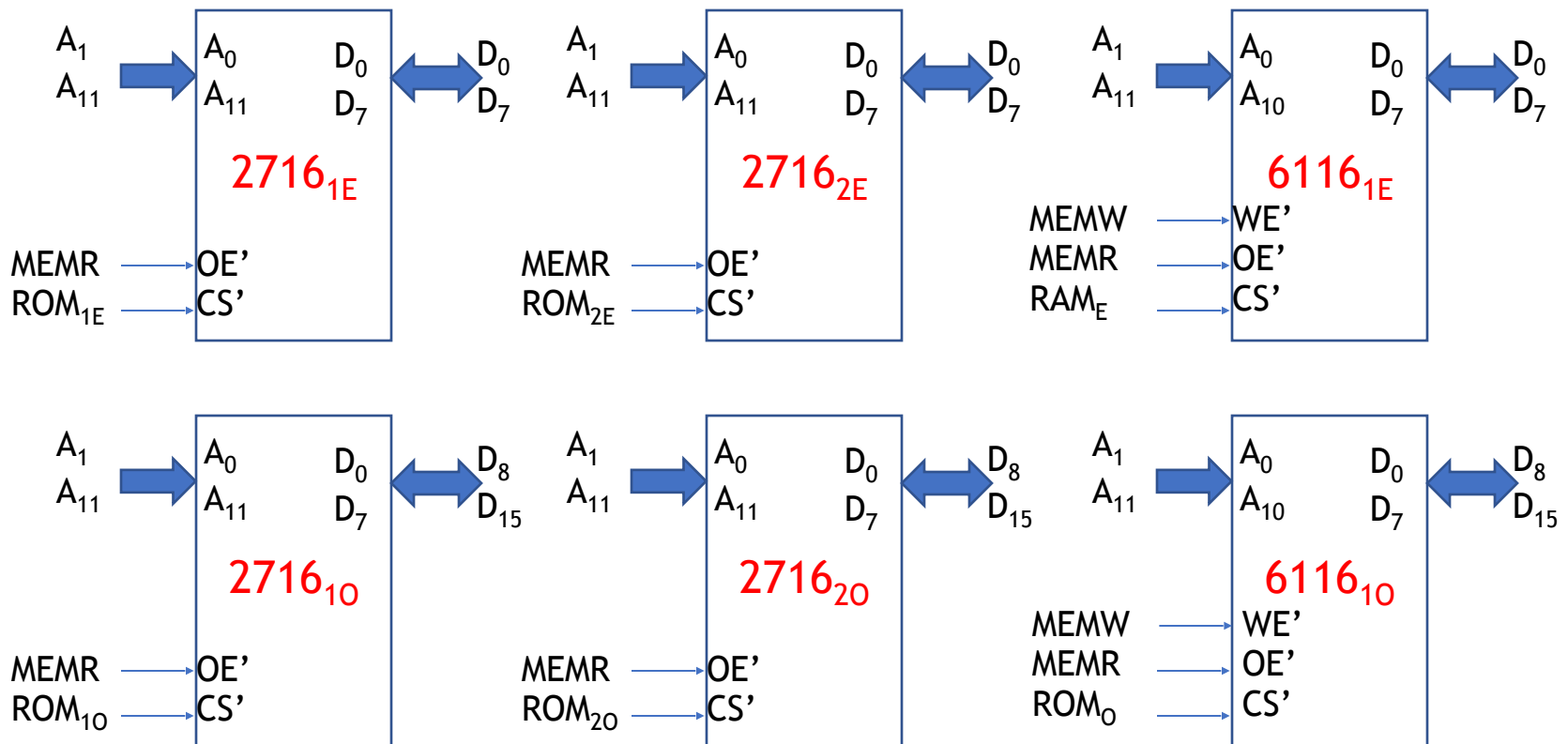


For Even Bank

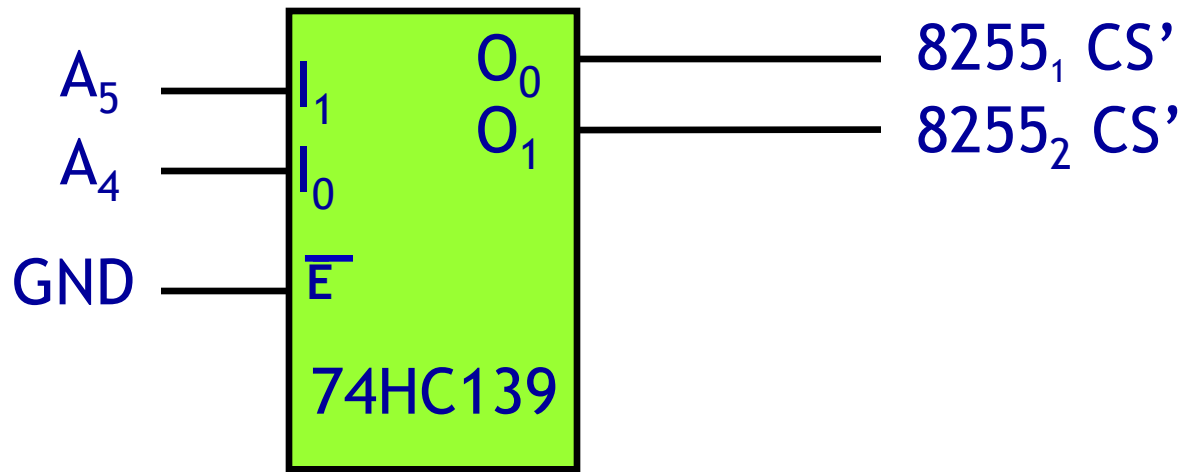


For Odd Bank

Memory Decoder

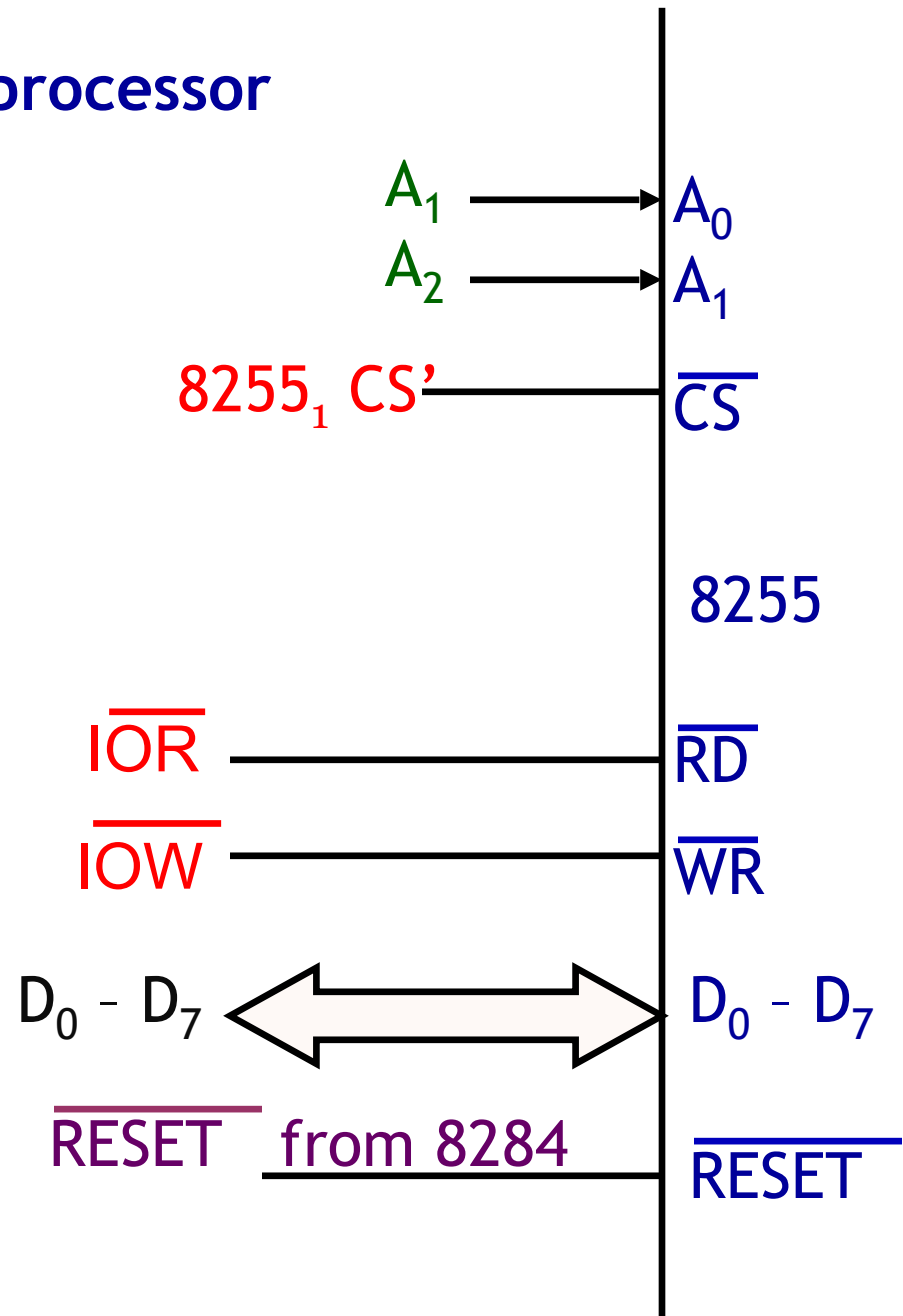


Memory Interfacing

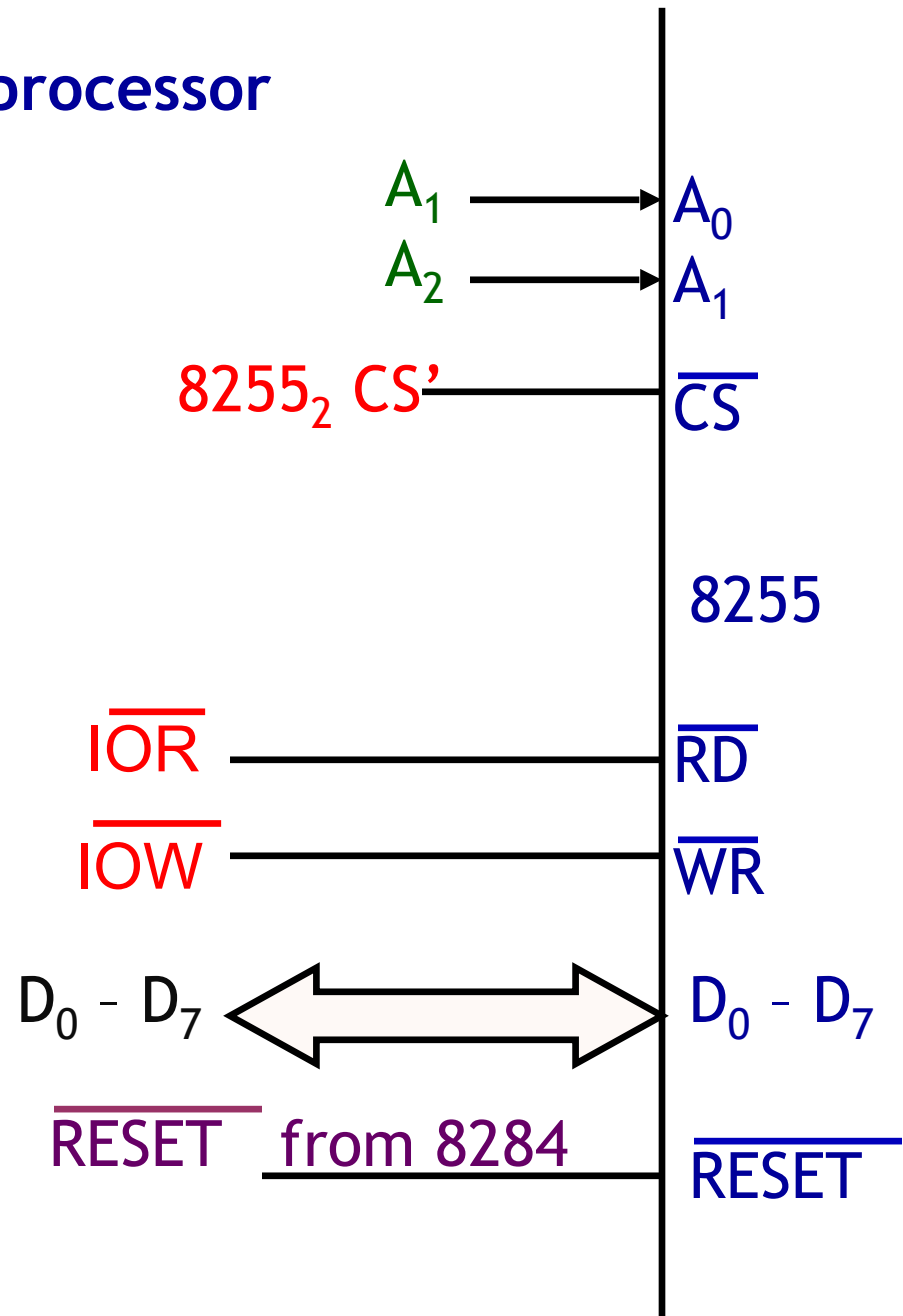


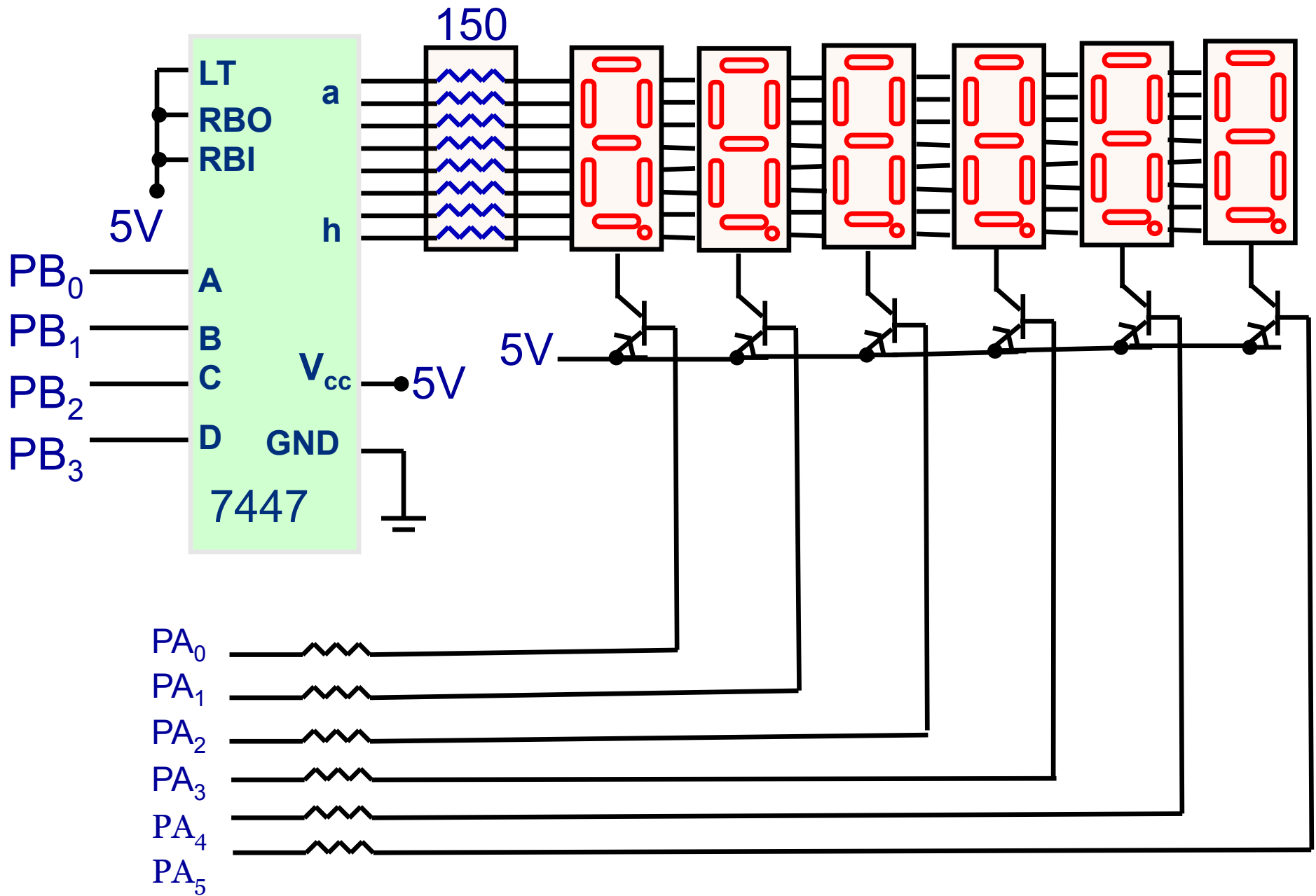
I/O
Decoder

8255₁ Interface to the processor

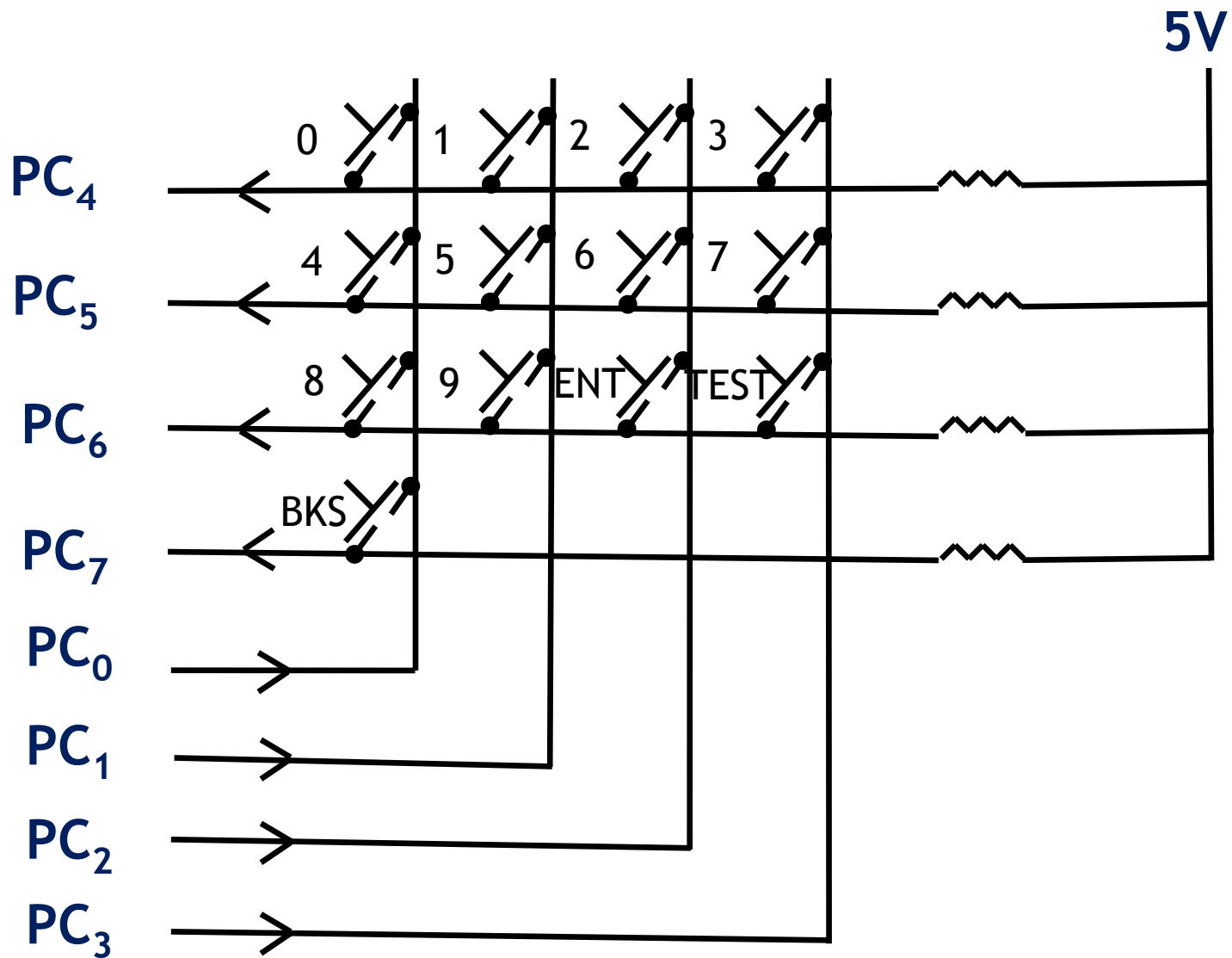


8255₂ Interface to the processor

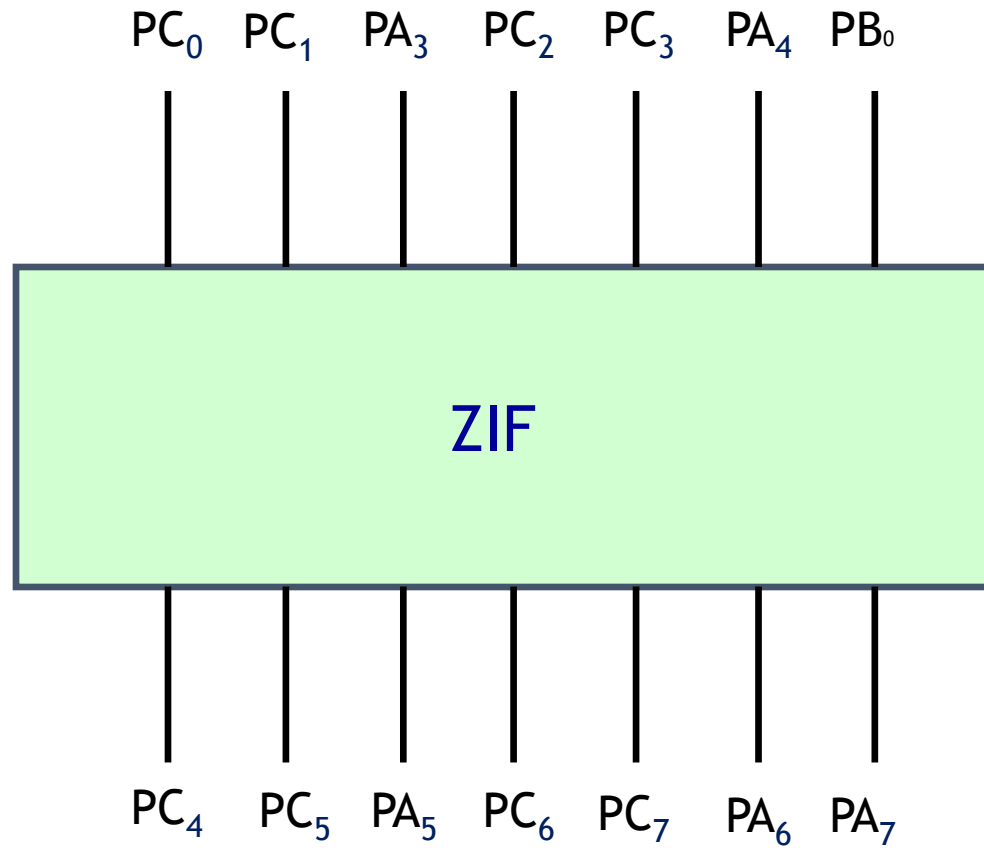




8255₁ Interface to the 7 Segment LED Display through



8255₁ Interface to the Hex Keypad



8255₂ Interface to the ZIF