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## EDUCATION

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M.S, Computer Sciences                      **University of Wisconsin-Madison**                      Fall 2014 - May 2016

- **GPA- 3.80**  
Courses – Adv. Databases (764), Big Data/ Cloud Computing (838), Algorithms (577), Computer Vision & Learning (766), Contest Programming (578), Adv. Graphics (838), Comp. Architecture (552)
- 22<sup>nd</sup> position at ICPC regionals representing UW-Madison from 150 teams.

B.S, EECE (Honors)                      **Indian Institute of Technology [IIT], Kharagpur**                      July 2009 – 2013

- **GPA – 3.5, Top 10%** of my class.  
**Minor in Computer Science** and Engineering (Among the 7 out of 1300 students to get this).
- GRE – 330, 4.5 in Analytical Writing. TOEFL – 117/120

## PROFESSIONAL EXPERIENCE - FULL TIME

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**Software Engineer, Research Intern**                      **Oracle, San Francisco, CA**                      Summer 2015

Accelerators for intensive database queries

- **Created API's** used by core Oracle DB teams to run their **operations faster** by exploiting hardware features (like vectors, multi-threads) available on the SPARC and Intel platforms.
- Implemented the **external merge sort** query, with lot of optimization in the sorting and merging procedures. These had to be planned keeping in mind the hardware limitations of the SPARC SoC.

**Associate Engineer**                      **Qualcomm Bangalore, India**                      June 2013- June 14

Bus Integrated Memory Controller team (DDR) at Qualcomm Bangalore Design Centre.

- Chip design and verification. Executed **formal verification** of protocol conversion bridges of the System Memory Management Units (SMMU). Found and **reported 7 bugs** in the design and tool.
- Deployed the **OVM methodology** for this project and dealt with systems in C, System Verilog. Developed and updated the native test cases and test vectors.

**Interim Engineering Intern**                      **Qualcomm India**                      Summer 2012

Post Silicon, Design Verification team.

- Worked on **Optimizing CPU utilization and memory footprint** during the **gate level functional simulation of a net-list**.
- Lead a group of interns on a side project which came runner's up in their innovation competition.

## PROGRAMMING SKILLS

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- C, C++
- Python, Perl (basic).
- MATLAB, Verilog/System Verilog, VHDL.

## SELECTED PROJECTS AND RESEARCH EXPERIENCE

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### Graduate Research Assistant

UW-Madison

Fall 2015

*Deploying a neuro-imaging application on Amazon AWS cloud*

- Investigating the best way of moving a **complex image processing application** involving 'Multivariate General Linear Models (MGLM) on Riemannian Manifolds' to the **Amazon Cloud**.
- Managing **\$30,000** worth of Amazon compute resources for the project.

### Course Project

UW-Madison

Spring 2015

*Web based database of a library of books*

- Created a web app using simple PHP with a backend of a MYSQL database of library of books.
- Supported complex queries like most popular author, cheapest seller for a genre etc.

## NOTABLE ACHIEVEMENTS

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- Offered admission to **Indian Institute of Management (IIM)** Ahmedabad, Bangalore. These are considered to be the world's toughest B-Schools to get into [2014]. Top 0.01% of applicants get in.
- Among **top 0.1% in IIT-JEE 2009** and among **top 0.02% in AIEEE 2009**, the prestigious entrance exams.
- Offered admission to **Purdue University** with the **prestigious Lynn Fellowship** award [Awarded to only 5 applicants from the graduate applicant pool] [2014].

## PUBLICATIONS

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- "An Overflow-free Fixed-point Singular Value Decomposition Algorithm for Dimensionality Reduction of Hyperspectral Images". Accepted at **NASA Hyspiri conference 2015**, NASA Goddard Space Centre.
- Anmol Mohanty, Chandrakanth Reddy. "Enhancement of Switching time and power of CMOS devices." Awarded the best paper award [[http://interscience.in/IJPSOEM\\_Vol2Iss1-2/61-64.pdf](http://interscience.in/IJPSOEM_Vol2Iss1-2/61-64.pdf)].