

<https://github.com/annadostoevskaya/DIPLOMA>



The figure shows three circuit diagrams, each representing a different PMIC pin configuration:

- Top Diagram:** The **PMIC_DCBSET** pin is connected to a node that branches into two paths. One path goes through resistor **R5** (labeled **0R**) to **PMIC_VINT**. The other path goes through resistor **R6** (labeled **0R**) to **GND**.
- Middle Diagram:** The **PMIC_MODSET** pin is connected to a node that branches into two paths. One path goes through resistor **R7** (labeled **0R**) to **PMIC_VINT**. The other path goes through resistor **R8** (labeled **0R**) to **GND**.
- Bottom Diagram:** The **PMIC_PHSET** pin is connected to a node that branches into two paths. One path goes through resistor **R9** (labeled **0R**) to **PMIC_VINT**. The other path goes through resistor **R10** (labeled **0R**) to **GND**.

Project:	AXP 805 Debug Board
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