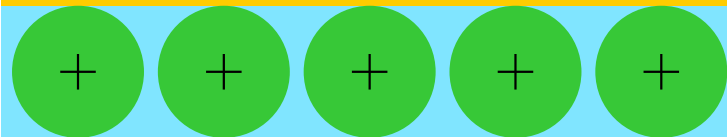
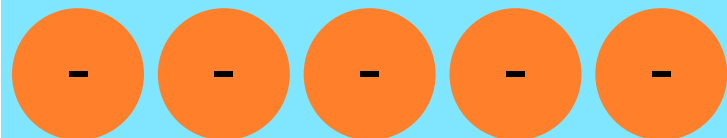


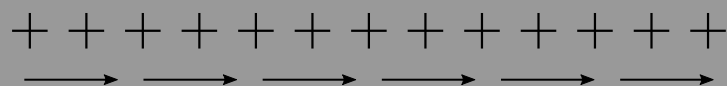
gate



V_{GS}



V_{DS}



semiconductor