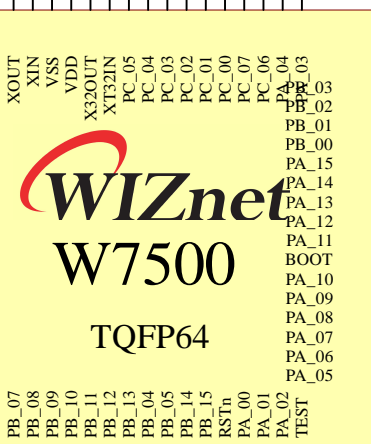


Optional_I2C_SCL0
Optional_I2C_SDA0
Optional_SPI_SSEL0
Optional_SPI_SCLK0
Optional_SPI_MISO0
Optional_SPI_MOSI0

PC_08 STATUS_LED0
PC_09 STATUS_LED1
PC_10 U_TXD2
PC_11 U_RXD2
PC_12 Expansion_GPIO_D
PC_13 Expansion_GPIO_C
PC_14 Expansion_GPIO_B
PC_15 Expansion_GPIO_A

CRS PD_00
RXDV PD_01
RXD0 PD_02
RXD1 PD_03
RXD2 PD_04
RXD3 PD_06

PC_08 XOUT
PC_09 XIN
PC_10 VSS
PC_11 X32OUT
PC_12 XT32IN
PC_13 PC_05
PC_14 PC_04
PC_15 PC_03
VSS PC_02
VDD PC_01
BOOT PC_00
BOOT PC_07
BOOT PC_06
BOOT PC_04
BOOT PC_03
BOOT PA_03
BOOT PA_02
BOOT PA_01
BOOT PA_00
BOOT PA_15
BOOT PA_14
BOOT PA_13
BOOT PA_12
BOOT PA_11
BOOT PA_10
BOOT PA_09
BOOT PA_08
BOOT PA_07
BOOT PA_06
BOOT PA_05



48 SPI_MOSI1
47 SPI_MISO1
46 SPI_SCLK1
45 SPI_SSEL1
44 U_RXD0
43 U_TXD0
42 U_RTS0
41 U_CTS0
40 BOOT
39 U_DTR0
38 U_DSR0
37 APP_BOOT
36 STATUS
35
34
33

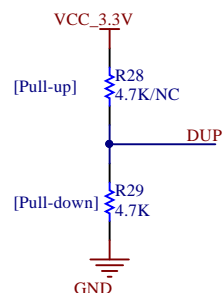
PB_03
PB_02
PB_01
PB_00
PA_14
PA_13
PA_12
PA_11
PA_10
PA_09
PA_08
PA_07

For Serial Flash Pins

with PHY_LINK Status function (output signal)
with HW_Trigger pin function (output signal)
TCP_Connection function

MDIO(PB_14),MDC(PB_15) pin is should be handled as GPIOs.

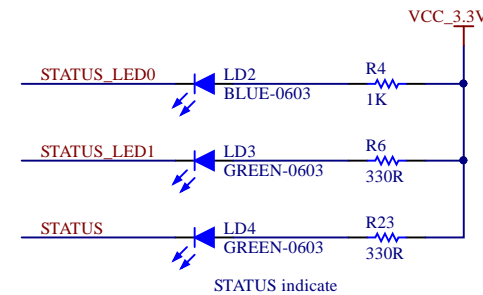
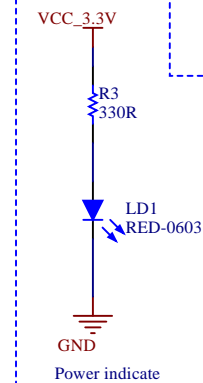
PA_00 is PHY LINK check pin(input). This pin checks PHY link from peer
This pin should be connected to the LINK status signal of the RJ-45.




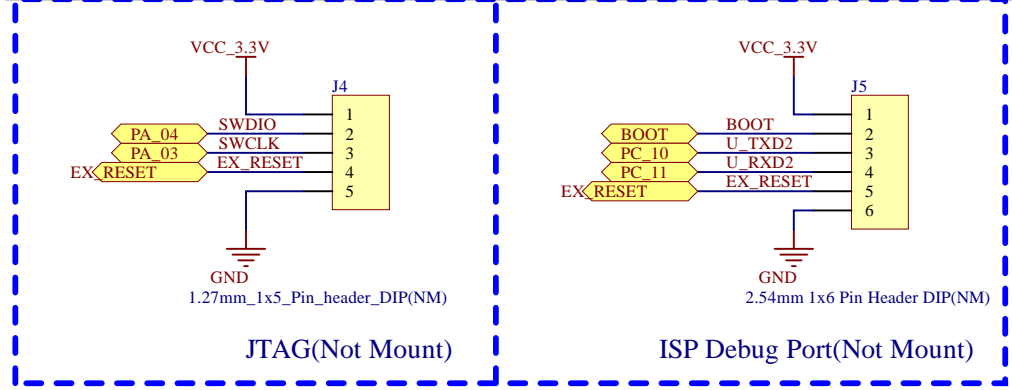
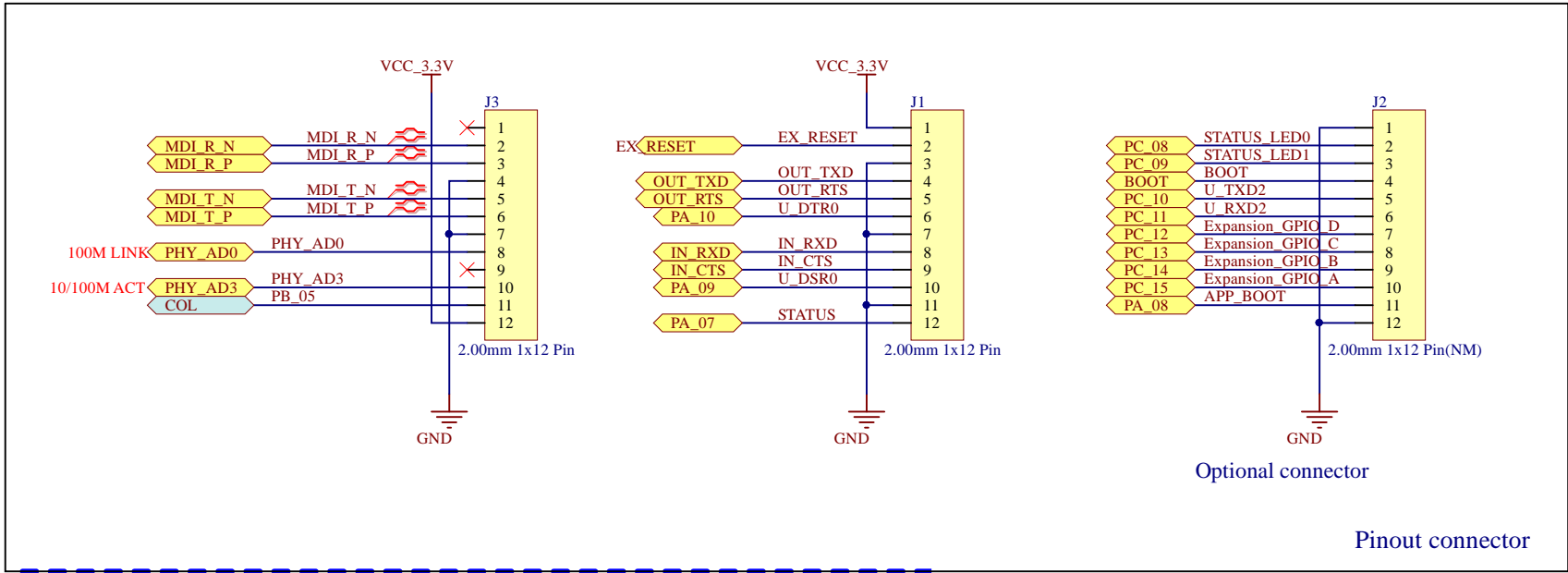
To use Half mode, you must set up pull up.
To use Full mode, you must set up pull down.(Default)

There is no pull-up or pull-down in the circuit, make sure W7500 internal register is set as below
1. Set as GPIO (Alternative function)
2. Set as Input pin. (IE in PADCON)
3. Set as Pull up or pull down. (PADCON)

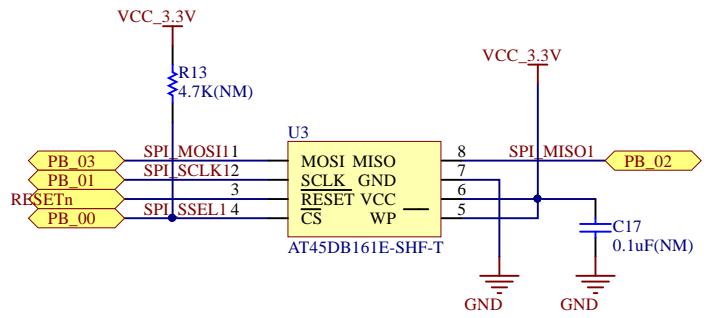
For more information, refer to the W7500 reference manual.



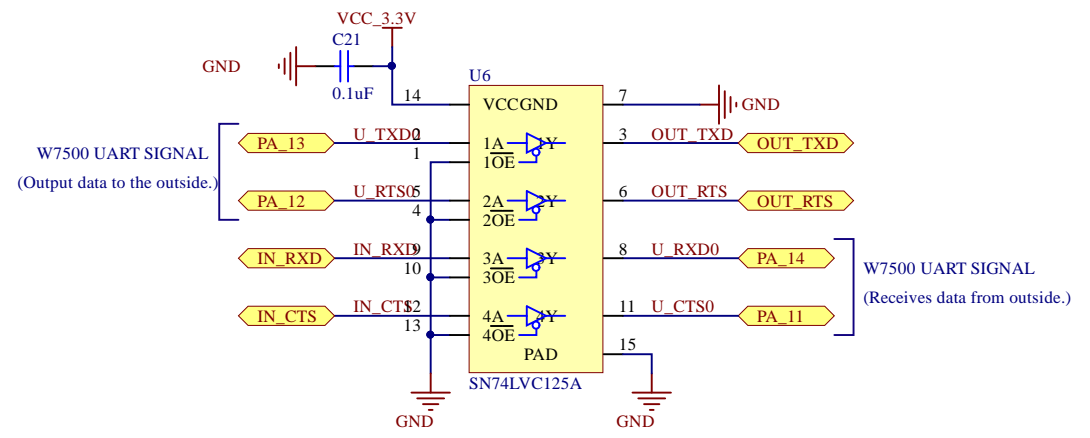
Title			WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea	
Size: A4	Number:	Revision:		
Date: 2017-09-11		Sheet1 of 3		
Team: Team Wiki		Drawn By: Edward		



This page bold dash line is NM(Not Mount)

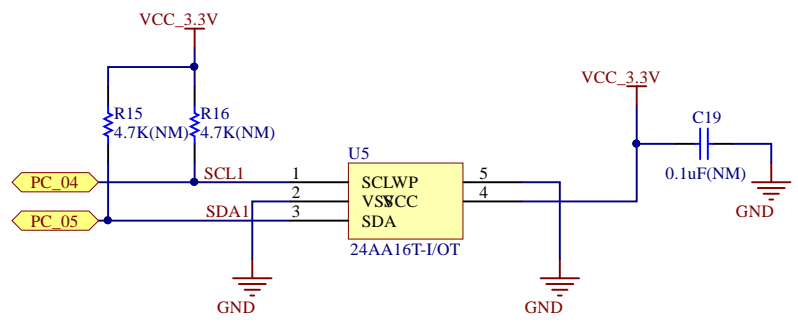


Serial Data Flash(Not Mount)

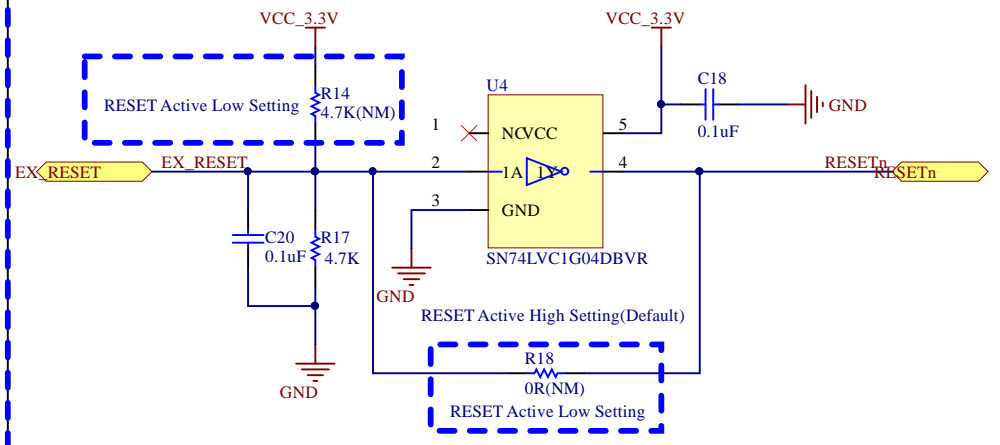


The buffer is used for impedance separation purposes.

UART CONVERSION



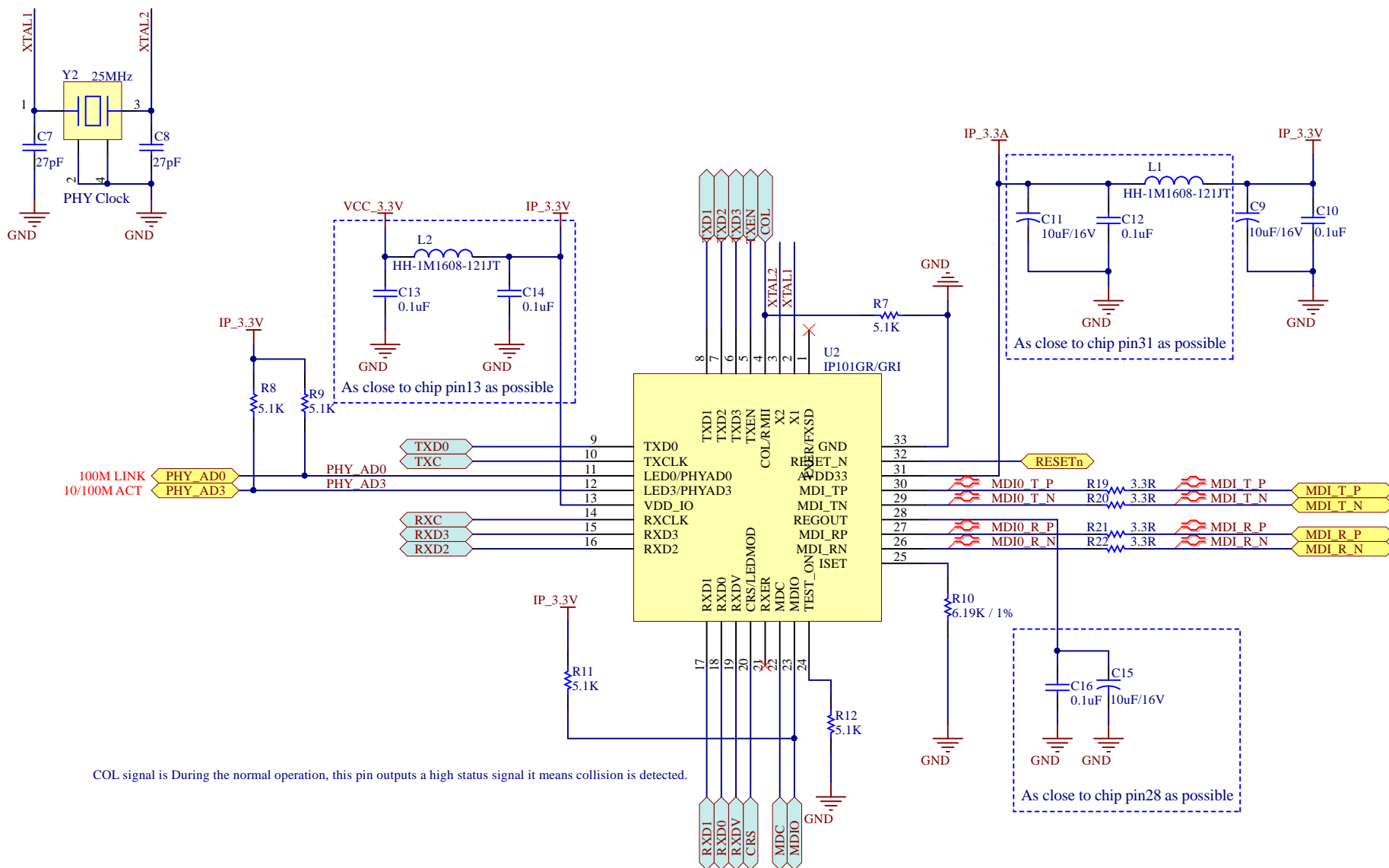
EEPROM(Not Mount)




Mode	U4	R14	R15	R17
Active High(Default)	Mount			Mount
Active Low		Mount	Mount	

RESET

This page bold dash line is NM(Not Mount)



COL signal is During the normal operation, this pin outputs a high status signal it means collision is detected.

Title			WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea	
Size: A4	Number:	Revision:		
Date: 2017-09-11		Sheet2 of 3		
Team: Team Wiki	Drawn By: Edward			