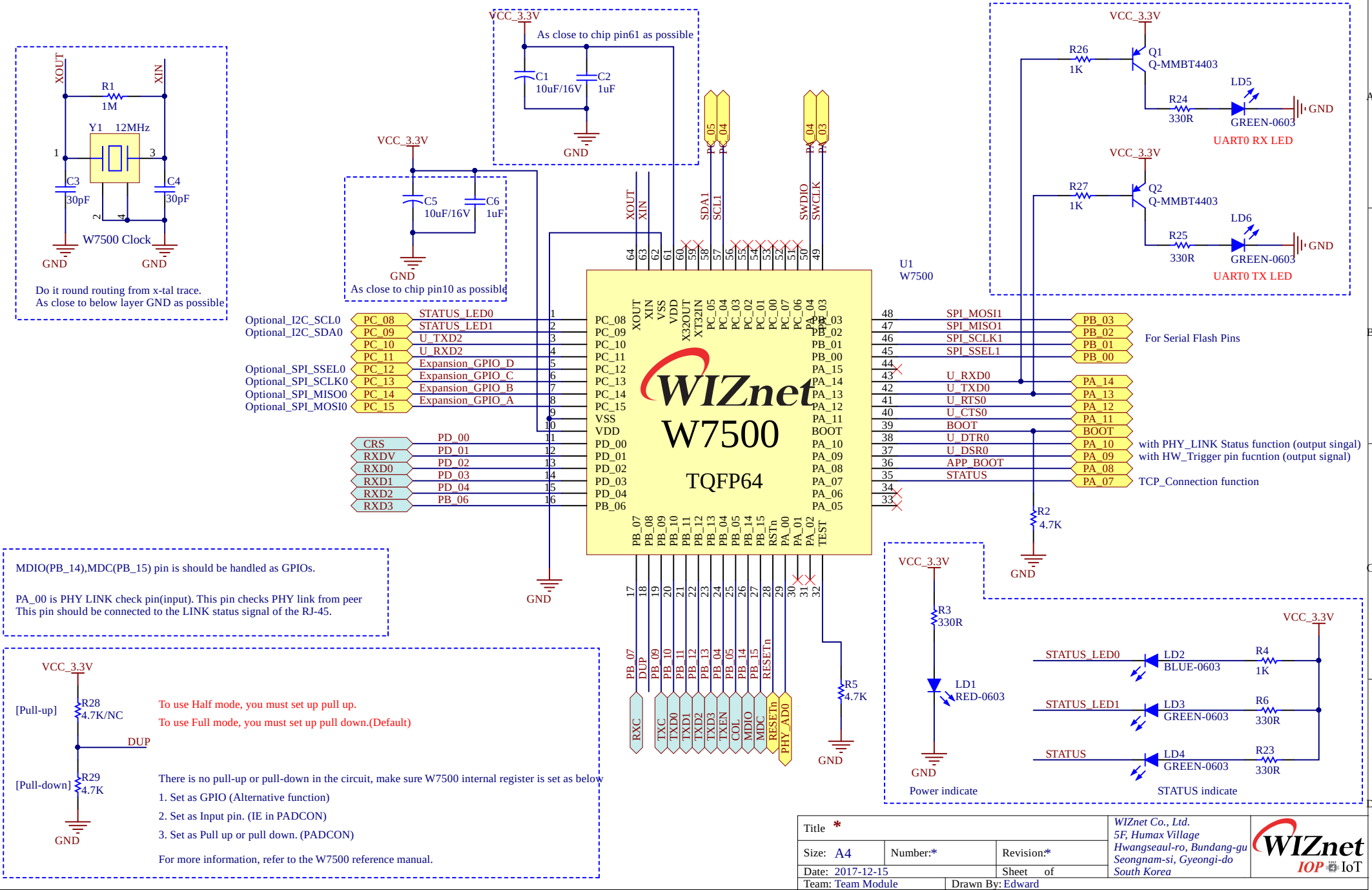
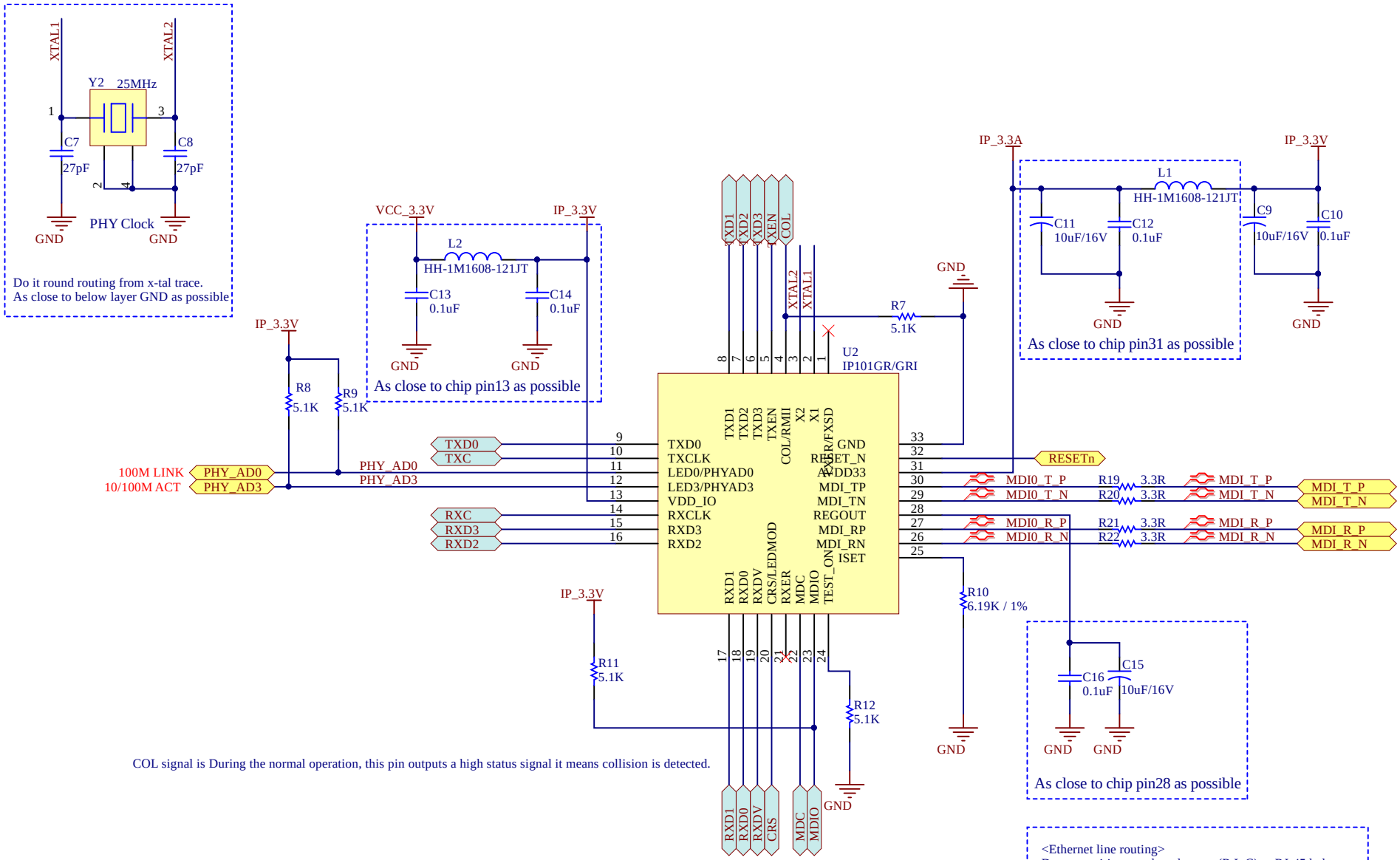
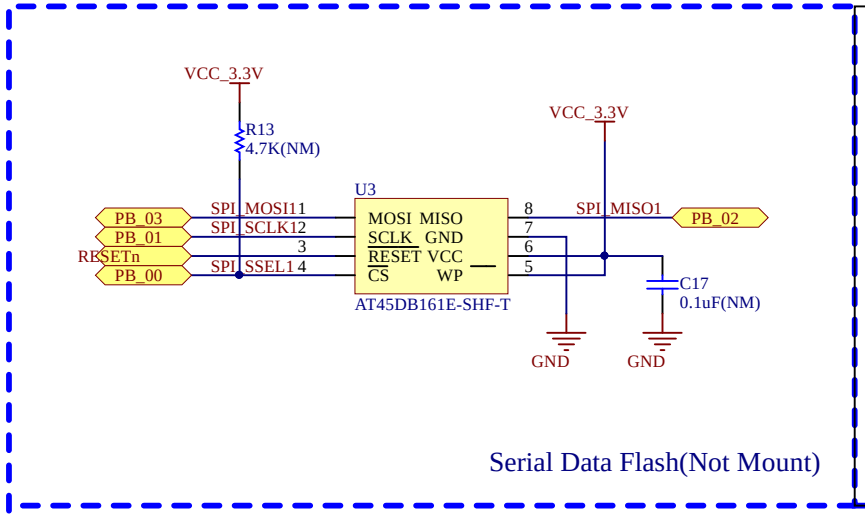
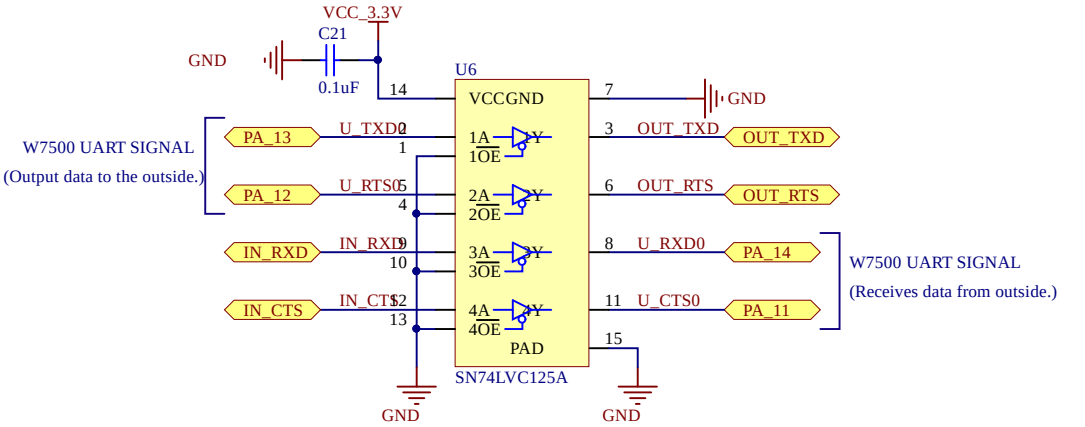
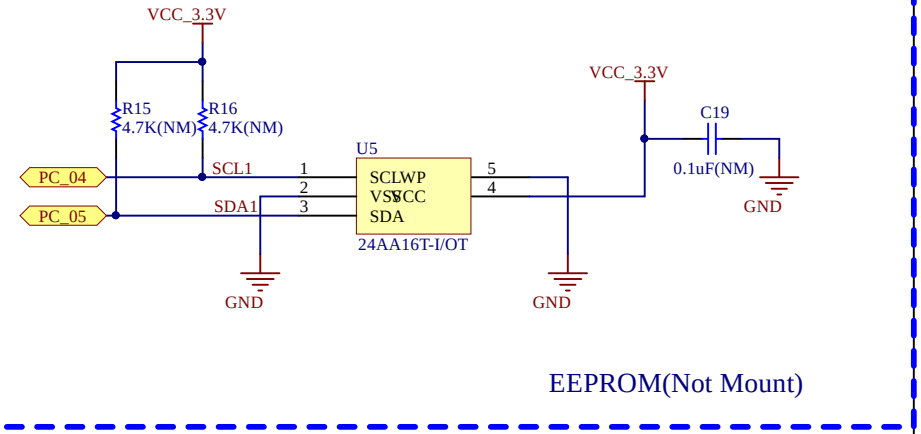

XOUT
XIN
R1
1M
Y1 12MHz
C3
30pF
C4
30pF
W7500 Clock
GND
Do it round routing from x-tal trace.
As close to below layer GND as possible
VCC_3.3V
As close to chip pin61 as possible
C1
10uF/16V
C2
1uF
GND
VCC_3.3V
C5
10uF/16V
C6
1uF
GND
As close to chip pin10 as possible
U1
W7500
WIZnet
W7500
TQFP64
Optional_I2C_SCL0
Optional_I2C_SDA0
Optional_SPI_SSEL0
Optional_SPI_SCLK0
Optional_SPI_MISO0
Optional_SPI_MOSI0
PC_08
PC_09
PC_10
PC_11
PC_12
PC_13
PC_14
PC_15
STATUS_LED0
STATUS_LED1
U_TXD2
U_RXD2
Expansion_GPIO_D
Expansion_GPIO_C
Expansion_GPIO_B
Expansion_GPIO_A
CRS
RXDV
RXD0
RXD1
RXD2
RXD3
PD_00
PD_01
PD_02
PD_03
PD_04
PB_06
SDA1
SCL1
SWDIO
SWCLK
SPI_MOSI1
SPI_MISO1
SPI_SCLK1
SPI_SSEL1
U_RXD0
U_TXD0
U_RTS0
U_CTS0
BOOT
U_DTR0
U_DSR0
APP_BOOT
STATUS
PB_03
PB_02
PB_01
PB_00
PA_14
PA_13
PA_12
PA_11
BOOT
PA_10
PA_09
PA_08
PA_07
For Serial Flash Pins
with PHY_LINK Status function (output singal)
with HW_Trigger pin fucntion (output signal)
TCP_Connection function
R2
4.7K
VCC_3.3V
R26
1K
Q1
Q-MMBT4403
LD5
R24
330R
GREEN-0603
GND
UART0 RX LED
VCC_3.3V
R27
1K
Q2
Q-MMBT4403
LD6
R25
330R
GREEN-0603
GND
UART0 TX LED
MDIO(PB_14),MDC(PB_15) pin is should be handled as GPIOs.
PA_00 is PHY LINK check pin(input). This pin checks PHY link from peer
This pin should be connected to the LINK status signal of the RJ-45.
VCC_3.3V
[Pull-up]
R28
4.7K/NC
DUP
[Pull-down]
R29
4.7K
GND
To use Half mode, you must set up pull up.
To use Full mode, you must set up pull down.(Default)
There is no pull-up or pull-down in the circuit, make sure W7500 internal register is set as below
1. Set as GPIO (Alternative function)
2. Set as Input pin. (IE in PADCON)
3. Set as Pull up or pull down. (PADCON)
For more information, refer to the W7500 reference manual.
RXC
TXC
TXD0
TXD1
TXD2
TXD3
TXEN
COL
MDIO
MDC
RESETn
PHY_AD0
R5
4.7K
GND
VCC_3.3V
R3
330R
LD1
RED-0603
GND
Power indicate
VCC_3.3V
STATUS_LED0
LD2
BLUE-0603
R4
1K
STATUS_LED1
LD3
GREEN-0603
R6
330R
STATUS
LD4
GREEN-0603
R23
330R
STATUS indicate
Title *
Size: A4
Number:*
Revision:*
Date: 2017-12-15
Sheet of
Team: Team Module
Drawn By: Edward
WIZnet Co., Ltd.
5F, Humax Village
Hwangseaul-ro, Bundang-gu
Seongnam-si, Gyeongi-do
South Korea
WIZnet
IOP IoT

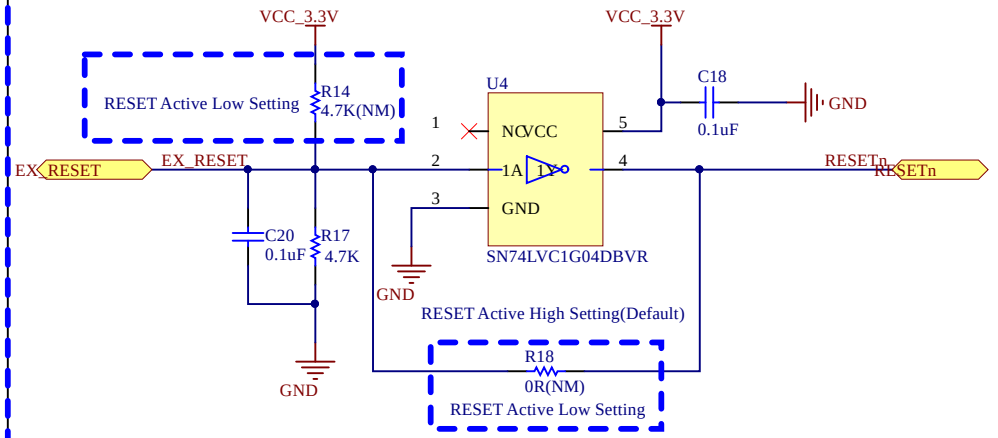


Title *			WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeongi-do South Korea
Size: A4	Number:*	Revision:*	
Date: 2017-12-15		Sheet of	
Team: Team Module	Drawn By: Edward		









Mode	U4	R14	R17	R18
Active High(Default)	Mount		Mount	
Active Low		Mount		Mount

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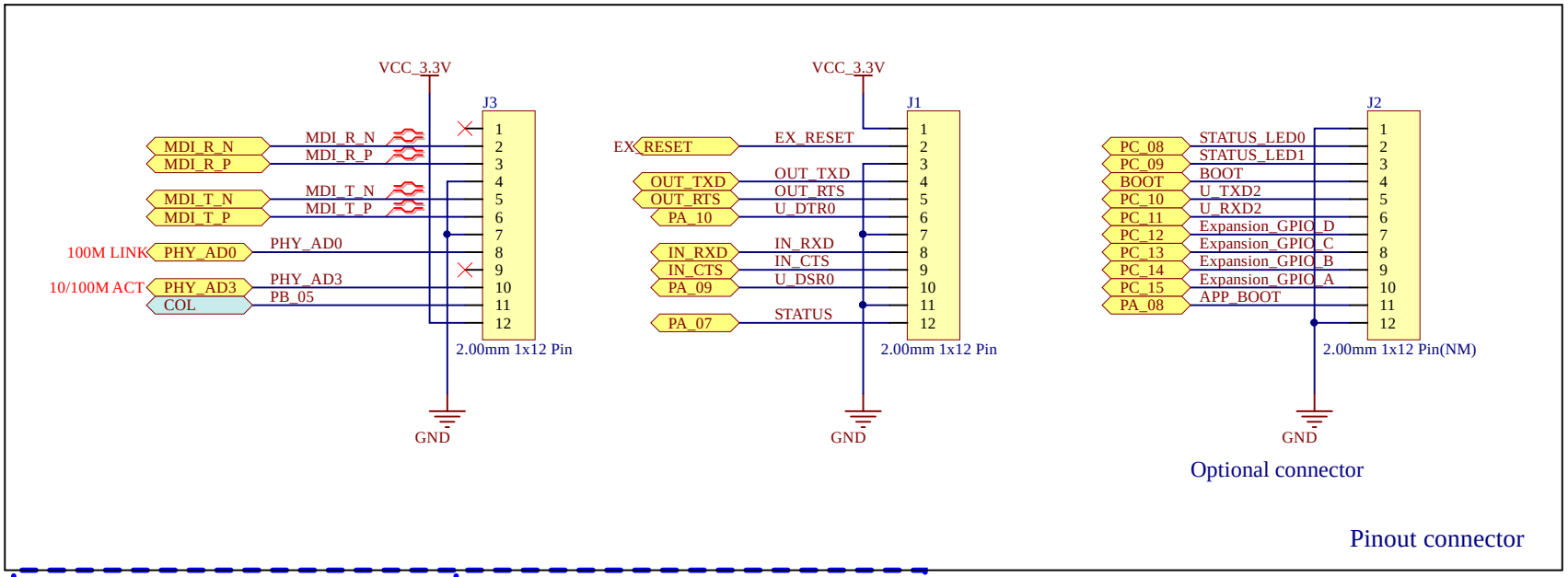

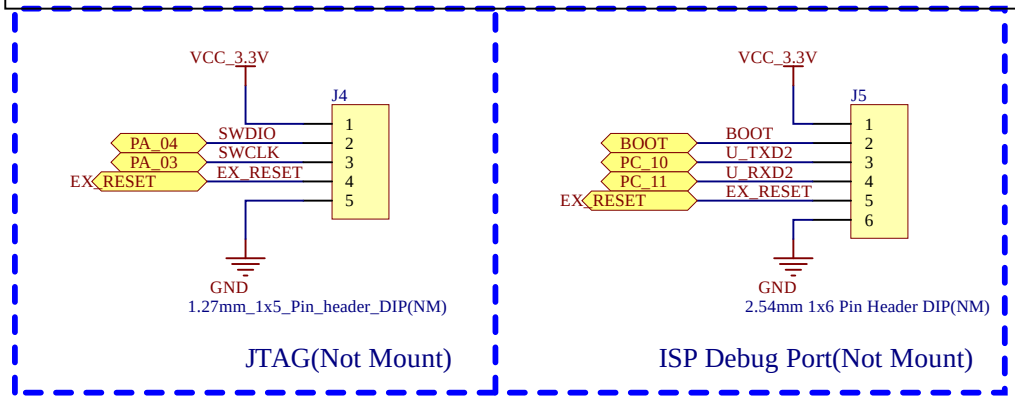

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