

# ILA: Correctness via Type Checking for Fully Homomorphic Encryption

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## Abstract

RLWE-based Fully Homomorphic Encryption (FHE) schemes add some small *noise* to the message during encryption. The noise accumulates with each homomorphic operation. When the noise exceeds a critical value, the FHE circuit produces an incorrect output. This makes developing FHE applications quite subtle, as one must closely track the noise to ensure correctness. However, existing libraries and compilers offer limited support to statically track the noise. Additionally, FHE circuits are also plagued by wraparound errors that are common in finite modulus arithmetic. These two limitations of existing compilers and libraries make FHE applications too difficult to develop with confidence.

In this work, we present a *correctness-oriented* IR, Intermediate Language for Arithmetic circuits, for type-checking circuits intended for homomorphic evaluation. Our IR is backed by a type system that tracks low-level quantitative bounds (e.g., ciphertext noise) without using the secret key. Using our type system, we identify and prove a strong *functional correctness* criterion for ILA circuits. Additionally, we have designed ILA to be maximally general: our core type system does not directly assume a particular FHE scheme, but instead axiomatizes a *model* of FHE. We instantiate this model with the exact FHE schemes (BGV, BFV and TFHE), and obtain functional correctness for free.

We implement a concrete type checker ILA, parameterized by the noise estimators for three popular FHE libraries (OpenFHE, SEAL and TFHE-rs). We also use the type checker to infer the optimal placement of modulus switching, a common noise management operation. Evaluation shows that ILA type checker is sound (always detects noise overflows), practical (noise estimates are tight) and efficient.

## 1 Introduction

Unlike traditional encryption schemes, a *fully homomorphic encryption* (FHE) enables computations on encrypted data. Popular FHE schemes—such as BGV [7], CKKS [9], TFHE [11]—that are based on Ring Learning With Errors (RLWE) adds some small *noise* to the message to make the encryption non-deterministic (and thus secure). This noise accumulates with each homomorphic operation. A crucial invariant that needs to be preserved for the successful decryption is that the noise must not exceed a *critical value*; otherwise, the decryption will not represent the accurate value. Moreover, noise overflows are attack vectors for secret key recovery attacks [19]. Noise management operations such as *modulus switching* and *programmable bootstrapping* (PBS) reduce noise but are computationally expensive, requiring judicious use. Additionally, all these schemes operate in finite rings (e.g.,  $\frac{\mathbb{Z}_q[x]}{x^d-1}$ ); a value overflow could potentially corrupt the output. Thus, FHE application developers need tools that guarantee correct evaluation.

Unfortunately, none of the existing FHE compilers [10, 14, 15, 18] or libraries—such as OpenFHE [5], SEAL [22], HELib [16], TFHE-rs [24], Lattigo [4]—guarantee correct FHE evaluation in the presence of overflows in noise or value. Indeed, to avoid incorrect outputs, they either rely on programmer expertise or require one to simulate many runs of the FHE circuit, which carries a high computational cost and thus harms iterative development. Additionally, *no* realistic FHE library to date can simultaneously reason about overflows in both RLWE-induced noise and overflows in the underlying encrypted values.

Thus, none of the existing libraries offer a generic yet robust correctness reasoning framework that statically and automatically detects overflows.

In response, we present ILA, a *correctness-oriented* intermediate representation (IR) for *statically typechecking* a *family* of RLWE-based circuits intended for homomorphic evaluation. ILA is backed by a type system that tracks low-level quantitative bounds (e.g., ciphertext noise, value ranges) without using the secret key. Using our type system, we identify and prove a strong *functional correctness* criterion for ILA circuits: if  $C$  is well-typed, then homomorphically evaluating  $C$ , and then decrypting, is equivalent to running a cleartext version  $C_{\text{clear}}$  on its decrypted inputs. In essence, this correctness criterion guarantees that FHE does not introduce any additional functional bugs, relative to the original, non-homomorphic circuit.

A crucial insight for ILA is that RLWE-based FHE schemes share the same high-level characteristics for homomorphic and noise management operations. Thus, we have designed ILA to be maximally general: our core type system does not directly assume a particular FHE scheme, but instead axiomatizes a *model* of FHE. Thus, ILA represents a family of FHE models. In turn, we are able to instantiate this model with the widely used non-approximate schemes: BGV, BFV and TFHE.

Importantly, all ILA model instantiations inherit functional correctness for free. This is notable as the correctness guarantees can be used to validate noise-preserving transformations. For instance, ILA’s BGV instantiation validates an optimization involving modulus switching. Similarly, ILA’s TFHE instantiation decouples standard homomorphic and PBS operations; this enables deploying faster but correct TFHE circuits that are PBS-free.

We do not intend ILA to be used directly, but instead to provide a layer of correctness validation on top of an existing pipeline for FHE. To demonstrate this use case, we implement an extensible ILA compiler with static type checkers parameterized by the scheme. ILA compiler supports multiple FHE backends (e.g., SEAL, OpenFHE and TFHE-rs) for BGV, BFV and TFHE schemes. Using the type checker, one can obtain, for free, an FHE circuit that is formally verified for functional correctness.

Our evaluation shows that the overflow analysis employed in the type checker is sound (no false negatives) and practical (fewer false

positives). More importantly, the entire analysis is static: neither a secret key nor circuit execution is required. This brings down the application debugging costs by enabling offline development.

Finally, we present a novel insight on the placement of *modulus switching* (MS), a common noise managing optimization for FHE circuits. Developers use MS to improve the multiplicative depth (the longest sequence of ciphertext multiplication) of the circuit. However, one cannot simply annotate the entire circuit with MS operations as they are expensive. Moreover, incorrect placement could counteract any potential multiplicative depth gains; worse the circuit may even fail to run as modulus-switched ciphertexts can only operate with equivalently switched ciphertexts.

Our MS inference procedure is guided by the type checker: it not only picks placement that maximizes the remaining noise budget but also validates the correctness of the resulting circuit.

**Contributions.** In summary, we:

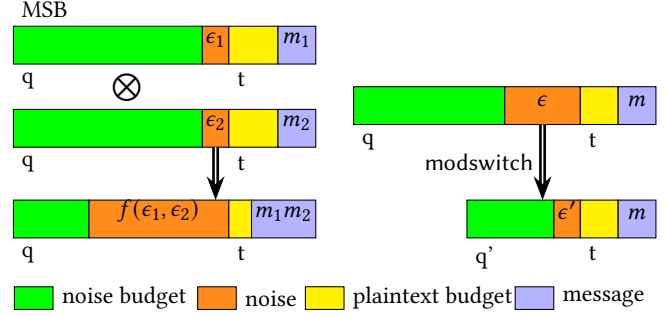
- present ILA, a generic and formal intermediate language and type system that embeds the notion of overflow(s) for FHE circuits in a scheme-agnostic manner;
- formally specify and prove a (functional) correctness theorem for ILA, which guarantees that well-typed circuits evaluate correctly;
- instantiate ILA with BGV, BFV and TFHE, the three popular absolute schemes, demonstrating the generality of ILA. Notably, TFHE operations are decoupled from the expensive programmable bootstrapping enabling users to deploy not only functionally correct but also runtime-efficient FHE circuits;
- transformation validation of our novel modulus switching inference algorithm;
- implement ILA compiler targeting three backends and a static type checker that detects both noise and value overflows for all the three schemes; importantly, the type checker does not require a secret key to estimate the noise in the ciphertext nor does it run the expensive FHE circuit; and
- demonstrates that ILA is both expressive and effective with tight noise bounds; ILA’s static approach at detecting overflows outperforms the state-of-the-art dynamic approach by orders of magnitude.

We make our implementation and evaluation case studies available as an anonymous GitHub repository<sup>1</sup>.

## 2 Background

A Ring Learning With Errors-based encryption scheme contains three steps: First, a probabilistic keygen algorithm that on input  $\lambda$ , generates a secret key  $sk$  and a public key  $pk$ . Second, a probabilistic encryption algorithm generates a ciphertext for a given  $pk$  and a message  $m$  taken from the message space  $\text{msg}$ . The encryption can be further seen as a two-step process, the first step encodes a message  $m$  to an element  $p$  of the *plaintext* space. The second step encrypts the plaintext  $p$  and adds a random noise for security generating an element  $ct$  of the *ciphertext* space. Finally, a deterministic decryption algorithm takes a ciphertext  $ct$  and  $sk$  and emits a plaintext message  $p$  followed by a decoding step that emits the corresponding message  $m$ .

<sup>1</sup><https://github.com/anon-ila/ila>



**Figure 1: Noise changes during binary and unary homomorphic operations. The noise grows multiplicatively with ciphertext multiplication (left). Modulus switching reduces both noise and the remaining noise budget (right).**

In an RLWE-based scheme, the plaintext space is a ring. The plaintext space encodes a message (or cleartext) space. The message space is usually  $\mathbb{Z}_t$  (the ring of integers modulo  $t$ ) or  $\mathbb{Z}_t^d$  ( $d$  tuples of  $\mathbb{Z}_t$ ). The plaintext space is usually the ring  $R_t = \frac{\mathbb{Z}_t[x]}{(x^d+1)}$ . The *plaintext modulus*  $t$  is a positive integer and  $d$  is usually a power of 2. Elements of this ring may be seen as polynomials of degree up to  $d-1$  with coefficients in  $\mathbb{Z}_t$ . Addition and multiplication are usual polynomials addition and multiplication modulo  $(t, (x^d+1))$ . The ciphertext space is  $R_q^{n+1}$ , where the *coefficient modulus*  $q$  is a large positive integer. Elements of this space are  $n+1$  tuples with each component a polynomial from  $R_q$ . For simplicity, the reader can assume  $n=1$ .

**Absolute RLWE FHE Schemes.** BGV, BFV and TFHE are absolute RLWE FHE schemes: the decrypted output of an encrypted computation is the same as the computation on the underlying message. By contrast, CKKS is an approximate scheme: the decrypted output contains a small error. In this work, we focus on absolute schemes. All of them operate on integers and support (binary) arithmetic homomorphic operations,  $\otimes$  and  $\oplus$  for homomorphic multiplication and addition, respectively, and  $\times$  for scalar multiplication of a ciphertext. BGV also supports modswitch, a modulus switching (unary) operation that reduces the noise in a ciphertext. TFHE supports three different types of ciphertexts—LWE, RLWE and RGSW—and different multiplication operators— $\otimes$ ,  $\boxtimes$ , and  $\boxdot$ —indexed by the ciphertext type. This makes the scheme more challenging.

**Noise changes.** Homomorphic operations change the ciphertext noise. Figure 1 illustrates the noise changes for multiplication and modulus switching. A ciphertext is represented as a polynomial. The length (number of bits) of each coefficient is given by coefficient modulus  $q$ . The message  $m_1$  can grow until the size of the plaintext modulus is given by  $t$  bits. Every ciphertext has a current noise  $\epsilon_1$  that can grow until  $(q-t)$  bits. If it grows beyond, then an overflow occurs.

In a ciphertext multiplication, the noise growth is proportional to the product of noises in the individual ciphertexts. Modulus switching refreshes the noise—it reduces the size of the coefficient modulus by switching to a different level. In doing so, it reduces

both the noise and noise budget leaving the message size and plaintext modulus unchanged. However, it reduces noise more than the noise budget. This relative gain in the noise budget enables more homomorphic operations.

*Programmable Bootstrapping (PBS).* Like modulus switching, PBS is also a noise management operation, however, it not only refreshes more noise budget but is also computationally more expensive. TFHE uses PBS to apply a lookup table (LUT) to LWE ciphertext(s). Technically, it is a composition of modulus switching, blind rotation and sample extraction operations. For PBS to succeed, there should be a non-zero noise budget. Since multiplication is expensive, TFHE tightly couples it with PBS. For other operations, PBS is added after a fixed number of operations.

### 3 Overview

ILA is a family of intermediate languages for developing FHE applications. At a high level, ILA is a standard imperative language with abstract homomorphic operations and types parameterized by the FHE scheme. Our key insight is driven by the observation that all RLWE-based FHE schemes share common characteristics such as the scheme parameters selection, cipher and plaintext construction as well as the propagation of noise through the circuit. As a part of parameter selection, all schemes choose a set of keys (public, private, and evaluation), coefficient modulus  $q$ , plaintext modulus  $t$  and a poly modulus degree  $d$ . Ciphertext and plaintexts are constructed as elements of finite rings involving the above chosen parameters.

The core language provides a common semantic framework for statically reasoning about the correctness of RLWE-based FHE schemes. Each scheme instantiates ILA with scheme-specific operations. For example, arithmetic operations on ciphertexts are common in all schemes, whereas noise management operations are only selectively supported. Both BGV and TFHE support modulus switching but only TFHE supports PBS. As described in Section 2, TFHE also supports multiple operations on different types of ciphertexts having distinct properties.

For an instantiation to be valid, it has to meet certain requirements (described in Section 4). A valid instantiation inherits, for free, all the correctness guaranteed offered by ILA. In Section 5, we describe  $\text{ILA}_{(bgv)}$ ,  $\text{ILA}_{(bfv)}$  and  $\text{ILA}_{(tfhe)}$ , instantiations for BGV, BFV and TFHE schemes, respectively. Furthermore, we prove that they are valid.

Figure 2 shows the implementation of private set intersection (PSI) in ILA [8]. The goal of PSI is to find if the intersection between two encrypted sets, A and B, (owned by different parties but encrypted using the public key owned by one of the parties) is non-empty. This is computed by subtracting each element of A from each element of B, and accumulating the difference in result. However, a naive implementation will be insecure as the decrypted result might leak more information about the other set. In the secure version, the computations are mixed with randomized data.

In the program, sets A and B are represented as vectors;  $A[i]$  denotes the element at index  $i$ . Both A and B are initialized encrypted vectors (e.g.,  $A = \text{cipher\_init}[1, 3, 36]$ ). Elements of A and B are iterated using while loops and the subtraction is computed using the combination of multiplication and addition. PSI is non-empty if the

```

1 # Private Set Intersection between encrypted sets A and B
2 result = 1
3 n_unit = -1
4
5 for j = len_A downto 0: # begin outer loop
6     t1 = B[j]           # index into set B
7     t2 = n_unit ⊗ t1     # compute -B[j]
8     t3 = random_data[j]
9     t4 = result ⊗ t3
10    i = len_B
11    while i = len_B downto 0: # begin inner loop
12        t5 = A[i]         # index into set A
13        t6 = t5 ⊕ t2       # t6 = A[i] - B[j]
14        t7 = result ⊗ t6
15        result = t7 ⊗ t4 # 0 if PSI is non-empty

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**Figure 2: ILA program for private set intersection (PSI) over encrypted sets A and B.. The sets are represented as vectors; operators  $\oplus$  and  $\otimes$  represent homomorphic addition and multiplication.**

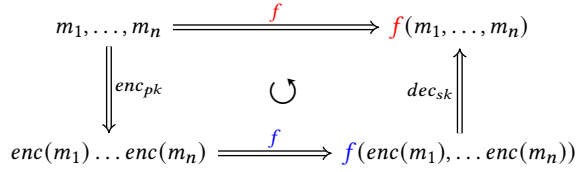
final value of the decrypted result is zero. Note that the operations on A and B in lines 9 and 13-15 are homomorphic:  $\otimes$  and  $\oplus$  correspond to homomorphic multiplication and addition, respectively. Each homomorphic operation adds noise, and thus the noise gets accumulated in the result. The amount of noise is dependent on the type as well as the number of homomorphic operations: multiplication adds more noise than any other operations. If a ciphertext undergoes a sequence of multiplications, measured in terms of *multiplicative depth*, then it quickly exhausts the noise budget leading to incorrect output due to the noise overflow. Additionally, each homomorphic operation could potentially increase the message size; if this increases beyond the threshold value (plaintext modulus chosen during the initial setup), then the message wraps around leading to a value overflow.

Thus, even though the PSI program is logically correct, the output is not guaranteed to be valid for all inputs. Crucially, it depends on the depth of the circuit (in this case, loop iterations determined by  $\text{len}_A$  and  $\text{len}_B$ ) and the actual inputs. For example, in SEAL, it fails to execute correctly for larger sets (e.g.,  $\text{len}_A$  is greater than 4) due to noise overflows. In OpenFHE and TFHE-rs, it fails when the input values are large due to value overflows. It is thus important to know when the output is valid.

ILA's approach is to statically track the overflows using an expressive type system. Notably, the type system embeds an abstract notion of bounds for the type of polynomials used to in the construction of ciphertext and plaintexts. It tracks the effect of homomorphic operations on these bounds. The type system constrains the overflows by ensuring that they are always below the threshold values (chosen during the setup). For example, if the product of two ciphertexts with types cipher  $\alpha_{\text{cipher}}^1$  and cipher  $\alpha_{\text{cipher}}^2$  has the type cipher  $\alpha_{\text{cipher}}$ , then the type system computes that  $\alpha_{\text{cipher}} = f(\alpha_{\text{cipher}}^1, \alpha_{\text{cipher}}^2)$  and checks if  $\alpha_{\text{cipher}}$  is less than the threshold values.

Since ILA is parametric in the FHE scheme, each scheme instantiates the abstract bounds with precise information. For example,  $\text{ILA}_{(bgv)}$  instantiates the ciphertext type cipher  $\alpha_{\text{cipher}}$  with cipher  $(\text{inf}, \text{sup}, \epsilon, \omega)$ : intuitively, the ciphertext at level  $\omega$  has the current noise  $\epsilon$  and the underlying messages lies in the interval





**Figure 3: Functional correctness via commutativity in ILA.** *Evaluation on top (in red) is over cleartext messages, while evaluation on bottom (in blue) is homomorphic over ciphertexts.*

$[\inf, \sup]$ . Multiplying two ciphertexts with bounds cipher  $\langle \inf_i, \sup_i, \epsilon_i, \omega \rangle$  yields the new bound cipher  $\langle \inf, \sup, \epsilon, \omega \rangle$  where  $\epsilon = f(\epsilon_1, \epsilon_2)$  and  $[\inf, \sup] = [\min(\inf_1, \inf_2), \max(\sup_1, \sup_2)]$ . Our formalism leaves  $f$  as an abstract function with the only requirement that it is monotonic. This enables one to instantiate ILA in multiple ways by picking the right noise estimator from the existing literature [11–13, 17]. Note that our work itself does not introduce any new noise estimation technique; rather our plug-and-play estimator framework is conducive to user defined estimations. For example,  $f(\epsilon_1, \epsilon_2) = \epsilon_1 \times \epsilon_2$  is a worst-case estimator. We discuss noise estimators later in Section 7.

**Functional correctness.** Suppose the above program passes the type checker, and the circuit executes. Also suppose that the underlying messages, that is,  $\text{decrypt}_{sk}(A)$  and  $\text{decrypt}_{sk}(B)$  are  $[m_1^A, m]$  and  $[m, m_2^B]$ , respectively. If the type checker is sound, then we should obtain that  $\text{decrypt}_{sk}(\text{result}) = 0$  (since intersection is non-empty).

Generalizing the above example, we formalize the functional correctness as a relational property of a pair of executions. The first execution runs the FHE circuit as intended, whereas the second execution runs the *same* circuit in *cleartext*, with plaintext inputs that correspond to the ciphertext ones. We then require that the two executions must match; that is, the first output decrypts to the second one. In other words, the diagram commutes, as shown in Figure 3. Theorem 4.4 from Section 4 states that well-typed ILA programs are functionally correct (given that the underlying FHE operations are, with appropriate noise bounds).

**Noise Management.** Going back to the running example, if ILA’s type checker rejects the program, we cannot guarantee that result will decrypt to 0. Indeed, the program fails to typecheck in BGV scheme if  $\text{len}_A$  is 4. In this case, one has to use modswitch, a noise refreshing operation (or PBS in TFHE), to gain additional noise budget. Using the noise budget estimates provided by ILA, line 9 could be modified to  $t4 = \text{result} \otimes t4$  to gain additional noise budget. If it still fails, depending on the amount of the noise budget either the following operations in lines 13–15 could be switched or line 9 could be further switched to a lower level in the modulus chain. This process repeats until the type checker accepts or the chain gets fully exhausted. In the latter case, the circuit runs without functional correctness guarantees.

The above iterative process shows that ILA’s type checker could be used as a layer for validating noise refreshing transformations. In Section 6, we describe an inference algorithm that exploits this property to help with the optimal placement of modswitch operations in loop-free programs. More generally, ILA provides a

validation layer for bounds-preserving transformations. In the future, we plan to use the validation layer as a foundation for building correct-by-construction circuit optimizations.

**Formal ILA Model.** To summarize, we define *ILA model* using an abstract core language. Values in the model are categorized into three sorts—message, encoded plaintext, and ciphertext—and each sort is associated with a corresponding set of abstract bounds, such as ciphertext noise and value intervals. Crucially, the bounds set forms a partial order, and operations over values manipulate these bounds. When the bounds exceed the scheme-specific special elements of the poset, the circuit fails.

We prove that the abstract FHE model enforces functional correctness. Interestingly, instantiating the abstract model with a suitable FHE scheme gives functional correctness for free. This is significant, as any correct instantiation of ILA now enjoys the functional correctness property. Concretely, we instantiate ILA with absolute FHE schemes to obtain  $\text{ILA}_{(bgv)}$ ,  $\text{ILA}_{(bfv)}$  and  $\text{ILA}_{(tfhe)}$  and furthermore prove that they are correct instantiations of ILA. Thus programs in instantiated models inherit the functional correctness property. This demonstrates the model’s general applicability to RLWE-based FHE schemes.

## 4 ILA: A formal model for FHE

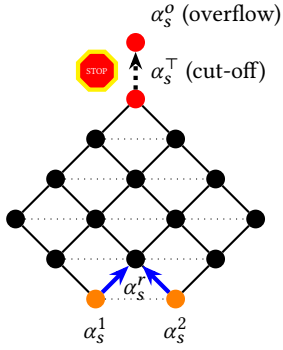
We now present ILA, our core type system for FHE. ILA is parameterized by a *model*, which encapsulates all necessary semantic details for homomorphic evaluation. Given an arbitrary model, we then construct a type system for homomorphic circuits and prove that all circuits constructible under our type system are functionally equivalent to their non-homomorphic counterparts.

### 4.1 ILA Models

To use ILA with a particular FHE scheme (e.g., BGV) one needs to define a *model*, which serves as the semantic interface between the FHE scheme and ILA’s type system.

**DEFINITION 4.1.** An ILA model  $M$  is given by the following data:

- Two sets  $\mathcal{PP}$  and  $\mathcal{SP}$  for public and secret parameters, respectively, with a mapping  $\text{topub} : \mathcal{SP} \rightarrow \mathcal{PP}$ ;
- Sets  $\llbracket s \rrbracket$  for each sort  $s \in \mathcal{S} = \{\text{msg}, \text{plain}, \text{cipher}\}$  (meaning messages, (encoded) plaintexts, and ciphertexts, respectively);
- Sets  $B_s$  of bounds for each sort  $s \in \mathcal{S}$ , partially ordered by  $\leq_s$ ;
- Mappings  $|\cdot|_{\text{msg}}^{\text{pp}} : \llbracket \text{msg} \rrbracket \rightarrow B_{\text{msg}}$  and  $|\cdot|_{\text{plain}}^{\text{pp}} : \llbracket \text{plain} \rrbracket \rightarrow B_{\text{plain}}$ , where  $\text{pp} \in \mathcal{PP}$ ;
- A mapping  $|\cdot|_{\text{cipher}}^{\text{sp}} : \llbracket \text{cipher} \rrbracket \rightarrow B_{\text{cipher}}$ , where  $\text{sp} \in \mathcal{SP}$ ;
- A mapping  $\text{interp}_{\text{plain}}^{\text{pp}} : \llbracket \text{plain} \rrbracket \rightarrow \llbracket \text{msg} \rrbracket$ , where  $\text{pp} \in \mathcal{PP}$ ;
- A mapping  $\text{interp}_{\text{cipher}}^{\text{sp}} : \llbracket \text{cipher} \rrbracket \rightarrow \llbracket \text{msg} \rrbracket$ , where  $\text{sp} \in \mathcal{SP}$ ;
- A set of operations  $\text{op}$  of arity  $\vec{s}_i \rightarrow s$ , where each  $s, s_i \in \mathcal{S}$ , equipped with three mappings:
  - $\llbracket \text{op} \rrbracket : \vec{\llbracket s_i \rrbracket} \rightarrow \llbracket s \rrbracket$ ;
  - A partial mapping  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}} : \vec{B_{s_i}} \rightarrow B_s$ , where  $\text{pp} \in \mathcal{PP}$ ;
  - A mapping on messages  $\llbracket \text{op} \rrbracket_{\text{msg}} : \vec{\llbracket \text{msg} \rrbracket} \rightarrow \llbracket \text{msg} \rrbracket$ , with the appropriate input arity.



**Figure 4: A partially-ordered bounds set  $B_s$  (indexed by sort  $s$ ). For any given two points  $\alpha_s^1$  and  $\alpha_s^2$ , the operator bounds mapping  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}$  computes the result bounds  $\alpha_s^r$ . The operation is well-defined iff  $\alpha_s^r \leq \alpha_s^T$  where  $\alpha_s^T$  is the cut-off for the output to be valid.**

We now walk through Definition 4.1. Each ILA model has a set of public and secret parameters, which refer directly to the public and secret keys of the FHE scheme in question. Then, each ILA model defines a set of messages, encoded plaintexts, and ciphertexts, captured through giving semantics  $\llbracket \cdot \rrbracket$  to the three sorts  $\mathcal{S} = \{\text{msg}, \text{plain}, \text{cipher}\}$ , respectively. To model quantitative concerns (e.g., ciphertext noise and integer overflow in the plaintext modulus), each sort  $s$  also comes equipped with a partially ordered set of *bounds*. For RLWE-based schemes, the bounds for messages and encoded plaintexts will track integer norms to prevent overflow in the modulus, while the bounds for ciphertexts will additionally track encryption-induced noise. We assume bounds for messages and encoded plaintexts can be computed using the public key via  $|\cdot|_{\text{msg}}^{\text{pp}}$  and  $|\cdot|_{\text{plain}}^{\text{pp}}$ , while computing bounds for ciphertexts via  $|\cdot|_{\text{cipher}}^{\text{sp}}$  needs the secret key.

Next, we have *interpretation* functions  $\text{interp}_{\text{plain}}^{\text{pp}}$  and  $\text{interp}_{\text{cipher}}^{\text{sp}}$ , mapping their respective sorts to messages. The interpretation function for plain takes the public parameters as an argument, while the interpretation function for cipher takes the secret parameters. Interpreting encoded plaintexts corresponds to *decoding*, and requires the public parameters, while interpreting ciphertexts corresponds to *decrypting*, and requires the secret parameters. For notational convenience, we define  $\text{interp}_{\text{msg}}^{\text{pp}}$  to be the identity function, and for  $s \in \{\text{msg}, \text{plain}\}$ , we define  $\text{interp}_s^{\text{sp}}$  to be  $\text{interp}_s^{\text{topub}(\text{sp})}$ . We omit the sort argument to  $|\cdot|$  and  $\text{interp}$  when it is clear from context.

Finally, we have a set of operations of varying arity, such as binary homomorphic addition and multiplication, operations on messages/encoded plaintexts, and unary *noise management* operations, such as modulus switching. Other than its native semantics  $\llbracket \text{op} \rrbracket$ , each operation comes equipped with a partial operation  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}$ , operating only on *bounds*, and a corresponding *message-level* operation  $\llbracket \text{op} \rrbracket_{\text{msg}}$ . The operation  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}$  is partial, so that it may throw an error of the estimated bound (e.g., ciphertext noise) is too large to carry out the operation. Figure 4 illustrates the operation on bounds. The message-level operation  $\llbracket \text{op} \rrbracket_{\text{msg}}$  emulates

Expressions  $e ::= x \mid v \in \llbracket s \rrbracket \ (s \in \{\text{msg}, \text{plain}\}) \mid \text{op}(\vec{e}_i)$   
 Commands  $c ::= \text{skip} \mid x := e \mid c; c \mid \text{if } e \text{ then } c \text{ else } c$

**Figure 5: ILA Syntax**

the operator’s intended semantics after interpretation. For example, the message-level operation for homomorphic multiplication is multiplication on messages, while the message-level operations for unary noise management operations on ciphertexts are the identity.

*Validity of Models.* A plain ILA model given by Definition 4.1 specifies the types of all operations, but does not encode whether the FHE scheme in question is *correct*; for example, whether homomorphic addition overflows given appropriate inputs. To encode correctness conditions for FHE, we require that the ILA model is additionally *valid*:

- **Commutativity:** for all secret parameters  $\text{sp}$ , every  $\text{op} : \vec{s}_i \rightarrow s$  and arguments  $v_i \in \llbracket s_i \rrbracket$ , we have that if  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(|v_i|_{s_i}^{\text{sp}}) = b$ , then  $\llbracket \text{op} \rrbracket(\vec{v}_i)|_s^{\text{sp}} \leq_s b$  and

$$\text{interp}_s^{\text{sp}}(\llbracket \text{op} \rrbracket(\vec{v}_i)) = \llbracket \text{op} \rrbracket_{\text{msg}}(\text{interp}_{s_i}^{\text{sp}}(v_i));$$

- **Downwards Closed:** If  $\text{op} : \vec{s}_i \rightarrow s$ , and if  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\vec{b}_i)$  is defined and equals  $b$ , then if  $b'_i \leq_{s_i} b_i$  for all  $i$ , we have that  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\vec{b}'_i) = b'$  with  $b' \leq_s b$ .

The first condition, commutativity, guarantees correctness by requiring that if the bounds chosen by operator  $\text{op}$  are defined and equals  $b$ , then the output of  $\llbracket \text{op} \rrbracket(\vec{v}_i)$  is equally bounded by  $b$ , and the message-level result of  $\llbracket \text{op} \rrbracket(\vec{v}_i)$  given by  $\text{interp}_s^{\text{sp}}$  is equal to the value of  $\llbracket \text{op} \rrbracket_{\text{msg}}$  evaluated on the message-level interpretations of its arguments. For homomorphic addition, this amounts to the fact that  $\text{enc}(x) \oplus \text{enc}(y)$  is an encryption of  $x + y$ ; while for bootstrapping, this condition encodes that  $\text{bootstrap}(c)$  does not change the underlying value inside of  $c$ . The second condition, downwards-closedness, guarantees monotonicity of the bounds: if an operator states that an output bound is defined on its inputs, then a smaller output bound is defined on smaller outputs.

Throughout the rest of this section, fix an ambient valid ILA model  $\mathcal{M}$ . We additionally assume a nullary operator  $\text{true} : () \rightarrow \text{msg}$ , in order to evaluate control flow operators.

## 4.2 Syntax and Semantics

Now that we have a model, we now define the core language of ILA. The syntax is given in Figure 5. Expressions may either be variables, values of sort  $\text{msg}$  or  $\text{plain}$ , or applications of operators. We do not allow hardcoding ciphertexts into programs, as they must come from the context. Commands are standard for an imperative language. Note that the core language does not consider loops, while our surface language (e.g., in Figure 2) does; our concrete type checker only allows loops which can be statically unrolled with a finite bound.

We give big-step semantics to ILA programs in Figure 6. Given a substitution  $\gamma$  mapping variables to values in  $\llbracket \text{msg} \rrbracket \cup \llbracket \text{plain} \rrbracket \cup \llbracket \text{cipher} \rrbracket$ , the big-step rules for expressions relate substitutions and expressions to values. The rule for  $\text{op}$  checks that each input is of

$$\begin{array}{c}
\boxed{\langle \gamma, e \rangle \Downarrow v} \quad \frac{\gamma(x) = v}{\langle \gamma, x \rangle \Downarrow v} \quad \overline{\langle \gamma, v \rangle \Downarrow v} \\
\\
\frac{\text{op} : \vec{s}_i \rightarrow s \quad (\forall i, \langle \gamma, e_i \rangle \Downarrow v_i \in \llbracket s_i \rrbracket) \quad \llbracket \text{op} \rrbracket(\vec{v}_i) = v}{\langle \gamma, \text{op}(\vec{e}_i) \rangle \Downarrow v} \\
\\
\boxed{\langle \gamma, c \rangle \Downarrow \gamma'} \quad \overline{\langle \gamma, \text{skip} \rangle \Downarrow \gamma} \quad \frac{\langle \gamma, e \rangle \Downarrow v}{\langle \gamma, x := e \rangle \Downarrow \gamma[x \mapsto v]} \\
\\
\frac{\langle \gamma, c_1 \rangle \Downarrow \gamma_1 \quad \langle \gamma, c_2 \rangle \Downarrow \gamma_2}{\langle \gamma, c_1; c_2 \rangle \Downarrow \gamma_2} \quad \frac{\langle \gamma, e \rangle \Downarrow \llbracket \text{true} \rrbracket() \quad \langle \gamma, c_1 \rangle \Downarrow \gamma'}{\langle \gamma, \text{if } e \text{ then } c_1 \text{ else } c_2 \rangle \Downarrow \gamma'} \\
\\
\frac{\langle \gamma, e \rangle \Downarrow v \neq \llbracket \text{true} \rrbracket() \quad \langle \gamma, c_2 \rangle \Downarrow \gamma'}{\langle \gamma, \text{if } e \text{ then } c_1 \text{ else } c_2 \rangle \Downarrow \gamma'}
\end{array}$$

Figure 6: Big-Step Semantics for ILA.

Bounds	$\alpha_s$	$\in$	$B_s$
Types	$\tau$	$::=$	cipher $\alpha_{\text{cipher}}$   plain $\alpha_{\text{plain}}$
			msg $\alpha_{\text{msg}}$
Contexts	$\Gamma$	$::=$	$\cdot$   $\Gamma, x : \tau$
	$\alpha \leq_s \alpha'$		$s \in \{\text{cipher}, \text{plain}, \text{msg}\}$
			$s \alpha \leq s \alpha'$

Figure 7: ILA Types and Subtyping

$$\begin{array}{c}
\boxed{\text{pp}; \Gamma \vdash e : \tau} \quad \frac{x : \tau \in \Gamma}{\text{pp}; \Gamma \vdash x : \tau} \text{VAR} \quad \frac{\text{pp}; \Gamma \vdash e : \tau \quad \tau \leq \tau'}{\text{pp}; \Gamma \vdash e : \tau'} \text{SUB} \\
\\
\frac{\text{sort}(v) = s}{\text{pp}; \Gamma \vdash v : s \mid v|_s^{\text{pp}}} \text{CONST} \\
\\
\frac{\text{op} : \vec{s}_i \rightarrow s \quad (\forall i, \text{pp}; \Gamma \vdash e_i : \tau_i \wedge \text{sort}(\tau_i) = s_i) \quad \llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}(\vec{\tau}_i) = \alpha}{\text{pp}; \Gamma \vdash \text{op}(\vec{e}_i) : s \alpha} \text{OP}
\end{array}$$

Figure 8: ILA Typing: Expressions

the correct sort, and if so, evaluates  $\llbracket \text{op} \rrbracket$ . The big-step rules for commands relate substitutions and commands to output substitutions, and are standard.

### 4.3 Type System

Types for ILA programs, and their associated rules for subtyping, are given in Figure 7. Each type  $\tau$  is a pair of a sort  $s$ , and an associated bound  $\alpha_s \in B_s$ . Given a type  $\tau$ , we write  $|\tau|$  for the bound contained in the type. Subtyping in ILA requires that the two types are of the same sort and the first associated bound is less than the second, in the ordering induced by the sort.

Expressions are typed using Figure 8. The type checking judgment takes the public parameters  $\text{pp}$  as input to check that all bounds are correct. The rule for values computes the bound  $|v|_s^{\text{pp}}$  as part of the output type; since we only allow messages and encoded plaintexts in source programs, we do not need the secret parameters. For operators, we check that all inputs are sort-correct, and

$$\begin{array}{c}
\boxed{\text{pp}; \Gamma \vdash c : \Gamma'} \quad \frac{\text{pp}; \Gamma \vdash e : \tau}{\text{pp}; \Gamma \vdash x := e : \Gamma[x \mapsto \tau]} \text{ASSGN} \\
\\
\overline{\text{pp}; \Gamma \vdash \text{skip} : \Gamma} \text{SKIP} \quad \frac{\text{pp}; \Gamma \vdash c_1 : \Gamma' \quad \text{pp}; \Gamma' \vdash c_2 : \Gamma''}{\text{pp}; \Gamma \vdash c_1; c_2 : \Gamma''} \text{SEQ} \\
\\
\frac{\text{pp}; \Gamma \vdash c_1 : \Gamma_1 \quad \text{pp}; \Gamma \vdash c_2 : \Gamma_2 \quad \text{pp}; \Gamma \vdash e : \text{msg } \alpha \quad \Gamma_1 \sqcap \Gamma_2 \sim \Gamma'}{\text{pp}; \Gamma \vdash \text{if } e \text{ then } c_1 \text{ else } c_2 : \Gamma'} \text{ITE} \\
\\
\boxed{\Gamma_1 \sqcap \Gamma_2 \sim \Gamma} \\
\\
\frac{(\forall x \in \Gamma, x \in \Gamma_1 \wedge \Gamma_1(x) \leq \Gamma(x)) \quad (\forall x \in \Gamma, x \in \Gamma_2 \wedge \Gamma_2(x) \leq \Gamma(x))}{\Gamma_1 \sqcap \Gamma_2 \sim \Gamma}
\end{array}$$

Figure 9: ILA Typing: Commands

then check that the output bound  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}(\vec{\tau}_i)$  is defined; if it is, we record this in the output type. In essence, since we have a valid ILA model (Section 4.1), the requirement that the output bound is correct enforces that all relevant quantities (e.g., RLWE noise) are within bounds.

Type checking for commands is given in Figure 9, and is largely standard. For if statements, we require that the branch is a cleartext message, and that both branches type check; then, we *merge* the two output contexts using the judgment  $\Gamma_1 \sqcap \Gamma_2 \sim \Gamma$ , which requires that  $\Gamma$  is a sound overapproximation of the post-states represented by  $\Gamma_1$  and  $\Gamma_2$ .

### 4.4 Semantic Validity

Before proving full functional correctness, we prove *semantic validity*, which demonstrates that all runtime values are within bounds described by their types (as modeled by the ILA model). We do so via *semantic type soundness*, which requires we give a semantics to types:

$$\begin{aligned}
\llbracket \text{cipher } \alpha \rrbracket^{\text{sp}} &= \{c \in \llbracket \text{cipher} \rrbracket \mid |c|_{\text{cipher}}^{\text{sp}} \leq_{\text{cipher}} \alpha\} \\
\llbracket \text{plain } \alpha \rrbracket^{\text{sp}} &= \{p \in \llbracket \text{plain} \rrbracket \mid |p|_{\text{plain}}^{\text{sp}} \leq_{\text{plain}} \alpha\} \\
\llbracket \text{msg } \alpha \rrbracket^{\text{sp}} &= \{m \in \llbracket \text{msg} \rrbracket \mid |m|_{\text{msg}}^{\text{sp}} \leq_{\text{msg}} \alpha\}
\end{aligned}$$

Thus,  $\llbracket \tau \rrbracket^{\text{sp}}$  are the values of the correct sort that are soundly approximated by  $\tau$  bounds. Note that while we use the secret parameters to define soundness, only the public parameters  $\text{pp}$  are required for type checking.

Now, we show that all runtime values of type  $\tau$  must reside in  $\llbracket \tau \rrbracket^{\text{sp}}$ . First, given a substitution  $\gamma$ , we say that  $\text{sp}; \Gamma \models \gamma$  if for all  $x \in \Gamma$ ,  $x \in \gamma \wedge \gamma(x) \in \llbracket \Gamma(x) \rrbracket^{\text{sp}}$ . Then, for expressions, say that  $\text{sp}; \Gamma \models e : \tau$  if for all  $\text{sp}; \Gamma \models \gamma$ ,  $\langle \gamma, e \rangle \Downarrow v \wedge v \in \llbracket \tau \rrbracket^{\text{sp}}$  for some  $v$ . Intuitively,  $\text{sp}; \Gamma \models \gamma$  states that  $\gamma$  is a well-formed substitution relative to its semantic types, while  $\text{sp}; \Gamma \models e : \tau$  states that, given a well-formed substitution,  $e$  evaluates to a well-formed value relative to  $\llbracket \tau \rrbracket^{\text{sp}}$ . We now show semantic safety for expressions:

**THEOREM 4.1 (SEMANTIC SAFETY: EXPRESSIONS).** *Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash e : \tau$ . Then  $\text{sp}; \Gamma \models e : \tau$ .*

$\langle \gamma, e \rangle \Downarrow_{\text{msg}}^{\text{sp}} v$	$\frac{\gamma(x) = v}{\langle \gamma, x \rangle \Downarrow_{\text{msg}}^{\text{sp}} v}$	$\frac{v \in \llbracket s \rrbracket}{\langle \gamma, v \rangle \Downarrow_{\text{msg}}^{\text{sp}} \text{interp}_s^{\text{sp}}(v)}$
$\text{op} : \vec{s}_i \rightarrow s$	$(\forall i, \langle \gamma, e_i \rangle \Downarrow_{\text{msg}}^{\text{sp}} v_i)$	$\frac{\llbracket \text{op} \rrbracket_{\text{msg}}(\vec{v}_i) = v}{\langle \gamma, \text{op}(\vec{e}_i) \rangle \Downarrow_{\text{msg}}^{\text{sp}} v}$
$\langle \gamma, c \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma'$	$\frac{}{\langle \gamma, \text{skip} \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma}$	$\frac{\langle \gamma, e \rangle \Downarrow_{\text{msg}}^{\text{sp}} v}{\langle \gamma, x := e \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma[x \mapsto v]}$
	$\frac{\langle \gamma, c_1 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma_1 \quad \langle \gamma_1, c_2 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma_2}{\langle \gamma, c_1; c_2 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma_2}$	
	$\frac{\langle \gamma, e \rangle \Downarrow_{\text{msg}}^{\text{sp}} \llbracket \text{true} \rrbracket () \quad \langle \gamma, c_1 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma'}{\langle \gamma, \text{if } e \text{ then } c_1 \text{ else } c_2 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma'}$	
	$\frac{\langle \gamma, e \rangle \Downarrow_{\text{msg}}^{\text{sp}} v \neq \llbracket \text{true} \rrbracket () \quad \langle \gamma, c_1 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma'}{\langle \gamma, \text{if } e \text{ then } c_1 \text{ else } c_2 \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma'}$	

**Figure 10: Big-Step Semantics for Message Equivalence.**

For commands, say that  $\text{sp}; \Gamma \models c \dashv \Gamma'$  if for all  $\text{sp}; \Gamma \models \langle \gamma, c \rangle \Downarrow \gamma'$  and  $\text{sp}; \Gamma' \models \gamma'$ . Intuitively, commands must map well-formed substitutions under  $\Gamma$  to well-formed substitutions under  $\Gamma'$ .

**THEOREM 4.2 (SEMANTIC SAFETY: COMMANDS).** *If  $\text{topub}(\text{sp}); \Gamma \vdash c \dashv \Gamma'$ , then we have that  $\text{sp}; \Gamma \models c \dashv \Gamma'$ .*

## 4.5 Message Equivalence

Now that we have proven semantic safety, we prove functional correctness, which we formalize as *message equivalence*. Intuitively, message equivalence states that decrypting the result of any homomorphic circuit is equivalent to running that same circuit in cleartext, on decrypted inputs. This is the main guarantee of ILA.

To formalize message equivalence, we give an alternative big-step semantics to expressions and commands in Figure 10 that describes cleartext evaluation. Here, all substitutions map variables to cleartext values (i.e., values in  $\llbracket \text{msg} \rrbracket$ ). We then define cleartext evaluation by immediately interpreting (i.e., decoding) all encoded plaintexts into messages, and interpreting operators through their message-level semantics  $\llbracket \text{op} \rrbracket_{\text{msg}}$ . Hence, homomorphic addition will be interpreted as ordinary addition on integers, and bootstrapping operators will be interpreted as no-ops. The semantics for commands are similar to their ordinary big-step rules.

We now show that evaluating expressions and commands under their ordinary semantics, and then interpreting (e.g., decrypting), is equivalent to evaluating under the alternative semantics  $\Downarrow_{\text{msg}}^{\text{sp}}$ . To define the below theorems formally, we introduce an auxiliary definition: given a type context  $\Gamma$  and a substitution  $\gamma$  such that  $\text{sp}; \Gamma \models \gamma$ , let

$$\text{interp}_{\Gamma}^{\text{sp}}(\gamma) = \{x \mapsto \text{interp}_{\text{sort}(\Gamma(x))}^{\text{sp}}(\gamma(x))\}.$$

Thus,  $\text{interp}_{\Gamma}^{\text{sp}}(\gamma)$  maps substitutions  $\gamma$  under the original FHE semantics into substitutions only over messages. With this definition, we can now prove message equivalence for expressions and commands:

	ILA	ILA <sub>(bgv)</sub>
		$\epsilon \in \mathbb{Q}_{\geq 1}^+$ $\omega \in \mathbb{Z}_{\geq 1}^+$ $\text{inf}, \text{sup} \in \mathbb{Q}$
<b>Values</b>	$v ::= \llbracket \text{msg} \rrbracket$ $  \llbracket \text{plain} \rrbracket$	$n \in \mathbb{Z}_t^d$ $  p \in \frac{\mathbb{Z}_t[x]}{(x^d+1)}$
<b>Expressions</b>	$e ::= x \mid v$ $  \text{op}(\vec{e}_i)$	$x \mid v$ $  e_1 \oplus e_2 \mid e_1 \otimes e_2$ $  \text{modswitch}(e)$
<b>Statements</b>	$c ::= \text{skip} \mid x := e \mid c; c \mid \text{if } e \text{ then } c \text{ else } c$	
<b>Types</b>	$\tau ::= \text{cipher } \alpha_{\text{cipher}}$ $  \text{plain } \alpha_{\text{plain}}$ $  \text{msg } \alpha_{\text{msg}}$	$\text{cipher } \langle \text{inf}, \text{sup}, \epsilon, \omega \rangle$ $  \text{plain } \langle \text{inf}, \text{sup} \rangle$ $  \mathbb{Z}_t^d$

**Figure 11: ILA<sub>(bgv)</sub> syntax instantiation. Columns ILA and ILA<sub>(bgv)</sub> show corresponding syntax. Sorts  $\llbracket s \rrbracket$ , operator  $\text{op}$  and bounds  $\alpha_s$  are instantiated accordingly. The row  $c$  denoting statements is common to both.**

**THEOREM 4.3 (MESSAGE EQUIVALENCE: EXPRESSIONS).** *Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash e : \tau$  and  $\text{sp}; \Gamma \models \gamma$ . Then,  $\langle \gamma, e \rangle \Downarrow v$  and  $\langle \text{interp}_{\Gamma}^{\text{sp}}(\gamma), e \rangle \Downarrow_{\text{msg}}^{\text{sp}} v_{\text{msg}}$  such that  $\text{interp}_{\text{sort}(v)}^{\text{sp}}(v) = v_{\text{msg}}$ .*

The proof of Theorem 4.3 follows by induction on the typing derivation of  $e$ . The case for operators makes use of commutativity and downwards-closedness of the ILA model (Section 4.1) in order to interchange decryption and evaluation of the operator.

After proving message equivalence for expressions, we can then prove it for commands. The proof is a straightforward induction on the typing derivation for commands. Appendix C shows the proof sketch.

**THEOREM 4.4 (MESSAGE EQUIVALENCE: COMMANDS).** *Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash c \dashv \Gamma'$  and  $\text{sp}; \Gamma \models \gamma$ . Then, if  $\langle \gamma, c \rangle \Downarrow \gamma_1$ , we have that  $\langle \text{interp}_{\Gamma}^{\text{sp}}(\gamma), c \rangle \Downarrow_{\text{msg}}^{\text{sp}} \gamma_1'$  such that, for all  $x \in \gamma_1$ ,  $\text{interp}_{\text{sort}(\gamma_1(x))}^{\text{sp}}(\gamma_1(x)) = \gamma_1'(x)$ .*

## 5 ILA Models

At a high-level, the recipe for scheme instantiation involves defining all sets and mappings from Definition 4.1 and proving that the commutativity and downwards axioms hold.

### 5.1 ILA<sub>(bgv)</sub>: ILA for BGV Scheme

We introduce ILA<sub>(bgv)</sub>, an instantiation of ILA model for the BGV scheme, a levelled RLWE scheme [7]. Figure 11 shows the syntax correspondence between ILA (Figures 5 and 7) and ILA<sub>(bgv)</sub>. Abstract operators, sorts and bounds from ILA are instantiated. Operator  $\text{op}$  is instantiated with homomorphic multiplication  $\otimes$ , addition  $\oplus$  and modulus switching  $\text{modswitch}$ . Plaintext and ciphertext bounds,  $\alpha_{\text{plain}}$  and  $\alpha_{\text{cipher}}$ , track the interval  $[\text{inf}, \text{sup}]$  in which the underlying message lies as well as the current noise  $\epsilon$ . Intuitively,  $\epsilon$  measures how far the coefficients are from corrupting the ciphertext. The level  $\omega$  is drawn from a pre-defined set of levels  $\{q_0, q_1, \dots, q_L\}$ . We say  $((\text{inf}, \text{sup}), \epsilon, \omega) \leq_{\text{cipher}} ((\text{inf}', \text{sup}'), \epsilon', \omega)$  if  $\text{inf}' \leq \text{inf} \leq \text{sup} \leq \text{sup}'$  and  $\epsilon \leq \epsilon'$ .

Since noise  $\epsilon$  is added during encryption, plaintext contains no noise. As a result plaintext-plaintext operations and plaintext ciphertext additions are not noise increasing operations. However,



noise increase after a plaintext-ciphertext multiplication is proportional to that of a fresh cipher-ciphertext multiplication.

Definition 5.1 summarizes the instantiation. Note that it closely follows ILA model defined in Section 4.

DEFINITION 5.1 (ILA<sub>(bgv)</sub>). *The ILA<sub>(bgv)</sub> model is defined as follows:*

- $\mathcal{SP} := \{t, d, \{q_\omega\}, pk, relin_k, encode, decode\} \times \{sk\}$
- $\mathcal{PP} = \{d, t, \{q_\omega\}, pk, relin_k, encode, decode\}$
- $topub = \mathcal{SP} = \mathcal{PP} \times \{sk\} \rightarrow \mathcal{PP}$  is the projection onto  $\mathcal{PP}$
- $Sort \mathcal{S} = \{msg, plain, cipher\}$ 
  - $\llbracket msg \rrbracket = n \in \mathbb{Z}_t^d$
  - $\llbracket plain \rrbracket = p \in \frac{\mathbb{Z}_t[x]}{(x^d+1)}$
  - $\llbracket cipher \rrbracket = ct \in R_q^{n+1} \times \mathbb{Z}_{\geq 0}$
- *Encoding and decoding functions:*
  - $encode: \mathbb{Z}_t^d \rightarrow R_t$
  - $decode: R_t \rightarrow \mathbb{Z}_t^d$
- *Bounds sets:*
  - $B_{msg} = \emptyset$
  - $B_{plain} = \{(\inf, \sup) \mid \inf \leq \sup\}$  ordered by  $\leq_{plain}$
  - $B_{cipher} = \{(\inf, \sup, \epsilon, \omega) \mid \inf \leq \sup \text{ and } \epsilon, \omega \in \mathbb{N}\}$  ordered by  $\leq_{cipher}$
- *Mappings for bounds computation:*
  - $|m|_{msg}^{pp} = \emptyset$
  - $|p|_{plain}^{pp} = (\inf, \sup)$  (explained below)
  - $|ct, \omega|_{cipher}^{sp} = (\inf, \sup, \epsilon, \omega)$  (explained below)
- *Decoding on plaintexts:*  $interp_{plain}^{pp}(p) = decode(p)$
- *Decryption on ciphertexts:*  $interp_{cipher}^{sp}(ct) = decode(decrypt(ct))$
- *Operator  $op \in \{\oplus, \otimes, modswitch\}$  defined as follows:*
  - $\llbracket \otimes \rrbracket: \{cipher, plain\}, \{cipher, plain\} \rightarrow \{cipher, plain\}$
  - $\llbracket \oplus \rrbracket: \{cipher, plain\}, \{cipher, plain\} \rightarrow \{cipher, plain\}$
  - $\llbracket modswitch \rrbracket: cipher \rightarrow cipher$
- *Operator bounds manipulation mapping,  $\llbracket op \rrbracket_{bnd}^{pp}$  defined in Table 1.*

The set  $\mathcal{SP}$  contains all scheme parameters including plaintext modulus  $t$ , poly modulus degree  $d$ , scheme-related keys as well as encoding and decoding functions. The public parameter set  $\mathcal{PP} \subseteq \mathcal{SP}$  contains all public parameters including public and relinearization keys. The function  $topub$  projects  $\mathcal{SP}$  to  $\mathcal{PP}$ .

Bounds sets for plain and ciphertexts include set of elements,  $\alpha_{plain}$  and  $\alpha_{cipher}$  (described above). Bounds set for messages is empty. In bounds computations for plaintext  $p$  if  $z = decode(p)$  then  $\inf$  and  $\sup$  are two random rationals such that  $\inf \leq \min_i \{z_i\} \leq \max_i \{z_i\} \leq \sup$ . Bound computations for ciphertext involves decrypting the ciphertext using the secret key to obtain minimum and maximum of the underlying cleartext. If  $eval\_noise_{sk}$  computes noise in a ciphertext then  $\epsilon$  is chosen (deterministically or randomly) such that  $eval\_noise_{sk}(ct) \leq \epsilon$ . We also assume (but not explicitly define) the decoding and decryption functions. Though these are BGV scheme primitives, they are outside ILA<sub>(bgv)</sub> model as these operations are typically used at the end of the circuit evaluation.

Table 1 provides the bounds computations for the key ILA<sub>(bgv)</sub> homomorphic operations. The second column computes the operator bound  $\llbracket \cdot \rrbracket_{bnd}^{pp}$  and the last column describes the conditions under

which the bound is defined. For example,  $ct_1 \oplus ct_2$  with input bounds  $(\inf_i, \sup_i, \epsilon_i, \omega)$  results in the bound  $(\inf_1 + \inf_2, \sup_1 + \sup_2, \epsilon_1 + \epsilon_2, \omega)$  provided  $-t/2 \leq \inf \leq \sup < t/2$  and  $\epsilon \leq \frac{q_\omega}{2}$  where  $t$  and  $q_\omega$  are the plaintext modulus and critical value at modulus level  $\omega$ , respectively. Operator  $modswitch$  decrements the modulus level by one and reduces the original noise by  $\frac{q_{\omega-1}}{q_\omega}$ ; a small correction  $B_r$  is also added to correct the rounding error induced by the division.

Our noise estimation for homomorphic multiplication is parametric in functions  $f$  and  $g$  with the only restriction that they are downwards closed. Thus, they are positive and monotonic. In Section 7, we refer to estimators from the existing literature that satisfy these conditions.

5.1.1 ILA<sub>(bgv)</sub> Typing Rules. ILA<sub>(bgv)</sub> typing rules are obtained by instantiating ILA type system from Figure 8 using the bounds information computed in Table 1. Below, we show a sample typing rule for  $\otimes$  operator that multiplies two ciphertexts.

$$\begin{array}{c} \otimes : cipher, cipher \rightarrow cipher \\ \hline \forall i, pp; \Gamma \vdash e_i : cipher \langle \inf_i, \sup_i, \epsilon_i, \omega \rangle \\ -t/2 \leq \inf \leq \sup < t/2 \quad f(\epsilon_1, \epsilon_2) \leq q_\omega/2 \\ \hline pp; \Gamma \vdash e_1 \otimes e_2 : cipher \langle \inf, \sup, f(\epsilon_1, \epsilon_2), \omega \rangle \end{array}$$

The output interval  $[\inf, \sup]$  is computed from Table 1. The exact definition of  $f$  is dependent on the noise estimation technique. This enables an extensible framework for plug-and-play noise estimators. Appendix E shows typing rule instantiation for  $modswitch$ .

5.1.2 Instantiation of ILA Functional Correctness. To complete the ILA<sub>(bgv)</sub> instantiation, we have to show that commutativity and downwards closure axioms hold in our instantiation. Downwards closed axiom is immediate as the noise growth in homomorphic operations is monotonic.

The commutativity axiom partly encodes the correctness theorems of the BGV scheme. Intuitively, it says that the cipher addition of two  $\omega$  level ciphers with noise  $\epsilon_1$  and  $\epsilon_2$  is a valid ciphertext i.e., decryption yields a *correct* plaintext value when  $\epsilon_1 + \epsilon_2 \leq q_\omega/2$ . Similarly, the cipher multiplication of such ciphertexts is valid when  $\epsilon_1 * \epsilon_2 \leq q_\omega/2$  (cf. Lemma 7 and 8 from Section 5 [7]). Further,  $-t/2 \leq \inf_1 + \inf_2 \leq \sup_1 + \sup_2 < t/2$  guarantees that the resultant plaintext decodes correctly. Correctness up to plaintext space follows Lemma 7 and 8 from Section 5 [7]. The rest is immediate by induction of typing derivation.

Since ILA<sub>(bgv)</sub> is a valid ILA model, we get Theorem 4.4 for free. That is if the initial environment contains correct-by-construction ciphertexts, then well-typed ILA<sub>(bgv)</sub> programs are functionally correct (up to the correctness of FHE primitive operations). Specifically, the programs are devoid of noise overflow errors. Additionally, the decryption and decode of the output(s) exactly matches the corresponding cleartext values, i.e., the absence of any plaintext modulus wraparound errors. We thus guarantee the functional correctness of a BGV circuit via type checking.

Note the assumption on the initial environment. If the ciphertexts are adversarial, i.e., they do not match the input types, then the theorem guarantees are invalid. Thus, the input sources are trusted. This is a reasonable assumption as FHE circuits run on sensitive but trusted inputs.



Op	$\llbracket \cdot \rrbracket_{\text{bnd}}^{\text{pp}}$	Conditions
$\oplus$	$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{pp}}(\overrightarrow{(\text{inf}_i, \text{sup}_i, \epsilon_i, \omega)}) = (\text{inf}, \text{sup}, \epsilon, \omega)$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$ and $\epsilon \leq \kappa$
	$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{pp}}(\overrightarrow{(\text{inf}_i, \text{sup}_i)}) = (\text{inf}, \text{sup})$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$
	$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{pp}}((\text{inf}_1, \text{sup}_1), (\text{inf}_2, \text{sup}_2, \epsilon_2, \omega)) = (\text{inf}, \text{sup}, \epsilon, \omega)$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$ and $\epsilon \leq \kappa$ where $\text{inf} := \text{inf}_1 + \text{inf}_2, \text{sup} := \text{sup}_1 + \text{sup}_2, \epsilon := \epsilon_1 + \epsilon_2$
$\otimes$	$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{pp}}(\overrightarrow{(\text{inf}_i, \text{sup}_i, \epsilon_i, \omega)}) = (\text{inf}, \text{sup}, f(\epsilon_1, \epsilon_2), \omega)$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$ and $f(\epsilon_1, \epsilon_2) \leq \kappa$
	$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{pp}}(\overrightarrow{(\text{inf}_i, \text{sup}_i)}) = (\text{inf}, \text{sup})$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$
	$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{pp}}((\text{inf}_1, \text{sup}_1), (\text{inf}_2, \text{sup}_2, \epsilon, \omega)) = (\text{inf}, \text{sup}, g(\epsilon), \omega)$	if $-t/2 \leq \text{inf} \leq \text{sup} < t/2$ and $g(\epsilon) \leq \kappa$ where $\text{inf} := \min\{\text{inf}_1 * \text{inf}_2, \text{inf}_1 * \text{sup}_2, \text{sup}_1 * \text{inf}_2, \text{sup}_1 * \text{sup}_2\}$ $\text{sup} := \max\{\text{inf}_1 * \text{inf}_2, \text{inf}_1 * \text{sup}_2, \text{sup}_1 * \text{inf}_2, \text{sup}_1 * \text{sup}_2\}$
modswitch	$\llbracket \text{modswitch} \rrbracket_{\text{bnd}}^{\text{pp}}(\text{inf}, \text{sup}, \epsilon, \omega) = (\text{inf}, \text{sup}, \epsilon', \omega - 1)$	$0 \leq \omega - 1 \leq L - 1; \epsilon' = \frac{q\omega-1}{q\omega}\epsilon + B_r \leq l_{\omega-1}$

**Table 1:  $\text{ILA}_{(bgv)}$ : ILA model for BGV and BFV scheme illustrating key homomorphic operations. Constant  $\kappa$  is  $\frac{q\omega}{2}$  for BGV, and  $\frac{1}{2}$  for BFV; scheme parameters  $t$  and  $q\omega$  are plaintext modulus and coefficient modulus at modulus level  $\omega$ , respectively.**

## 5.2 $\text{ILA}_{(bfv)}$ : ILA for BFV Scheme

BFV is a levelled RLWE scheme that is highly similar to BGV except that messages are scaled up and encoded from the most significant bit.  $\text{ILA}_{(bfv)}$  is the instantiation of ILA for BFV scheme. For brevity, we only focus on key differences.

In a BFV scheme modulus switching is used as a tool to improve computational performance. Since this is orthogonal to the current focus,  $\text{ILA}_{(bfv)}$  supports all operations of  $\text{ILA}_{(bgv)}$  except modswitch. Thus, the instantiation is similar to that of  $\text{ILA}_{(bgv)}$  with two differences. First, the cipher bounds set  $B_{\text{cipher}}$  is the set of triples,  $(\text{inf}, \text{sup}, \epsilon)$  with the standard lexicographic ordering. Note that modulus level is not included. Second, the operator bounds manipulation mapping uses the condition  $\epsilon \leq \frac{1}{2}$  instead of  $\epsilon \leq \frac{q\omega}{2}$  (the last column of Table 1).

It is obvious that  $\text{ILA}_{(bfv)}$  also satisfies commutativity and downwards closed axioms, and thus we get correctness, that is, Theorem 4.4, for free.

## 5.3 $\text{ILA}_{(tfhe)}$ : ILA for TFHE Scheme

We introduce  $\text{ILA}_{(tfhe)}$ , an instantiation of ILA model for the CGGI/TFHE scheme [11]. TFHE is a fast bootstrapping based scheme. It differs from BGV in that it supports additional types of ciphertexts (LWE, RLWE and RGSW) and the corresponding operations including boolean (e.g., exclusive-or). Plaintext and ciphertexts are elements of the sets  $R_t$  and  $\mathbb{Z}_q^{n+1} \cup R_q^{n+1}$ , respectively (see Section 2). LWE and RLWE ciphertexts are drawn from the sets  $\mathbb{Z}_q^{n+1}$  and  $R_q^{n+1}$ , respectively; a GLWE ciphertext is either a LWE cipher or a RLWE cipher. A RGSW ciphertexts can be seen as elements of the space  $R_q^{n+1 \times n+1}$ .

RGSW ciphertexts should be considered as tools necessary to support various internal TFHE operations that are not exposed to the user. Hence, a ciphertext is an element of either  $\mathbb{Z}_q^{n+1}$  or  $R_q^{n+1}$ , and we track the set using  $id$ . Thus, we define  $\alpha_{\text{cipher}}$  as  $(id, \text{inf}, \text{sup}, \epsilon)$ . We say  $(id, \text{inf}, \text{sup}, \epsilon) \leq_{\text{cipher}} (id', \text{inf}', \text{sup}', \epsilon')$  if  $id = id'$  followed by the standard lexicographic ordering of the remaining triples. The bounds set for plaintext is the same as that of BGV. Similarly, a plaintext could be an element of  $\mathbb{Z}_t$  or  $R_t$ , however the former coincides with the cleartext space. As a result we assume a plaintext is always a RLWE plaintext.

TFHE supports programmable bootstrapping (PBS), pbs, for noise reduction in the ciphertext. Recall from Section 2 that programmable bootstrapping is a composition of modulus switching, blind rotation and sample extraction operations. For PBS to succeed, the switching operation must succeed; the latter requires a non-zero noise budget.

TFHE also supports internal product,  $\boxtimes$  that operates exclusively on GLWE ciphertexts and an external product,  $\boxdot$ , that operates on (restricted) combinations of ciphertexts. TFHE supports more operators, however, these additional operators are implemented as a combination of basic operators. For example, controlled multiplexer, cmux, that conditionally selects a ciphertext is implemented as a combination of external product, GLWE cipher addition and multiplication. Definition 5.2 summarizes the key details and focuses on differences with respect to the BGV scheme. We provide the full instantiation in Appendix D.

**DEFINITION 5.2 ( $\text{ILA}_{(tfhe)}$ ).** The  $\text{ILA}_{(tfhe)}$  model is defined as follows:

- Sort  $S = \{\text{msg}, \text{plain}, \text{cipher}\}$ 
  - $\llbracket \text{msg} \rrbracket = n \in \mathbb{Z}_t$
  - $\llbracket \text{plain} \rrbracket = p \in \frac{\mathbb{Z}_t[x]}{(x^d+1)}$
  - $\llbracket \text{cipher} \rrbracket = ct \in R_q^{n+1} \cup \mathbb{Z}_t^{n+1}$
- Bounds sets:
  - $B_{\text{cipher}} = \{(id, \text{inf}, \text{sup}, \epsilon) \mid \text{inf} \leq \text{sup}, id \in \{\text{LWE}, \text{RLWE}, \text{RGSW}\} \text{ and } \epsilon \in \mathbb{N}\}$
- Mapping for bounds computation:
  - $|ct|_{\text{cipher}}^{\text{sp}} = (id, \text{inf}, \text{sup}, \epsilon)$  (explained below)
- Operator  $\text{op} \in \{\oplus, \otimes, \boxtimes, \boxdot, \text{pbs}\}$ . Select operators defined as follows:
  - $\llbracket \boxdot \rrbracket : \{\text{cipher}, \text{cipher}\} \rightarrow \{\text{cipher}\}$
  - $\llbracket \text{pbs} \rrbracket : \{\text{cipher}, \text{cipher}\} \rightarrow \{\text{cipher}\}$
- Operator bounds manipulation mapping,  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}$  defined in Table 2.

Similar to  $\text{ILA}_{(bgv)}$ ,  $\text{ILA}_{(tfhe)}$  defines scheme parameters as well as bounds sets. For a cipher  $ct$  the map  $| \cdot |_{\text{cipher}}^{\text{sp}}$  is defined as a tuple  $(id, \text{inf}, \text{sup}, \epsilon)$ , where  $id = \text{LWE}$  if the coefficients of the given cipher are elements of  $\mathbb{Z}_t$  and  $id = \text{RLWE}$  if the coefficients are elements of  $R_q^{n+1}$ . Value bounds  $\text{inf}$  and  $\text{sup}$  are random rational

numbers such that  $\inf \leq \text{decrypt}(ct) \leq \sup$ . The noise in  $ct$  is bounded by  $\epsilon$  i.e.,  $\text{eval\_noise}_{sk}(ct) \leq \epsilon$ . Table 2 defines the operator bounds manipulation mappings for select TFHE operations. Despite the differences in the types of ciphertexts, the bounds manipulation is mostly straightforward for all types of ciphertext multiplication. The output noise is parameterized by the functions that are downward closed; the operation is defined iff the output noise less than  $\frac{q}{t}$ .

Given two ciphertexts  $ct_1$  and  $ct_2$ , the  $\text{pbs}$  operator applies the function (e.g., LUT) in  $ct_1$  to the second input  $ct_2$ . At the same time it resets the noise in the output to the nominal level (constant noise denoted by  $\epsilon_b$ ). The operation emits a LWE ciphertext with new bounds provided there are no overflows. The condition  $-t/2 < \inf_0 < \sup_0$  ensures that there is no overflow in the output value. Note that it is sufficient to check that  $\inf_0$  and  $\sup_0$  as they are the value bounds of the LUT in  $ct_1$ . We can ignore  $\inf$  and  $\sup$  from the input  $ct_2$ . Interestingly, the constraint on the input noise  $\epsilon < q/t - \delta$  is required to ensure that the switching step of the bootstrapping operation succeeds (see Section 2);  $\delta$  is a constant representing switching error, thus  $\epsilon$  must have a non-zero noise budget for PBS to succeed.

The commutativity and downwards closure axioms are similar to that of  $\text{ILA}_{(bgv)}$ . Since  $\text{ILA}_{(tfhe)}$  is a valid ILA model, we get Theorem 4.4 for free.

## 6 Transformation Validation

ILA's type system can be used to validate that the transformations are bounds-preserving. For example, transforming  $x_1 \otimes x_2 \otimes x_3 \otimes x_4$  to  $y_1 \otimes y_2$  where  $y_1 = x_1 \otimes x_2$  and  $y_2 = x_3 \otimes x_4$  is not bounds-preserving: the former has a multiplicative depth of 3 where as the latter has the multiplicative depth of 2; this leads to different noise levels in the output. This is useful in building correct-by-construction optimizations.

We describe an optimization that infers the placement of modulus switching operations for loop-free programs and uses the type system to validate the bounds-preservation property.

### 6.1 Modulus Switch Inference

Recall from Section 2 that modulus switching refreshes noise. Though it reduces both noise and noise budget in a ciphertext, the relative reduction in noise is greater than that of noise budget. This is significant and somewhat surprising: if a program turns on switching level for every ciphertext, then the noise budget gets quickly exhausted reducing the circuit's overall multiplicative depth (compared to the vanilla circuit). Moreover, the operation is computationally expensive.

However, modulus switching could improve the circuit's multiplicative depth. Our inference algorithm identifies the conditions under which modulus switch placement succeeds. Toward this end, the algorithm iteratively estimates the placement and uses the type checker to validate the placement at every iteration.

Consider the program shown on the left of Figure 12. It computes  $c1^{16}$ . Suppose that  $c5$  in line 4 yields incorrect output due to the noise overflow. Transforming line 4 to  $c5 = \text{modswitch}(c4 \otimes c4)$  will not help as switching cannot recover any additional noise budget after-the-fact. So where should the placement be such that the computation succeeds? Our novel insight is to place the switching

#### Algorithm 1: Modulus Switch Inference

```

// Create a definition list.
1 defList ← getDefList(ast);
// Create a MD Tree for a given node.
2 mdtree ← MulDepthTree(node,  $\phi$ );
3 ast ← Modswitch(ast, mdtree,  $\Gamma$ );
4 Function Modswitch (ast, mdtree,  $\Gamma$ ) → AST:
    // Pick a leaf node.
    5 node ← Leaves(mdtree);
    6 p ← Parent(node, mdtree);
    7 rhs ← defList[p];
    8 ast, rhs' ← MSinsert(ast, p, rhs);
    9  $\tau$  ← TypeCheck(rhs',  $\Gamma$ );
    10  $\Gamma \leftarrow \Gamma[\text{node} \mapsto \tau]$ ;
    11 node ← p;
    12 while node ≠ root(ast) do
    13     p ← Parent(node, mdtree);
    14     rhs ← defList[p];
    15     ast, rhs ← MSLevel(ast, p, rhs,  $\Gamma$ );
    16     stat,  $\tau$  ← TypeCheck(rhs,  $\Gamma$ );
    17     if !stat then
    18         return fail;
    19      $\Gamma \leftarrow \Gamma[\text{node} \mapsto \tau]$ ;
    20     node ← p;
    21 return ast;
// Type check an expression.
22 Function TypeCheck (e,  $\Gamma$ ) → (bool,  $\tau$ ):
    23 status ← ...;
    24 return status,  $\tau$ ;
// Ensure that operands have same modulus level.
25 Function MSLevel (ast, p, rhs,  $\Gamma$ ) → (bool,  $\tau$ ):
    26 rhs ← ...;
    27 ast ← ...;
    28 return ast, rhs;
29 Function MulDepthTree(node, wl):
    30 rhs ← defList[node];
    31 wl ← wl  $\cup$  vars(rhs);
    32 foreach xnode  $\in$  wl do
    33     if xnode  $\in$  dom(defList) then
    34         xtree ← MulDepthTree(xnode,  $\phi$ );
    35         // Update children set for node
    36         node.child ← node.child  $\cup$  xtree;
    // returns MD Tree rooted at node
    return node;
```

operations at *least positive multiplicative depth*. This yields maximal noise budget savings that could enable more operations. In this case, the multiplicative depth of  $c5$  is 15 (since  $c5 = c1^{16}$  with 15 multiplications) and that of  $c2$  is 1. Thus, switching  $c2$  yields maximal noise budget gains.

Op	$\llbracket \cdot \rrbracket_{\text{bnd}}^{\text{PP}}$	Conditions
$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{PP}}(\text{LWE}, \text{inf}_i, \text{sup}_i, \epsilon_i)$	$= (\text{LWE}, \text{inf}, \epsilon_b)$	$\epsilon_b$ is nominal ( $= 1$ )
$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{PP}}(\text{RGSW}, \text{inf}_i, \text{sup}_i, \epsilon_i)$	$= (\text{RGSW}, \text{inf}, \text{sup}, f(\epsilon_1, \epsilon_2))$	$f(\epsilon_1, \epsilon_2) \leq q/t$
$\llbracket \square \rrbracket_{\text{bnd}}^{\text{PP}}(\text{RGSW}, \text{inf}_1, \text{sup}_1, \epsilon_1), (\text{id}, \text{inf}_2, \text{sup}_2, \epsilon_2)$	$= (\text{RLWE}, \text{inf}, \text{sup}, g(\epsilon_1, \epsilon_2))$	$g(\epsilon_1, \epsilon_2) \leq q/t$
$\llbracket \text{pbs} \rrbracket_{\text{bnd}}^{\text{PP}}((\text{RGSW}, \text{inf}_0, \text{sup}_0, \epsilon_0), (\text{LWE}, \text{inf}, \text{sup}, \epsilon_b))$	$= (\text{LWE}, \text{inf}, \text{sup}, \epsilon_b)$	$\max\{\log_2  \text{inf} , \log_2  \text{sup} \} \leq (\log_2(t) - 1)$ $\epsilon < q/t - \delta$ and $-t/2 \leq \text{inf}_0 \leq \text{sup}_0 < t/2$

**Table 2: ILA<sub>(tfhe)</sub>: ILA model for TFHE scheme illustrating select TFHE operations. Input ciphertext operand has the type  $(\text{id}_i, \text{inf}_i, \text{sup}_i, \epsilon_i)$**

1	$c2 = c1 \otimes c1$	1	$c2 = c1 \otimes c1$
2	$c3 = c2 \otimes c2$	2	$c3 = \text{modswitch}(c2) \otimes \text{modswitch}(c2)$
3	$c4 = c3 \otimes c3$	3	$c4 = c3 \otimes c3$
4	$c5 = c4 \otimes c4$	4	$c5 = c4 \otimes c4$

**Figure 12: Modulus switch inference on the left program yields the right program.**

However, there are two alternatives. First, changing line 1 to  $c2 = \text{modswitch}(c1 \otimes c1)$ ; this causes the modulus level of  $c2$  to be one less than that of  $c1$ . Second, changing line 2 to  $c3 = \text{modswitch}(c2) \otimes \text{modswitch}(c2)$ ; this leaves the level of  $c2$  unchanged.

Recall that switched operands can only operate with operands at the same level (Section 2). Thus, the former approach initiates a modulus level propagation chain: every use of  $c2$  must ensure that the remaining operands are also at the same modulus level; otherwise the operation fails. However, the latter approach leaves the level of  $c2$  unchanged; this allows program to use  $c2$  as much as possible minimizing the number of changes.

Algorithm 1 presents the modulus switch inference algorithm. The input abstract syntax tree (AST) is assumed to be in Static Single Assignment (SSA) form, a reasonable assumption, as many widely used compiler intermediate representations (IRs) adopt SSA to facilitate optimization.

At a high level, the algorithm constructs a Multiplicative Depth Tree (MD Tree) for a cipher variable and inserts switching operations along the path with the highest multiplicative depth. The function `MulDepthTree` constructs the MD tree. The multiplicative depth is defined as the maximum distance from the root to any leaf node. Given a node `node`, the tree is built by recursively identifying the multiplicative operands that contribute to `node` and representing them as its child nodes. Additive operands are ignored, as they do not directly contribute to multiplicative depth; instead, their corresponding MD subtrees are reused in the construction. For example, the MD tree for  $c_7$  in the program  $c_1 := c_2 \otimes c_3$ ;  $c_4 := c_5 \otimes c_6$ ;  $c_7 := c_4 \oplus c_1$ ,  $c_4$  is as follows:  $c_7$  is the root node with  $c_2$ ,  $c_3$ ,  $c_5$  and  $c_6$  as the children. Note that  $c_4$  and  $c_1$  aren't used; instead their MD trees are used.

The inference function, `Modswitch`, picks a leaf node in the MD tree; inserts modulus switch operations on the operands of the node's parent; and updates the type of the node to reflect the correct modulus level. `Modulus levelizer`, `MSLevel`, transforms all the reaching definitions of the node to use the same modulus level. The definition of `MSLevel` and `TypeCheck` are straightforward and are

thus omitted. Note that every AST transformation uses ILA's type system as a validation framework.

The worst-case runtime complexity of the algorithm is  $O(\ell \cdot n)$ , where  $\ell$  is the length of the modulus chain and  $n$  is the number of multiplications in the circuit. In other words, each multiplication may undergo up to  $\ell$  modulus switch operations. Although achieving optimality is not a goal of this work, the algorithm aims to minimize the number of switching operations while preserving the correctness of the transformation.

Our inference algorithm is designed for loop-free programs; therefore, any loops must be unrolled prior to the inference step. For example, suppose the last iteration of the PSI program (Figure 2) causes an overflow. In this case, the inference process would insert modulus switching operations into lines 14–16 of the unrolled version of the program. In practice, nested loop depth is typically less than 5; as such, loops can be unrolled without significantly increasing runtime complexity.

## 7 Implementation

We extend the core language in Figure 5 with scheme-specific homomorphic operations as well as with other user-friendly features such as finite loops, vectors and matrices in a straightforward manner. Note that all loops have to terminate; otherwise the program violates Theorem 4.4. Under the hood, matrices are implemented as vectors. Interestingly, indexing vector might lead to out-of-bounds accesses if the index is not a constant. Since all ILA programs are terminating, a simple static analysis can help detect OOB accesses. However, it is orthogonal to the current work.

We implement ILA as a retargetable compiler with support for three popular backends—SEAL, OpenFHE and TFHE-rs—for executing the FHE circuits. It supports BGV, BFV and TFHE schemes. The compiler is implemented in Python and invokes backends either by using Python bindings (OpenFHE and SEAL) or by generating Rust code (TFHE-rs). During the setup phase, it instantiates all parameters and constructs the initial environment; statically type checks the environment and program; and if successful, invokes the corresponding backend to execute the circuit.

There are a couple of implementation challenges. First, FHE circuits inherently involve arbitrarily large number arithmetic (e.g., the coefficient modulus can be as large as  $10^{250}$ ). Fortunately, Python supports accurate arithmetic for integers. For floating point calculations, we rely on high-precision libraries such as `numpy`. Second, to determine the noise overflow statically, we need a heuristic estimate to estimate noise growth. In our current implementation

we employed the worst case—most conservative—noise heuristics for  $ILA_{(bfv)}$  [17],  $ILA_{(bgv)}$  [12] and average case heuristics for TFHE [11].

## 8 Evaluation

Our evaluation aims to answer two broad questions. First, *is ILA expressive*, in the sense of handling complex FHE circuits? Second, *is ILA efficient*?

### 8.1 Expressiveness

We evaluated a series of benchmarks that varied in both multiplicative depth (up to approximately 22 levels) and circuit breadth (up to approximately 8000 multiplicative gates). The breadth of a circuit refers to the number of multiplicative gates it contains. While ILA is theoretically capable of supporting circuits with arbitrary depth and breadth, in practice, its capabilities are limited by the constraints of the underlying cryptographic libraries.

Achieving higher multiplicative depth requires a larger coefficient modulus. In Microsoft SEAL, the maximum supported bit count for the coefficient modulus is 881 bits [3], which, in our experiments, corresponded to a maximum multiplicative depth of 21. OpenFHE, employs the Residue Number System (RNS) to represent the coefficient modulus. Although the use of the Residue Number System (RNS) enables support for larger coefficient moduli, it also incurs significantly higher memory overhead [21] and necessitates larger cache sizes for efficient execution [5]. In our experiments, OpenFHE programs with a multiplicative depth greater than 22 could not be executed successfully.

The breadth of a circuit is also limited by ciphertext size. Each ciphertext is represented as a degree- $d$  polynomial, making its size proportional to  $d$ . For instance, when  $d = 32768$ , storing 8000 distinct ciphertexts would require more than 50 GB of memory.

Our evaluation includes all combinations of cipher-cipher and cipher-plain operations. The benchmarks also include computing functions for exponentiation (proxy for multiplicative depth) and fibonacci (proxy for additive depth). This shows that ILA compiler can handle large FHE circuits that are both wide and deep.

Additionally, to demonstrate that ILA can handle complex FHE circuits, we implemented a *private set intersection* (PSI), *private information retrieval* (PIR) and an image processing application. Each of these applications have complex control flow operations involving nested (finite) loops and use rich data structures such as vectors and matrices. PIR models database with 1000 rows.<sup>2</sup> All entries of the database are packed into a plaintext vector and the query as a ciphertext vector containing the secret index. A cipher-plain vector multiplication yields the required information. The image processing application uses matrix (size 1000x1000) operations to apply a filter (can be encrypted or plaintext) to the encrypted image. Note that for larger circuits, we optionally rely on an encoding that packs multiple ciphertexts reducing the size of the circuit.

In all these cases, ILA is able to detect overflows when the multiplicative depth exceeds the noise budget. When type checking succeeds, the compiler invokes the corresponding backend to execute the circuit. Note that all applications can run on multiple backends using different schemes. For example, PIR can run on

<sup>2</sup>Similar to *Wide Database* of Apple's caller ID PIR [1].

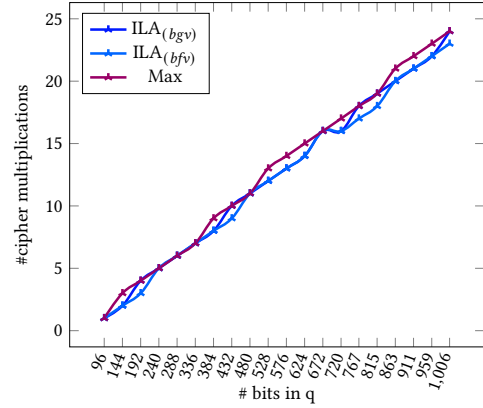


Figure 13: Multiplicative depth of ILA vs possible maximum.

both SEAL and OpenFHE by selecting BGV/BFV scheme; PSI can run on all backends for all schemes.

### 8.2 Efficiency

A type checker that rejects all programs is trivially sound but not useful. In other words, too many false positives limit the practical use. To demonstrate the practicality of ILA, we evaluated ILA's instantiations against various measures.

Since noise estimators employed by ILA type checker are conservative, we evaluated if the type checker admits practical FHE circuits. We performed various experiments to measure how tight ILA's noise estimators are for  $ILA_{(bgv)}$  and  $ILA_{(bfv)}$ . This is measured using the multiplicative depth—the maximal number of sequential homomorphic multiplications that can be performed on fresh ciphertexts without noise overflow. To a good approximation, it can be used as a proxy for the lower bound of the circuit's size [6, 25].

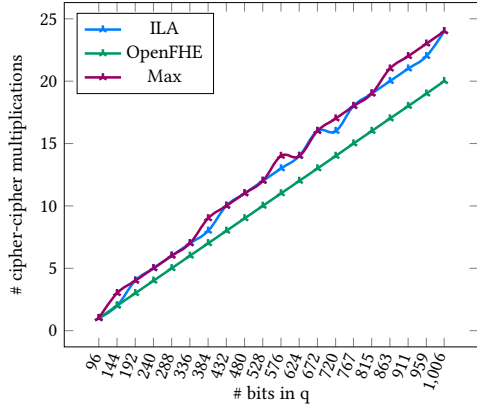
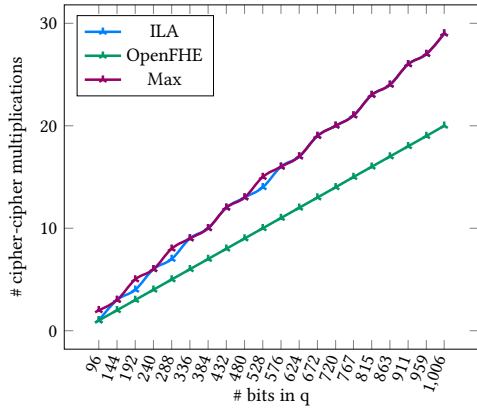
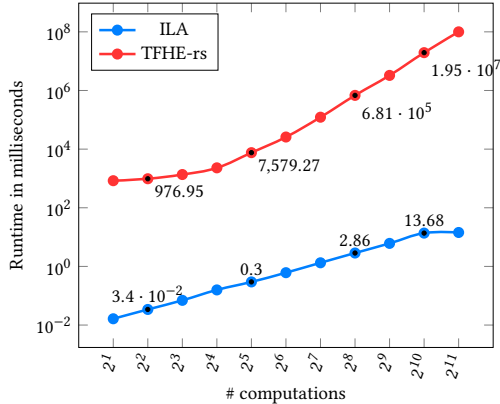
For TFHE, since noise is less of a concern, we measured the runtime of value overflow detection. Figures 13 and 16 summarize the results.

**Multiplicative depth.** To compute the multiplicative depth for a given coefficient modulus  $q$ , we multiply a sequence of fresh ciphers until ILA type checker detected a noise overflow. Using the same scheme parameters, we check whether is a true noise overflow in both SEAL and OpenFHE libraries. Note that the libraries may admit higher multiplicative depth and we record the maximal depth. Figure 13 plots the multiplicative depth obtained by varying the coefficient modulus  $q$ . It shows that ILA's multiplicative depth closely matches the actual maximal multiplicative depth for both BGV and BFV schemes.

Given that OpenFHE is a industrial strength library, we refine the above evaluation by comparing the multiplicative depth of ILA and OpenFHE. Figure 14 shows that ILA always outperforms OpenFHE. This demonstrates that ILA's noise overflow analysis is useful and admits practical FHE circuits.

**Multiplicative depth of OpenFHE vs ILA.** Recall that OpenFHE guarantees correctness of an FHE circuit up to a preselected multiplicative depth. There are two main problems with their estimates, their estimates are not tight, and they treat cipher-cipher and plain-cipher multiplications as the same. Note that plaintext does not



Figure 14: Multiplicative depth of ILA<sub>(bgv)</sub> vs OpenFHE.Figure 15: Plain-cipher mult depth of ILA<sub>(bgv)</sub> vs OpenFHEFigure 16: ILA<sub>(tfhe)</sub> vs TFHE value overflow runtimes.

carry any noise. As a result plain-cipher multiplication result has less noise than a cipher-cipher multiplication. Hence, more plain-cipher multiplications are possible than OpenFHE guarantees. In this experiment, we computed plain-cipher multiplications for BGV. Figure 15 shows that ILA over performed OpenFHE for every value of  $q$ . Appendix B shows full evaluation of ILA for BGV and BFV schemes.

**Value overflow.** In the case of TFHE, the multiplication operation resets the noise in the result. Moreover, the noise growth is very slow with other homomorphic operations. Hence, runtime performance of value overflow detection is a greater concern.

Our goal is to compare the runtime performance of value overflow detections in TFHE and ILA<sub>(tfhe)</sub>. To this end, we construct a FHE circuit with  $t = 2^p$  additions. Note that this allows  $t - 1$  valid additions and overflow occurs with the last addition. The circuit is implemented in both ILA and TFHE-rs. For the latter, we use *overflow\_add* that tracks the value overflows during the circuit execution. We measure the time taken to detect overflows for the same number of operations in both ILA and TFHE-rs.

Figure 16 shows the runtime comparison of ILA<sub>(tfhe)</sub> and TFHE-rs for detecting value overflows when run on a Intel i9 2.3 GHz 8-core processor with 16 GB RAM. Note that we use the average (1000 runs) execution time for ILA<sub>(tfhe)</sub> type checker to offset any randomness in the runs.

Evaluation shows that TFHE-rs is orders of magnitude slower than ILA<sub>(tfhe)</sub>. In all instances, ILA<sub>(tfhe)</sub> was able to detect value overflows in milliseconds, whereas TFHE-rs took hours for larger circuits ( $p \geq 11$ ). Even though ILA<sub>(tfhe)</sub>'s runtime increased with more number of computations, the rate of increase is much slower than that of TFHE-rs. This is unsurprising as FHE circuits are known to be slow, and any dynamic detection inherits the poor performance. Though a GPU support might accelerate the performance of TFHE-rs, ILA<sub>(tfhe)</sub> may still outperform as it does not execute the FHE circuit for overflow detection.

**Modulus Switch inference.** Our modulus switch inference algorithm is an optimization that can further tighten the multiplicative depth that ILA can guarantee. We implemented the proposed algorithm using the Microsoft SEAL library as the backend, and evaluated it across multiple modulus chains ordered. We sorted elements of each modulus chain in decreasing order. Experimental results show that, in almost all tested cases, the multiplicative depth inferred by our MS algorithm matches the maximum achievable depth for the corresponding modulus chain.

## 9 Related Work

**Existing FHE Solutions.** Table 3 summarizes the support for correctness reasoning among the state-of-the-art FHE libraries. Only a few libraries support more than one FHE scheme and all of them have limited—if any—error detection support. Neither OpenFHE [5], SEAL [22], Lattigo [4] or HELib [16] handle value overflows. As a concrete example, if the plaintext modulus is 100, and the decrypted output is 20, then the intended result could be one of 120, -20 or 50720.

SEAL, Lattigo, HELib and OpenFHE [5] support BGV and BFV schemes including modulus switching operation. None of them support modulus switch inference. OpenFHE and TFHE-rs [24] also support TFHE although the former's support is limited in functionality.

Noise overflow issues differ with every library. OpenFHE guarantees are valid up to a preselected depth (chosen by the user) of cipher-cipher operations; however, the guarantees get invalidated in the presence of other homomorphic operations (e.g., cipher-plain multiplication). To overcome this limitation, OpenFHE recommends

FHE Library	Schemes	$O_{value}$	$O_{noise}$	SK Ind	Static
OpenFHE [5]	✓	✗	✗	✗	✗
SEAL [22]	✗	✗	✗	✗	✗
Lattigo [4]	✓	✗	✗	✗	✗
HElib [16]	✗	✗	✓	✓	✗
TFHE-rs [24]	✗	✓	✗	✓	✗
ILA (this work)	✓	✓	✓	✓	✓

**Table 3: The first column indicates support for multiple schemes. The next two columns check for value and noise overflow errors. The fourth column verifies secret key independence, and the final column indicates whether the technique is static.**

estimating the depth parameter that over-approximates all operations as cipher-cipher [2]. Since noise growth is slower for other combinations, the over-approximation leads to a much slower circuit (due to larger parameters). Moreover, it is tricky to get these estimations correct as the depth is a function of fresh ciphertext operations whereas the programs often operate on a series of mutated ciphertexts.

SEAL and Lattigo do not offer any noise estimation whereas HElib supports explicit user queries on noise estimate queries at runtime. Note that the latter setup is undesirable for two reasons. First, a user has to explicitly query whether an overflow has occurred. Second, it can be orders of magnitude slower as FHE circuits are well-known to suffer from poor performance.

TFHE-rs is more nuanced: it supports detecting value overflows but not noise overflows. However, value overflow detection requires running the FHE circuit which is once again slow. In this work, we use a static approach that can be orders of magnitude fast (Section 8). Interestingly, it has a noise management strategy that automatically inserts PBS operations after a preselected operation depth (controlled by the user). Hence, noise overflow might appear to be of lesser concern. However, we argue that it isn't. First, the user may choose to turn it off for some operations (e.g., addition). Second, the operation depth for PBS is chosen apriori and is agnostic to whether a PBS is required. As PBS is computationally expensive, this approach is sub-optimal and further slows down the circuit. More generally, precise PBS insertion would entail noise overflow detection that TFHE-rs lacks.

**Other FHE Compilers.** We refer the reader to a survey on the state-of-the-art in FHE compilers [23]. To the best of our knowledge, ILA is the only work to prove both functional correctness property. ALCHEMY [14] is an embedded DSL in Haskell for using FHE with type-level tracking of ciphertext noise; however, neither do these type-level noise computations come with formal guarantees, nor are the heuristics for noise estimations sound.

Other FHE compilers such as EVA [15], Coyote [20] and others [10, 14, 23] focus on efficient encoding schemes but not formal guarantees. They are orthogonal to our work.

## 10 Conclusion and Future Work

We present ILA, a correctness-oriented IR and an abstract model for FHE circuits. Our IR is backed by a type system that statically tracks low-level quantitative bounds (e.g., ciphertext noise) without using the secret key. Using our type system, we identify and prove a strong

*functional correctness* criterion for ILA circuits. Furthermore, we instantiate ILA with three absolute schemes—BGV, BFV and TFHE—and get functional correctness for free. Comparative evaluation of ILA against three popular FHE libraries shows that ILA's static analysis is sound, tight and efficient.

In future work, we aim to extend ILA to support functional, higher-order programming with FHE. While function types are straightforward, they are not ergonomic without *bound polymorphism*, which would enable types such as  $\forall \alpha. \text{cipher } \alpha \rightarrow \text{cipher } (2 * \alpha)$ . Bound polymorphism in our setting requires extending the noise inference algorithms of the underlying cryptosystems (e.g., BGV) to handle *symbolic* bounds, which will likely require insights from nonlinear arithmetic constraint solving.

Additionally, we aim to instantiate ILA with approximate FHE schemes such as CKKS [9]. This extension primarily requires our notion of an ILA model to be generalized so that Commutativity does not hold on the nose, but holds with some bounded amount of error.

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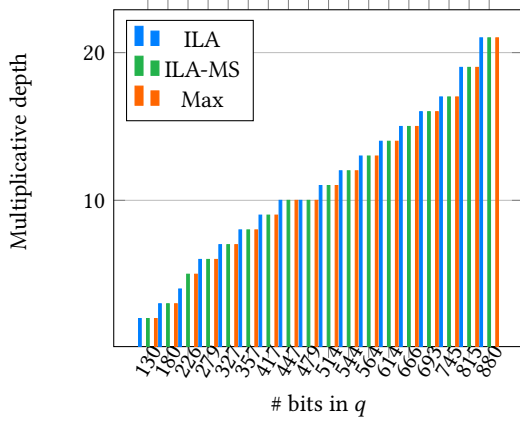


Figure 17: Multiplicative depth before and after modulus switch inference.

### A Modulus Switching

Since the focus of modulus switch inference is to demonstrate the role of the type system in transformation validation, we keep it simple. Our inference algorithm works for loop-free programs. Multiplicative depth computation for ciphertexts inside a loop depends on the loop iteration. One potential way to fix this would be to carry out the inference after unrolling the loop. Since ILA programs only have finite loops, this is feasible.

As an example, suppose that the last iteration of the PSI program (Figure 2) overflows. Then this approach would yield a program where lines 14 – 16 in the unrolled version of the program have modulus switching operations as shown below.

```

12 ... # unroll the inner loop
13 t5 = A[0] # last iteration
14 t6 = modswitch(t5) ⊕ modswitch(t2) # switch levels
15 t7 = modswitch(result) ⊗ t6 # level propagation
16 result = t7 ⊗ modswitch(t4)

```

### B Evaluation

Evaluation shows comparative multiplicative depth of ILA against SEAL and OpenFHE for BGV and BFV schemes. Figure ?? compares ILA<sub>(bgv)</sub> with SEAL whereas Figures 18, 19, 20 and 21 compare ILA with OpenFHE for cipher-cipher and cipher-plain multiplications. Additionally, Figure 17 compares the multiplicative depth before and after modulus switching inference.

### C Proofs

**THEOREM C.1 (SEMANTIC SAFETY: EXPRESSIONS).** *Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash e : \tau$ . Then  $\text{sp}; \Gamma \models e : \tau$ .*

**PROOF.** Recall that we must show that, for all  $\gamma$  such that  $\text{sp}; \Gamma \models \gamma$  (i.e.,  $\gamma(x) \in \llbracket \Gamma(x) \rrbracket^{\text{sp}}$  for all  $x \in \Gamma$ ), there exists a  $v$  such that  $\langle \gamma, e \rangle \Downarrow v$  and  $v \in \llbracket \tau \rrbracket^{\text{sp}}$ .

By induction on the typing derivation (Figure 8).

- Case VAR: We have that  $\langle \gamma, x \rangle \Downarrow \gamma(x)$ ,  $\gamma(x) \in \llbracket \Gamma(x) \rrbracket^{\text{sp}}$ , and  $\tau = \Gamma(x)$ . The result follows.
- Case SUB: The result follows once we show that, for all sorts  $s \in \{\text{cipher}, \text{plain}, \text{msg}\}$ , if  $\alpha \leq_s \alpha'$ , then  $\llbracket s \alpha \rrbracket^{\text{sp}} \subseteq \llbracket s \alpha' \rrbracket^{\text{sp}}$ .

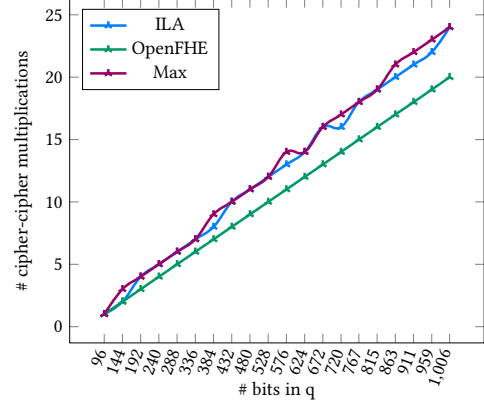


Figure 18: Multiplicative depth of ILA<sub>(bgv)</sub> vs OpenFHE. X-axis shows the number of bits in coefficient modulus q; Y-axis shows number of plain-cipher multiplications estimated by ILA, OpenFHE vs maximal possible (higher is better).

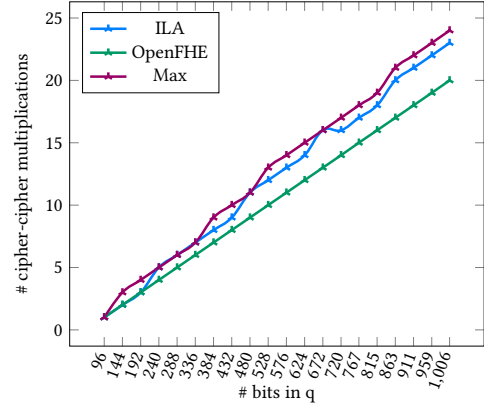


Figure 19: Multiplicative depth of ILA<sub>(bfv)</sub> vs OpenFHE. X-axis shows the number of bits in coefficient modulus q; Y-axis shows number of plain-cipher multiplications estimated by ILA, OpenFHE vs maximal possible (higher is better).

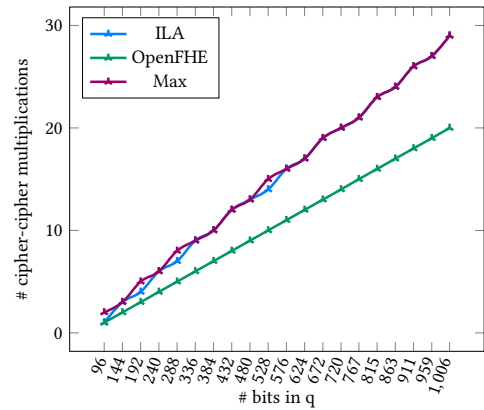
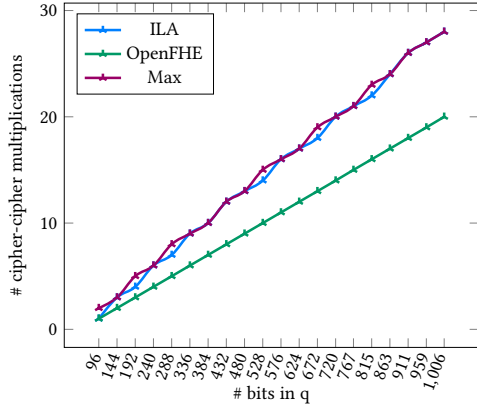


Figure 20: Plain-cipher multiplicative depth of ILA<sub>(bgv)</sub> vs OpenFHE.





**Figure 21: Plain-cipher multiplicative depth of ILA<sub>(bfv)</sub> vs OpenFHE.** X-axis shows the number of bits in coefficient modulus  $q$ ; Y-axis shows number of plain-cipher multiplications estimated by ILA, OpenFHE vs maximal possible (higher is better).

Since  $\llbracket s \alpha \rrbracket^{\text{SP}} = \{v \in \llbracket s \rrbracket \mid |v|_s^{\text{SP}} \leq_s \alpha\}$ , this follows from transitivity of  $\leq_s$ .

- Case CONST: We need to show that, for  $s \in \{\text{msg}, \text{plain}\}$ , for all  $v \in \llbracket s \rrbracket$ ,  $v \in \llbracket s |v|_s^{\text{topub}(\text{sp})} \rrbracket^{\text{SP}}$ . Hence, we must show that  $|v|_s^{\text{topub}(\text{sp})} \leq_s |v|_s^{\text{SP}}$ . For  $s \in \{\text{msg}, \text{plain}\}$ , the right bound is defined to be the left.

- Case OP: We know that for all  $i$ ,  $\text{sp}; \Gamma \models e_i : \tau_i$  and that  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|\tau_i|}) = \alpha$ , and we must prove that  $\text{sp}; \Gamma \models \text{op}(\overrightarrow{e_i}) : s \alpha$ .

To this end, take  $\gamma$  such that  $\text{sp}; \Gamma \models \gamma$ . By induction, we have that  $\langle \gamma, e_i \rangle \Downarrow v_i \in \llbracket \tau_i \rrbracket^{\text{SP}}$  for all  $i$ , and thus  $\langle \gamma, \text{op}(\overrightarrow{e_i}) \rangle \Downarrow v = \llbracket \text{op} \rrbracket(\overrightarrow{v_i})$ . We know that  $\text{sort}(v) = s$ . We must show that  $|\llbracket \text{op} \rrbracket(\overrightarrow{v_i})|_s^{\text{SP}} \leq \alpha$ . First, observe that  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|v_i|_{\text{sort}(\tau_i)}})^{\text{SP}} \leq \llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|\tau_i|}) = \alpha$  by the Downwards Closed axiom. Then, by the Commutativity axiom, we have  $|\llbracket \text{op} \rrbracket(\overrightarrow{v_i})|_s^{\text{SP}} \leq_s \alpha$ .

□

**THEOREM C.2 (SEMANTIC SAFETY: COMMANDS).** Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash c \dashv \Gamma'$ . Then  $\text{sp}; \Gamma \models c \dashv \Gamma'$ .

**PROOF.** Recall that we need to show that whenever  $\text{sp}; \Gamma \models \gamma$  and  $\langle \gamma, c \rangle \Downarrow \gamma'$ , that  $\text{sp}; \Gamma' \models \gamma'$ .

Induct on the typing derivation of  $c$  (Figure 9):

- Case SKIP: immediate, since  $\langle \gamma, \text{skip} \rangle \Downarrow \gamma$ .
- Case ASSGN: we have that  $\langle \gamma, x := e \rangle \Downarrow \gamma[x \mapsto v]$ , where  $\langle \gamma, e \rangle \Downarrow v$ , and we must show that  $\text{sp}; \Gamma[x \mapsto \tau] \models \gamma[x \mapsto v]$ . By Theorem C.1, we have that  $v \in \llbracket \tau \rrbracket \in \llbracket \tau \rrbracket^{\text{SP}}$ . Since we are updating  $\Gamma$  to always point to  $\tau$ , the result follows.
- Case SEQ: immediate, since we know by induction that any post-state of  $c_1$  will satisfy its output type context, which is assumed to be the input type context of  $c_2$ .
- Case IF: we have that  $\langle \gamma, \text{if } e \text{ then } c_1 \text{ else } c_2 \rangle$  evaluates as  $\langle \gamma, c_1 \rangle \Downarrow \gamma_1$  if  $\langle \gamma, e \rangle$  evaluates to  $\llbracket \text{true} \rrbracket()$ , and  $\langle \gamma, c_2 \rangle \Downarrow \gamma_2$  otherwise. By induction, we have that  $\text{sp}; \Gamma_1 \models \gamma_1$  and

$\text{sp}; \Gamma_2 \models \gamma_2$ . We must show that if  $\Gamma_1 \sqcap \Gamma_2 \sim \Gamma'$ , then  $\text{sp}; \Gamma' \models \gamma_1$  and  $\text{sp}; \Gamma' \models \gamma_2$ .

Take  $x \in \Gamma'$ . Then, by assumption,  $x \in \Gamma_1$  and  $\Gamma_1(x) \leq \Gamma'(x)$ . Since  $\gamma_1(x) \in \llbracket \Gamma_1 \rrbracket^{\text{SP}}$ , we have that  $\gamma_1(x) \in \llbracket \Gamma' \rrbracket^{\text{SP}}$  since subtyping respects  $\llbracket \cdot \rrbracket^{\text{SP}}$ . The argument is the same for  $\Gamma_2$ .

□

**THEOREM C.3 (MESSAGE EQUIVALENCE: EXPRESSIONS).** Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash e : \tau$  and  $\text{sp}; \Gamma \models \gamma$ . Let

$$\gamma' = \{x \mapsto \text{interp}_{\text{sort}(\Gamma(x))}^{\text{SP}}(\gamma(x))\}.$$

Then,  $\langle \gamma, e \rangle \Downarrow v$  and  $\langle \gamma', e \rangle \Downarrow_{\text{msg}}^{\text{SP}} v_{\text{msg}}$  such that  $\text{interp}_{\text{sort}(v)}^{\text{SP}}(v) = v_{\text{msg}}$ .

**PROOF.** By induction on the typing derivation.

- Case VAR: we have that if  $e = x$ , then  $v = \gamma(x)$  and  $v_{\text{msg}} = \gamma'(x) = \text{interp}_{\text{sort}(\Gamma(x))}^{\text{SP}}(\gamma(x))$ . The result follows from the fact that  $\text{sort}(\gamma(x)) = \text{sort}(\Gamma(x))$ , since  $\text{sp}; \Gamma \models \gamma$ .
- Case SUB: the inductive hypothesis on  $e$  is equivalent to the result, since the sort of  $\tau$  does not change (only the bound).
- Case CONST: if  $e = v \in \llbracket s \rrbracket$ ,  $\langle \gamma, v \rangle \Downarrow v$  and  $\langle \gamma', v \rangle \Downarrow_{\text{msg}}^{\text{SP}} \text{interp}_{\text{sort}(v)}^{\text{SP}}(v)$ . The result follows.
- Case OP: We have that  $e = \text{op}(\overrightarrow{e_i})$  where  $\text{op} : \overrightarrow{s_i} \rightarrow s$ , and  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|\tau_i|})$  is defined. Inductively, we have that for each  $i$ ,  $\langle \gamma, e_i \rangle \Downarrow v_i$  and  $\langle \gamma', e_i \rangle \Downarrow_{\text{msg}}^{\text{SP}} v_{i, \text{msg}}$  such that  $\text{interp}_{s_i}^{\text{SP}}(v_i) = v_{i, \text{msg}}$ . We also have that  $\langle \gamma, \text{op}(\overrightarrow{e_i}) \rangle \Downarrow \llbracket \text{op} \rrbracket(\overrightarrow{v_i})$  and that  $\langle \gamma', \text{op}(\overrightarrow{e_i}) \rangle \Downarrow_{\text{msg}}^{\text{SP}} \llbracket \text{op} \rrbracket_{\text{msg}}(\overrightarrow{v_{i, \text{msg}}})$ . Also, observe that  $\llbracket \text{op} \rrbracket(\overrightarrow{v_i}) \in \llbracket s \rrbracket$  by assumption. Putting everything together, we need to show that

$$\text{interp}_s^{\text{SP}}(\llbracket \text{op} \rrbracket(\overrightarrow{v_i})) = \llbracket \text{op} \rrbracket_{\text{msg}}(\text{interp}_{s_i}^{\text{SP}}(v_i)).$$

This is the conclusion of Commutativity in the model. To apply Commutativity, we must show that

$$\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|v_i|_{s_i}^{\text{SP}}})$$

is defined. From Semantic Safety, we have that  $v_i \in \llbracket \tau_i \rrbracket^{\text{SP}}$  for all  $i$ , and hence  $|v_i|_{s_i}^{\text{SP}} \leq_{s_i} |\tau_i|$ . Since  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{topub}(\text{sp})}(\overrightarrow{|\tau_i|})$  is defined, the result follows from the Downwards Closed axiom in the model.

□

**THEOREM C.4 (MESSAGE EQUIVALENCE: COMMANDS).** Suppose that  $\text{topub}(\text{sp}); \Gamma \vdash c \dashv \Gamma'$  and  $\text{sp}; \Gamma \models \gamma$ . Let  $\gamma' = \{x \mapsto \text{interp}_{\text{sort}(\Gamma(x))}^{\text{SP}}(\gamma(x))\}$ . Then, if  $\langle \gamma, c \rangle \Downarrow \gamma_1$ , we have that  $\langle \gamma', c \rangle \Downarrow_{\text{msg}}^{\text{SP}} \gamma'_1$  such that, for all  $x \in \gamma_1$ ,  $\text{interp}_{\text{sort}(\gamma_1(x))}^{\text{SP}}(\gamma_1(x)) = \gamma'_1(x)$ .

**PROOF.** Follows by induction on the typing derivation.

- Case SKIP: We have that  $\langle \gamma, \text{skip} \rangle \Downarrow \gamma$  and  $\langle \gamma', \text{skip} \rangle \Downarrow_{\text{msg}}^{\text{SP}} \gamma'$ . The result follows by assumption.
- Case ASSGN: We have that  $\langle \gamma, y := e \rangle \Downarrow \gamma[y \mapsto v]$  where  $\langle \gamma, e \rangle \Downarrow v$  and  $\langle \gamma', y := e \rangle \Downarrow_{\text{msg}}^{\text{SP}} \gamma'[y \mapsto v']$  where  $\langle \gamma', e \rangle \Downarrow_{\text{msg}}^{\text{SP}} v'$ .

If  $x \neq y$ , the result follows by assumption. If  $x = y$ , the result follows by Message Equivalence on expressions (Theorem C.3).

- Case SEQ: The result follows, since the postcondition of the inductive hypothesis for  $c_1$  matches the precondition of the inductive hypothesis for  $c_2$ .
- Case IF: Note that since the guard  $e$  of the if statement has type  $\text{msg } \alpha$ , and  $\text{interp}_{\text{msg}}^{\text{sp}}(v) = v$  for all  $v$ , we will have that if  $e$  then  $c_1$  else  $c_2$  will evaluate in the same direction for both  $\gamma$  and  $\gamma'$ . If  $e$  evaluates to  $\llbracket \text{true} \rrbracket()$ , then the result follows by the inductive hypothesis on  $c_1$ ; otherwise, the result follows by the inductive hypothesis on  $c_2$ .

□

## D ILA<sub>(tfhe)</sub> instantiation

We introduce ILA<sub>(tfhe)</sub>, an instantiation of ILA model for the CG-GI/TFHE scheme. TFHE is a fast bootstrapping GLWE scheme.

Definition D.1 shows the full instantiation. Note that it closely follows the definition of ILA model (see Definition 4.1, Section 4).

DEFINITION D.1 (ILA<sub>(tfhe)</sub>). *The ILA<sub>(bgv)</sub> model is defined as follows:*

- *Bounds sets:*
  - $B_{\text{msg}} = \emptyset$ ;
  - $B_{\text{plain}} = \{(\inf, \sup, \epsilon) \mid \inf \leq \sup \text{ and } \epsilon \in \mathbb{N}\}$  ordered by  $\leq_{\text{plain}}$ ;
  - $B_{\text{cipher}} = \{(\text{id}, \inf, \sup, \epsilon) \mid \inf \leq \sup, \text{id} \in \{\text{LWE}, \text{RLWE}, \text{RGSW}\} \text{ and } \epsilon \in \mathbb{N}\}$  ordered by  $(\text{id}, \inf_1, \sup_1, \epsilon_1) \leq_{\text{cipher}} (\text{id}, \inf_2, \sup_2, \epsilon_2)$  if  $\inf_2 \leq \inf_1 \leq \sup_1 \leq \sup_2$  and  $\epsilon_1 \leq \epsilon_2$ ;
- *Mappings for bounds computation:*
  - $|m|_{\text{msg}}^{\text{pp}} = \emptyset$ ;
  - $|p|_{\text{plain}}^{\text{pp}} = (\inf, \sup)$ ; computed as in ILA<sub>(bgv)</sub>
  - $|c|_{\text{cipher}}^{\text{sp}} = (\text{id}, \inf, \sup, \epsilon)$  computation similar to ILA<sub>(bgv)</sub>
- *Decoding on plaintexts:*  $\text{interp}_{\text{plain}}^{\text{pp}}(p) = \text{decode}(p)$ ;
- *Decryption on ciphertexts:*  $\text{interp}_{\text{cipher}}^{\text{sp}}(\text{ct}) = \text{decode}(\text{decrypt}(\text{ct}))$ ;
- *Operator op*  $\in \{\oplus, \otimes, \times, \text{cmux}, \boxtimes, \square, \text{pbs}\}$  defined as follows:
  - $\llbracket \otimes \rrbracket : \{\text{cipher}, \text{plain}\}, \{\text{cipher}, \text{plain}\} \rightarrow \{\text{cipher}, \text{plain}\}$ ;
  - $\llbracket \oplus \rrbracket : \{\text{cipher}, \text{plain}\}, \{\text{cipher}, \text{plain}\} \rightarrow \{\text{cipher}, \text{plain}\}$ ;
  - $\llbracket \text{cmux} \rrbracket : \{\text{cipher}, \text{cipher}, \text{cipher}\} \rightarrow \{\text{cipher}\}$ ;
  - $\llbracket \square \rrbracket : \{\text{cipher}, \text{cipher}\} \rightarrow \{\text{cipher}\}$ ;
  - $\llbracket \text{pbs} \rrbracket : \{\text{cipher}, \text{cipher}, \text{cipher}\} \rightarrow \{\text{cipher}\}$ ;
  - $\llbracket \times \rrbracket : \text{int}, \text{cipher} \rightarrow \text{cipher}$ ; and
  - *bounds manipulation mapping*  $\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}$  shown in Table 4.

We prefix 'cipher' with id whenever id is significant. For simplicity, we assume that  $\text{LWE} < \text{RLWE} < \text{RGSW}$ .

D.0.1 *Instantiation of ILA Functional Correctness.* The commutativity and downwards closure axioms are similar to that of ILA<sub>(bgv)</sub>. Since ILA<sub>(tfhe)</sub> is a valid ILA model, we thus get Theorem 4.4 for free.

Sorts	$s$	$\in$	$\{\text{msg}, \text{plain}, \text{cipher}\}$
Bounds	$\alpha_s$	$\in$	$B_s$
Types	$\tau$	$::=$	$\text{cipher } \alpha_{\text{cipher}} \mid \text{plain } \alpha_{\text{plain}} \mid \text{msg } \alpha_{\text{msg}}$
Contexts	$\Gamma$	$::=$	$\cdot \mid \Gamma, x : \tau$

Figure 22: ILA Extensions

$\boxed{\text{pp}; \Gamma \vdash e : \tau}$	$\frac{x : \tau \in \Gamma}{\text{pp}; \Gamma \vdash x : \tau}$	$\frac{\text{sort}(v) = s}{\text{pp}; \Gamma \vdash v : s \mid v _s^{\text{pp}}}$	2040
			2041
			2042
	$\frac{\text{pp}; \Gamma \vdash e_i : s \alpha_i}{\text{pp}; \Gamma \vdash [e_1, \dots, e_n] : \text{vec } s(\alpha_1, \dots, \alpha_n)}$		2043
			2044
			2045
$\text{op} : \vec{s}_i \rightarrow s$	$(\forall i, \text{pp}; \Gamma \vdash e_i : \tau_i \quad \text{sort}(\tau_i) = s_i)$	$\llbracket \text{op} \rrbracket_{\text{bnd}}^{\text{pp}}(\vec{\tau}_i) = \alpha$	2046
			2047
	$\text{pp}; \Gamma \vdash \text{op}(\vec{e}_i) : s \alpha$		2048

Figure 23: ILA Typing: Expression including vectors

## E ILA<sub>(bgv)</sub> modswitch Typing Rule

As yet another example, we show the typing rule for modswitch operation using the bounds computation from Table 1. Recall that modulus switching reduces both the modulus level and noise on a ciphertext. Rule T-MS states that modulus switching reduces the noise by  $\frac{q_{\omega-1}}{q_{\omega}}$  with some rounding error  $B_r$ . Additionally, the level of the ciphertext is lowered from  $\omega$  to  $\omega - 1$ . Notice that the resulting noise  $\epsilon'$  is always lower than  $\epsilon$  due to decreasing chain property of modulus levels.

T-MS	$\text{pp}; \Gamma \vdash e : \text{cipher } \langle \inf, \sup, \epsilon, \omega \rangle$
	$0 \leq \omega - 1 < L \quad \epsilon' = \frac{q_{\omega-1}}{q_{\omega}} \epsilon + B_r \leq l_{\omega-1}$
	$\text{pp}; \Gamma \vdash \text{modswitch}(e) : \text{cipher } \langle \inf, \sup, \epsilon', \omega - 1 \rangle$

## F ILA Extensions

Op	$\llbracket \cdot \rrbracket_{\text{bnd}}^{\text{PP}}$	Conditions
$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{PP}}(\overrightarrow{\text{id}_i, \text{inf}_i, \text{sup}_i, \epsilon_i})$	$= (\text{id}, \text{inf}, \text{sup}, \epsilon)$	$\text{id}_i \in \{\text{LWE}, \text{RLWE}\}$ and $\text{id} = \max\{\text{LWE}, \text{RLWE}\}$
$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{PP}}(\overrightarrow{\text{inf}_i, \text{sup}_i})$	$= (\text{inf}, \text{sup})$	
$\llbracket \oplus \rrbracket_{\text{bnd}}^{\text{PP}}(\text{inf}_1, \text{sup}_1), (\text{id}, \text{inf}_2, \text{sup}_2, \epsilon)$	$= (\text{id}, \text{inf}, \text{sup}, \epsilon)$	
where		and in all cases
$\text{inf} := \text{inf}_1 + \text{inf}_2, \text{sup} := \text{sup}_1 + \text{sup}_2, \epsilon := \epsilon_1 + \epsilon_2$		$-t/2 \leq \text{inf} \leq \text{sup} < t/2$ and $\epsilon \leq q/2t$
$\llbracket \otimes \rrbracket_{\text{bnd}}^{\text{PP}}(\overrightarrow{\text{inf}_i, \text{sup}_i})$	$= (\text{inf}, \text{sup})$	
$\llbracket \boxtimes \rrbracket_{\text{bnd}}^{\text{PP}}(\overrightarrow{\text{RGSW}, \text{inf}_i, \text{sup}_i, \epsilon_i})$	$= (\text{RGSW}, \text{inf}, \text{sup}, f'(\epsilon_1, \epsilon_2))$	$f'(\epsilon_1, \epsilon_2) \leq q/t$
$\llbracket \square \rrbracket_{\text{bnd}}^{\text{PP}}(\text{RGSW}, \text{inf}_1, \text{sup}_1, \epsilon_1), (\text{LWE}, \text{inf}_2, \text{sup}_2, \epsilon_2)$	$= (\text{RLWE}, \text{inf}, \text{sup}, g(\epsilon_1, \epsilon_2))$	$g(\epsilon_1, \epsilon_2) \leq q/t$
$\llbracket \square \rrbracket_{\text{bnd}}^{\text{PP}}(\text{RGSW}, \text{inf}_1, \text{sup}_1, \epsilon_1), (\text{RLWE}, \text{inf}_2, \text{sup}_2, \epsilon_2)$	$= (\text{RLWE}, \text{inf}, \text{sup}, g'(\epsilon_1, \epsilon_2))$	$g'(\epsilon_1, \epsilon_2) \leq q/t$
		where
		$\text{inf} := \min\{\text{inf}_1 * \text{inf}_2, \text{inf}_1 * \text{sup}_2, \text{sup}_1 * \text{inf}_2, \text{sup}_1 * \text{sup}_2\}$
		$\text{sup} := \max\{\text{inf}_1 * \text{inf}_2, \text{inf}_1 * \text{sup}_2, \text{sup}_1 * \text{inf}_2, \text{sup}_1 * \text{sup}_2\}$
$\llbracket \times \rrbracket_{\text{bnd}}^{\text{PP}}(n, (\text{id}, \text{inf}, \text{sup}, \epsilon))$	$= (\text{id}, \text{inf}', \text{sup}',  n * \epsilon )$	$\text{inf}' = \min\{n * \text{inf}, n * \text{sup}\}$ and $\text{sup}' = \max\{n * \text{inf}, n * \text{sup}\}$
		if $-t/2 \leq \text{inf}' \leq \text{sup}' < t/2$ and $ n * \epsilon  \leq q/t$
$\llbracket \text{pbs} \rrbracket_{\text{bnd}}^{\text{PP}}(\text{RGSW}, \text{inf}_0, \text{sup}_0, \epsilon_0)(\text{LWE}, \text{inf}, \text{sup}, \epsilon)$	$= (\text{LWE}, \text{inf}, \text{sup}, \epsilon_b)$	if $\max\{\log_2  \text{inf} , \log_2  \text{sup} \} \leq (\log_2(t) - 1)$
		if $-t/2 \leq \text{inf}_0 \leq \text{sup}_0 < t/2$

**Table 4: ILA<sub>(tfhe)</sub>: ILA model for TFHE scheme illustrating key homomorphic operations. Note that input operand has the type  $(\text{id}_i, \text{inf}_i, \text{sup}_i, \epsilon_i)$  (for ciphertext)**