#### SPEC® CPU2017 Integer Speed Result Copyright 2017-2025 Standard Performance Evaluation Corporation Supermicro SPECspeed2017\_int\_base = 1.16 MegaDC ARS-211M-NR SPECspeed2017 int peak **Mot Run** (R13SPD, Ampere AmpereOne A192-32X) Aug-2025 **CPU2017 License:** 001176 Test Date: Hardware Availability: Aug-2024 **Test Sponsor:** Supermicro Tested by: Software Availability: Jul-2023 Supermicro **Threads** 0 1.00 600.perlbench\_s 1 1.95 602.gcc\_s 1 605.mcf s 1 620.omnetpp\_s 1 | 623.xalancbmk\_s 1 |-0.924 625.x264\_s 1 631.deepsjeng\_s 1 641.leela\_s 1 648.exchange2\_s 657.xz\_s 1 SPECspeed 017\_int\_base (1.16) Hardware Software

CPU Name: AmpereOne A192-32X

Max MHz.: 3200 Nominal: 3200

Enabled: 192 cores, 1 chip

Orderable: 1 chip

16 KB I + 64 KB D on chip per core Cache L1:

2 MB I+D/on chip per core L2:

64 MB J+D on chip per chip L3:

Other: None

512 GB (8 x 64 GB 2Rx4 PC5-5600B-R, Memory:

502.223 GB fixme: If using DDR4, the format is:

NGB (N x N GB nRxn PC4-nnnnX-X)'

**\$**torage: 828 GB add more disk info here

Other: CPU Cooling: Air OS: Fedora release 42 (Adams) 6.14.0-63.fc42.aarch64

C/C++/Fortran: Version 13.2.0 of GCC, the Compiler:

**GNU** Compiler Collection

Parallel:

Firmware: Version 1.0 released Jul-2024

File System: xfs

System State: Run level 3 (multi-user)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Jemalloc memory allocator library v5.3.0 Other:

#### **Errors**

'reportable' flag not set during run

648.exchange2 s (base) did not have enough runs!

623.xalancbmk s (base) did not have enough runs!

605.mcf\_s (vase) did not have enough runs!

631.deepsjeng\_s (base) did not have enough runs!

641.leela\_s (base) did not have enough runs!

620.omnetpp\_s (base) did not have enough runs!

657.xz s (base) did not have enough runs!

600.perlbench s (base) did not have enough runs!

625.x264\_s (base) did not have enough runs!

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SPECspeed2017\_int\_base =

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECspeed2017 int peak **Mot Run** 

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

1.16

### **Errors** (Continued)

602.gcc s (base) did not have enough runs! Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsuy/ for information about how to get rid of this error.

#### Results Table

	Bage									Peak					
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Sec	onds	Ratio	Timeads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	1	<u>1137</u>	<u>1.56</u>			\	/ )	$\Big)$							
602.gcc_s	1	<u>2040</u>	1.95		$\langle \rangle$										
605.mcf_s	1	<u>2117</u>	2.23												
620.omnetpp_s	1	<u>1548</u>	<u>1.05</u>				$\overline{}$	/							
623.xalancbmk_s	1	<u>1877</u>	<u>0.755</u>				)/								
625.x264_s	1	<u>1909</u>	0.924				•								
631.deepsjeng_s	1	<u>1154</u>	1/24												
641.leela_s	1	<u>2560</u>	<u>0.666</u>		NV	1									
648.exchange2_s		<i>/</i> 1	$\bigcup \mathcal{A}$												
657.xz_s	1	6627	0.933												

SPECspeed2017\_int\_base

1.16 Not Run

SPECspeed2017\_jrft\_peak =

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### **Compiler Notes**

were compiled on a system with 1x AmpereOne CPU 2 GB Memory using Fedora Linux 37

#### **Submit Notes**

The config file option 'submit' was used.

'numactl' was used to bind copies to the cores.

### **Operating System Notes**

'ulimit -s unlimited' was used to set environment stack size

Set dirty\_ratio=8 to limit dirty cache to 8% of memory

echo 8 | sudo tee /proc/sys/vm/dirty\_ratio

Set swappiness=1 to swap only if necessary

echo 1 | sudo tee /proc/sys/vm/swappiness

Set zone reclaim mode=1 to free local node memory and avoid remote memory

echo 1 | sudo tee /proc/sys/vm/zone\_reclaim\_mode

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SPECspeed2017\_int\_base \( \) 1.16

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

CPU2017 License: 001176
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SPECspeed2017\_int\_peak Not Run

Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

### Operating System Notes (Continued)

Set drop\_caches=3 to reset caches before invoking runcpu

echo 3 | sudo tee /proc/sys/vm/drop\_caches

Set numa\_balancing=0 to disable automatic numa balancing

echo 0 | sudo tee /proc/sys/kernel/numa\_balancing

Switch off all ktune and tuned setting

sudo tuned-adm off

Transparent huge pages set to 'never'

sudo bash -c "echo never > /sys/kernel/mm/transparent\_hugepage/enabled"

runcpu command invoked through numactl i

1P: numactl --interleave=0-3 runcpu <etc>

2P: numactl --interleave=all runcpy <etc>

### General Notes

Jemalloc v5.3.0 is available via

https://github.com/jemalloc/jemalloc/releases/download/5.3.0/jemalloc-5.3.0.tar.bz2

It was built on Fedora Lynux 37 using Version 13.2.0 of GCC

The configure options and

"--with-lg-page=16" for building libjemalloc.so, and

"--with-lg-quantum=37 --with-lg-page-18" for building libjemalloc\_ext.so

Tuned MALLOC\_CONF in terms of https://jemalloc.net/jemalloc.3.html

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

#### **Platform Notes**

Note: lscpu is not able to detect the SLC.

SNC is defined at https://developer.arm.com/documentation/100180/0103/bry1436285730281 BIOS Settings:

Sub-NOMA Mode = Quadrant

Sysinfo program /root/mte/spec17/bin/sysinfo

Rev: r5274 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on fedora Tue Aug 12 12:52:44 2025

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

\*

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# Supermicro

**Tested by:** 

SPECspeed2017\_int\_base =

SPECspeed2017 int peak

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

Supermicro

CPU2017 License: 001176
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Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

1.16

Mot Run

```
Platform Notes (Continued)
```

```
* Did not identify cpu model.
                               If you would
 like to write your own sysinfo program, see
* www.spec.org/cpu2017/config.html#sysinfo
  0 "physical id" tags found. Perhaps this is an older system,
* or a virtualized system. Not attempting to guess how to
 count chips/cores for this system.
      192 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
From lscpu:
                                            aarch64
    Architecture:
                                           64-bit
     CPU op-mode(s):
     Byte Order:
                                           Little Endian
```

CPU(s): On-line CPU(s) lis 0 - 191Vendor ID: Ampere BIOS Vendor ID: Ampere (R) Model name: Ampere-la BIOS Model name AmpereOne (R) A192-32X CPU @ 3.2GHz BIOS CPU family Model: 0 Thread(s) per core 1

192 Core(s) per socket Socket(s): 1 Stepping: 0x0Frequency boost: disabled (PU(s) scaling MHz: 81% CPU max MHz: 3200.0000 CPU min MHz: 1000.0000 BogoMIRS: 2000.00

Vulnerability Itlb multihit:

Vulnerability L1tf:

Flags: fp asimd evtstrm aes pmull sha1 sha2 crc32 atomics fphp asimdhp cpuid asimdrdm jscvt fcma lrcpc dcpop sha3 sm3 sm4 asimddp sha512 asimdfhm dit uscat ilrcpc flagm ssbs sb paca pacg dcpodp flagm2 frint i8mm bf16 rng bti mte ecv mte3

Not affected

Not affected

Lli cache:

Lli cache:

Lli cache:

3 MiB (192 instances)

3 MiB (192 instances)

384 MiB (192 instances)

10 MiB (192 instances)

384 MiB (192 instances)

10 MiB (192 instances)

10 MiB (192 instances)

10 MiB (192 instances)

11 NUMA node(s):

10 Vulnerability Gather data sampling:

10 Vulnerability Gather data sampling:

11 Not affected

Not affected

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SPECspeed2017\_int\_base =

SPECspeed2017 int peak

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

1.16

Mot Run

#### Platform Notes (Continued)

```
Vulnerability Mds:
                                            Not affected
     Vulnerability Meltdown:
                                            Not affected
     Vulnerability Mmio stale data:
                                            Not affected
     Vulnerability Reg file data sampling
                                            Not affected
     Vulnerability Retbleed:
                                            Not affected
                                            Not affected
     Vulnerability Spec rstack overflow:
     Vulnerability Spec store bypass:
                                           Itigation; Speculative Store Bypass disabled
     via prctl
     Vulnerability Spectre v1:
                                            Mitigation __user pointer sanitization
     Vulnerability Spectre v2:
                                            Not affected
     Vulnerability Srbds:
                                            Not affected
     Vulnerability Tsx asymc abort;
                                            Not affected
   WARNING: the 'lscpu' utility claims that 1 "Socket(s)" were seen, which does not match
   the could not determine "physyical id"s seen in /proc/cpuinfo. The tester should
   verify the count independently
From numactl --hardware WARNING:
                                  a numact 'node' might or might not correspond to a
physical chip.
From /proc/meminfo
   MemTotal:
                   526618556
                             kΒ
   HugePages_Total;
                       2048
   Hugepagesize:
From /etc/*release* /etc/*version*
   fedora-release: Fedora release 42 (Adams)
   os-release:
      NAME="Fedora Linux"
      VERSION= 42 (Adams)"
      RELEASE_TYPE=stable
      ID fedora
      VERSION_ID=\2
      VERSION_CODENAME=""
      PLATFORM_ID="platform:f42"
      PRETTY_NAME="Fedora Linux 42 (Adams)"
   redhat-release: Fedora release 42 (Adams)
   system-release: Fedora release 42 (Adams)
   system_release-cpe: cpe:/o:fedoraproject:fedora:42
   Linux fedora 6.14.0-63.fc42.aarch64 #1 SMP PREEMPT_DYNAMIC Mon Mar 24 20:18:11 UTC
   2025 aarch64 GNU/Linux
Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown):
                                   Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
```

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SPECspeed2017\_int\_base =

MegaDC ARS-211M-NR

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SPECspeed2017 int peak Mot Run

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Aug-2025 Test Date: Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

#### Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Not affected

SPEC is set to: /root/mte/spec17

Filesystem Type Size Used Ava Use Mounted on

/dev/nvme0n1p4 xfs 828G 47G 781G

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system day a which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS EDK II 04.04.00004001 2025-02-04 22.23:30 02/04/2025

8x SK Hynix HMCG94AGBRAI81N 64 GB 2 rank 5600

(End of data from sysinfo program)

### Compiler Version Notes

<u>4</u>|-----

CC 600.perlbench\_s/pase) 602.gc base) 605.mcf\_s(base) 625.x264\_s(base) 657.xz s(base/

clang version 18/1.0rc git@github.com:UT-Security/llvm-mte.git

464a00b150ec9bb9d88363bf3016ce0778bc0dde)

Target: aarch64-unknown-linux-gnu

Thread model: posix

InstalledDir: Yhome/noah/code/llvm-mte/tagcfi/build//bin

CXXC 620 omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base) 641.leela\_s(base)

clarg version 18.1.0rc (git@github.com:UT-Security/llvm-mte.git

464a00b150c29bb9d88363bf3016ce0778bc0dde)

Target: aarch64-unknown-linux-gnu

Thread model: posix

InstalledDir: /home/noah/code/llvm-mte/tagcfi/build//bin

### **Base Unknown Flags**

600.perlbench\_s: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang -std=c99 --target=aarch64-linux-qnu

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## Supermicro

SPECspeed2017\_int\_base \( \) 1.16

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECspeed2017\_int\_peak Not Run

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

### Base Unknown Flags (Continued)

```
600.perlbench s (continued):
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../do/mloads/gcc/gcc-linaro-123.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CC)
"/home/noah/code/llvm-mte/tagcfi/build//bin/clang
-std=c99 --target=aarch64-linux-qnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gco/gcc-1/haro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in LD) "-fgnu89-inline -fcommonARRAY(0x1f61a420)
"-static -unwindlib=libunwindARRAY(0x1f61a270)
"/home/noah/code/llvm-mte/tagcfi/downloads/glibc/.bu/ld//libc.aARRAY(0x1f7361e0)
602.gcc_s: "/home/noah/code/ll/m-mte/tagcfl/build//bin/clang
-std=c99 --target=aarch64-linux-gau
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build .../down/oads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CC)
//home/noah/code/llvm-mte/tagcfl/build/bin/clang
-std=c99 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/con///llvm-mte/tagcfi/htifd//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in LD) "-fgnu89-inline fcommonARRAY (0x1f7252b8)
"-static -unwindlib=libunwindARRAY(0x1f725078)
"/home/noah/code/lvm-mt/e/tagcfi/downloads/glibc/.build//libc.aARRAY(0x1f735c70)
605.mcf_s: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang
-std=c99 --target=aarch64-linux-gnu
-march=armv8-a-memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/
(in CC)
home noah code/Nym-mte/tagcfi/build//bin/clang
-std=c99 -target=aarch64-linux-gnu
-march=armv8-a+memtag
 c-tolchain=/home/roah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/
(in LD) "-fgnu89 Inline -fcommonARRAY(0x1f735910)
"-static -upwindlib=libunwindARRAY(0x1ea60040)
"/home/nah/code/llvm-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x1f735988)
620.omnetp/s:"/home/noah/code/llvm-mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
//home/noah/code/llvm-mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch64-linux-gnu
-march=armv8-a+memtag
```

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SPECspeed2017\_int\_base = 1.16

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECspeed2017 int peak Mot Run

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro Tested by: Supermicro

Aug-2025 Test Date: Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

### Base Unknown Flags (Continued

#### 620.omnetpp s (continued): --gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linar-12.31-2003.06-x86\_64\_aarch64-linux-gnu/" (in LD) "-static -unwindlib=libunwindARRAY(0x1f6/249%) "/home/noah/code/llvm-mte/tagcfi/downloads/glibc/.build//libc/aARRAY(0x1f7356e8) 623.xalancbmk\_s: "/home/noah/code/llvm-mte/taggf1/build//bin/clang++

-std=c++03 --target=aarch64-linux-gnu

-march=armv8-a+memtag

--gcc-toolchain=/home/noah/code/llvm-mte/tagc **√**build//../do wnloads/qcc/q linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CXX)

"/home/noah/code/llvm-mte/tagsfi/bui/ld//bin/clang+

-std=c++03 --target=aarch64-linux-gnu

-march=armv8-a+memtag

--gcc-toolchain=/home/noah/code/llvm-mte/tagch/buhld//../dowhlords/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in TD)

-static -unwindlib=libunwindARRAY(0x1f6130f8)

"/home/noah/code/llvm-mte/kagcfl/downloads/glibc/.build//libc.aARRAY(0x1f73ea38)

625.x264\_s: "/home/noah/code/Nvw/mce/tagcfi/build//bin/clang

-std=c99 --target=aarch64-1ihux-gnu

-march=armv8-a+memtag

--gcc-toolchain=/home/noah/code 1 vm-mte/agcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CC)

"/home/noah/code/llvm-mte/tagcfi/build//bin/clang

-std=c99 --target=aarch64-linux-gnu

-march=armv8-a+memtag

--gcc-toolcharn=/homexnoah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD) "-fgru89-inline -fcommonARRAY(0x1f783e30)

-static Hunwindlib-libunwindARRAY(0x1f783ba8)

home/noal/code/llvm/mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x1f78a2c0)"

631.deepsjeng\_s."/home/noah/code/llvm-mte/tagcfi/build//bin/clang++

-std=c++03 --target=aarch64-linux-gnu

-march=armv8-armemtag

--gcc-toolchain-/hmme/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CXX)

'/home/no<mark>ah</mark>/code/llvm-mte/tagcfi/build//bin/clang++

-std=c++63 --target=aarch64-linux-gnu

-march=armv8-a+memtag

--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD)

"-static -unwindlib=libunwindARRAY(0x1f724e38)

"/home/noah/code/llvm-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x1f78a608)

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# Supermicro

SPECspeed2017\_int\_base \( \) 1.16

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECspeed2017\_int\_peak Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

### Base Unknown Flags (Continued)

641.leela\_s: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang++

- -std=c++03 --target=aarch64-linux-gnu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//.//downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CXX)
- "/home/noah/code/llvm-mte/tagcfi/build//bin/alang++
- -std=c++03 --target=aarch64-linux-gnu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mte/tagfii/puild//../dpwwloads/gcc/gcc-llnaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD)
- "-static -unwindlib=libunwindARRAY(0x1173c810)
- "/home/noah/code/llvm-mte/tagefi/dowploads/glibc/build//libc.aARRAY(0x1f78ab30)

657.xz\_s: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang

- -std=c99 --target=aarch64-linux-gnu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../ddwnloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CC)
- "/home/noah/code/llvm-mte(tagdfi/build//bin/clang
- -std=c99 --target=aarch64-linux-gnu
- -march=armv8-a+memtag(
- --gcc-toolchain=/home/noah/orde/llvm-mte/tagcf//uild//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD) "-fgnu89-inline fcommonARRAY (0x1f793490)
- "-static -unwind/ib=libunwindARRAY(0x1f793058)
- "/home/noah/code/llvm-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x1f796910)

### **Base Portability Flags**

600.perpench\_s: -DSPEC\_LINUX\_AARCH64 -DSPEC\_LP64

602.gcc\_s: -DSPEC\_LP64 605.mcf\_s: -DSPEC\_LP64

620.omhetpp\_s: -DSPEC\_LP64

623.xalancbmk\_s: DSPEC LINUX -DSPEC LP64

625.x264\_s: -DSPEC\_LP64 631.deepsjeng\_s: -DSPEC\_LP64 641.leela\_s: /DSPEC\_LP64 657.xz\_s: -DSPEC\_LP64

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2025-08-12 12:52:43-0700.

Report generated on 2025-08-12 18:44:48 by CPU2017 PDF formatter v5866.

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