#### SPEC® CPU2017 Integer Rate Result Copyright 2017-2025 Standard Performance Evaluation Corporation Supermicro SPECrate2017\_int\_base = **3**.31 MegaDC ARS-211M-NR SPECrate2017 int peak **∧**Not Run (R13SPD, Ampere AmpereOne A192-32X) Aug-2025 **CPU2017 License:** 001176 Test Date: Hardware Availability: Aug-2024 **Test Sponsor:** Supermicro Tested by: Software Availability: Jul-2023 Supermicro **Copies** 0 1.00 2.00 3.00 7.00 8.00 9.00 500.perlbench\_r 1 4.00 502.gcc\_r 1 505.mcf r 1 520.omnetpp\_r 1 | 523.xalancbmk\_r 1 -525.x264\_r 1 531.deepsjeng\_r 1 541.leela\_r 1 548.exchange2\_r 1 557.xz\_r 1 SPECrate2017 int base (3.31) Hardware Software CPU Name: AmpereOne A192-32X OS: Fedora release 42 (Adams) Max MHz.: 3200 6.14.0-63.fc42.aarch64 Nominal: 3200 Compiler: C/C++/Fortran: Version 13.2.0 of GCC, the Enabled: 192 cores, 1 chip **GNU** Compiler Collection Orderable: 1 chip Parallel: 16 KB I + 64 KB D on chip per core Cache L1: Firmware: Version 1.0 released Jul-2024 2 MB I+D/on chip per core L2: File System: xfs

### **Errors**

Other:

System State: Run level 3 (multi-user)

Jemalloc memory allocator library v5.3.0

Base Pointers: 64-bit

Peak Pointers: Not Applicable

541.leela\_r (base) did not have enough runs!
525.x264\_r (base) did not have enough runs!
520.omnetpp\_r (base) did not have enough runs!
502.gcc\_r (base) did not have enough runs!
548.exchange2\_r (base) did not have enough runs!
531.deepsjeng\_r (base) did not have enough runs!
557.xz\_r (base) did not have enough runs!

CPU Cooling: Air

64 MB J+D on chip per chip

512 GB (8 x 64 GB 2Rx4 PC5-5600B-R,

NGB (N x N GB nRxn PC4-nnnnX-X)'

828 GB add more disk info here

502.223 GB fixme: If using DDR4, the format is:

None

'reportable' flag not set during run

523.xalancbmk\_r (base) did not have enough runs! 500.perlbench\_r (base) did not have enough runs!

(Continued on next page)

L3:

Other:

Memory:

**\$**torage:

Other:

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MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro SPECrate2017\_int\_base = 3.31

SPECrate2017\_int\_peak **N**ot Run

> Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

### **Errors** (Continued)

505.mcf r (base) did not have enough runs!

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsuy/ for information about how to get rid of this error.

### Results Table

	Base						$\overline{}$	•		Peak					
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Sed	onds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	1	<u>469</u>	3.39					$\overline{}$							
502.gcc_r	1	<u>354</u>	4.00					$\Big)$							
505.mcf_r	1	<u>477</u>	3.39					^							
520.omnetpp_r	1	<u>581</u>	<u>2.26</u>					/							
523.xalancbmk_r	1	<u>450</u>	<u>2.35</u>												
525.x264_r	1	<u>201</u>	8.70	/	/		<b>,</b>								
531.deepsjeng_r	1	<u>306</u>	3.74												
541.leela_r	1	<u>428</u>	3.87		$\mathcal{A}^{\prime}$										
548.exchange2_r	1	1228	2.13	( )											
557.xz_r	1	451	<u>2.39</u>												

SPECrate2017\_int\_base =

3.31 Not Run

SPECrate2017\_int\_peak =

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

#### **Compiler Notes**

were compiled on a system with 1x AmpereOne CPU Binaries chip .2 GB Memory using Fedora Linux 37

#### **Submit Notes**

The config file option 'submit' was used.

was used to bind copies to the cores.

### **Operating System Notes**

'ulimit -s unlimited' was used to set environment stack size

Set dirty\_ratio=8 to limit dirty cache to 8% of memory echo 8 | sudo tee /proc/sys/vm/dirty\_ratio

Set swappiness=1 to swap only if necessary

echo 1 | sudo tee /proc/sys/vm/swappiness

Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory echo 1 | sudo tee /proc/sys/vm/zone\_reclaim\_mode

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# Supermicro

SPECrate2017\_int\_base = 3.31

SPECrate2017\_int\_peak =

MegaDC ARS-211M-NR

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CPU2017 License: 001176
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Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

**Not Run** 

### **Operating System Notes (Continued)**

Set drop\_caches=3 to reset caches before invoking runcpu

echo 3 | sudo tee /proc/sys/vm/drop\_caches

Set numa\_balancing=0 to disable automatic numa balancing

echo 0 | sudo tee /proc/sys/kernel/numa\_balancing

Switch off all ktune and tuned setting sudo tuned-adm off

Transparent huge pages set to 'never'

sudo bash -c "echo never > /sys/kernel/mm/transparent\_hugepage/enabled"

runcpu command invoked through numactl i e

1P: numactl --interleave=0-3 runcpu <etc>

2P: numactl --interleave=all runcpy <etc>

### General Notes

Jemalloc v5.3.0 is available via

https://github.com/jemalloc/jemalloc/releases/download/5.3.0/jemalloc-5.3.0.tar.bz2

It was built on Fedora Linux 37 using Version 13.2.0 of GCC

The configure options and

"--with-lg-page=16" for building libjemalloc.so, and

"--with-lg-quantum=37 --with-lg-page-18" for building libjemalloc\_ext.so

Tuned MALLOC\_CONF in terms of https://jemalloc.net/jemalloc.3.html

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

#### **Platform Notes**

Note: lscpu is not able to detect the SLC.

SNC is defined at https://developer.arm.com/documentation/100180/0103/bry1436285730281 BIOS Settings:

Sub-NOMA Mode = Quadrant

Sysinfo program /root/mte/spec17/bin/sysinfo

Rev: r5274 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on fedora Tue Aug 19 01:39:26 2025

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

\*

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**Tested by:** 

SPECrate2017\_int\_base = 3.31

SPECrate2017\_int\_peak

MegaDC ARS-211M-NR

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Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

**N**ot Run

### Platform Notes (Continued)

```
* Did not identify cpu model.
                               If you would
* like to write your own sysinfo program, see
* www.spec.org/cpu2017/config.html#sysinfo
```

0 "physical id" tags found. Perhaps this is an older system, \* or a virtualized system. Not attempting to guess how to

count chips/cores for this system.

192 "processors" cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

#### From lscpu:

```
Architecture:
CPU op-mode(s):
Byte Order:
CPU(s):
On-line CPU(s) lis
Vendor ID:
BIOS Vendor ID:
Model name:
BIOS Model name
BIOS CPU family
```

Model: Thread(s) per core Core(s) per socket Socket(s):

Stepping: Frequency boost: (PU(s) scaling MHz: CPU max MHz:

CPU min MHz: BogoMIRS: Klags:

bf 6 rng bti mte ecv mte3 Lld cache: Lli cache: L2 cache: NUMA node(s):

NUMA node0 CPU(s): Vulnerability Gather data sampling: Vulnerability Ghostwrite: Vulnerability Itlb multihit: Vulnerability L1tf:

aarch64 6∕4-bit Little Endian

0 - 191Ampere Ampere (R) Ampere-la

AmpereOne (R) A192-32X CPU @ 3.2GHz

1 192 1 0x0disabled 81%

0

3200.0000 1000.0000 2000.00

fp asimd evtstrm aes pmull shal sha2 crc32 atomics fphp asimdhp cpuid asimdrdm jscvt fcma lrcpc dcpop sha3 sm3 sm4 asimddp sha512/asimdfhm dit uscat ilrcpc flagm ssbs sb paca pacg dcpodp flagm2 frint i8mm

> 12 MiB (192 instances) 3 MiB (192 instances) 384 MiB (192 instances) 0 - 191

Not affected Not affected Not affected

Not affected

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# Supermicro

SPECrate2017\_int\_base = 3.31

SPECrate2017\_int\_peak

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro

Aug-2025 Test Date:

Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

**N**ot Run

### Platform Notes (Continued)

```
Vulnerability Mds:
                                            Not affected
     Vulnerability Meltdown:
                                            Not affected
     Vulnerability Mmio stale data:
                                            Not affected
     Vulnerability Reg file data sampling
                                            Not affected
     Vulnerability Retbleed:
                                            Not affected
     Vulnerability Spec rstack overflow:
                                            Not affected
     Vulnerability Spec store bypass:
                                           Itigation; Speculative Store Bypass disabled
     via prctl
     Vulnerability Spectre v1:
                                            Mitigation __user pointer sanitization
     Vulnerability Spectre v2:
                                            Not affected
                                            Not affected
     Vulnerability Srbds:
     Vulnerability Tsx asymc abort;
                                            Not affected
   WARNING: the 'lscpu' utility claims that 1 "Socket(s)" were seen, which does not match
   the could not determine "physyical id"s seen in /proc/cpuinfo. The tester should
   verify the count independently
From numactl --hardware WARNING:
                                  a numact 'node' might or might not correspond to a
physical chip.
From /proc/meminfo
   MemTotal:
                   526618552
                             kΒ
   HugePages_Total;
                       2048 ki
   Hugepagesize:
From /etc/*release* /etc/*version*
   fedora-release: Fedora release 42 (Adams)
   os-release:
      NAME="Fedora Linux"
      VERSION= 42 (Adams)"
      RELEASE_TYPE=stable
      ID fedora
      VERSION_ID=42
      VERSION_CODENAME=""
      PLATFORM_ID="platform:f42"
      PRETTY_NAME="Fedora Linux 42 (Adams)"
   redhat-release: Fedora release 42 (Adams)
   system-release: Fedora release 42 (Adams)
   system_release-cpe: cpe:/o:fedoraproject:fedora:42
   Linux fedora 6.14.0-63.fc42.aarch64 #1 SMP PREEMPT_DYNAMIC Mon Mar 24 20:18:11 UTC
   2025 aarch64 GNU/Linux
Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown):
                                   Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
```

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# Supermicro

MegaDC ARS-211M-NR

SPECrate2017\_int\_base =

SPECrate2017\_int\_peak = **Y**ot Run

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro Tested by: Supermicro

Aug-2025 Test Date: Hardware Availability: Aug-2024 **Software Availability:** Jul-2023

#### Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Not affected

SPEC is set to: /root/mte/spec17

(R13SPD, Ampere AmpereOne A192-32X)

Filesystem Type Size Used Ava 1 Use Mounted on /dev/nvme0n1p4 xfs 828G 121G 707G 15%

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system day a which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS EDK II 04.04.00004001 2025-02-04 22.23:30 02/04/2025

8x SK Hynix HMCG94AGBRAI81N 64 GB 2 rank 5600

(End of data from sysinfo program)

### Compiler Version Notes

·----FC 548.exchange2\_r/pase

GNU Fortran (GCC) 12.3 1 20230605 [releases/gcc-12 revision

256cb821273d324e98d87aae2a061e0e889ec408]

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·----<del>/</del>

CC 500.perlbench\_r/base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557 xz r(base)

clang version 18.1.0rc (git@github.com:UT-Security/llvm-mte.git

464a00b150ec9bb9d88363bf3016ce0778bc0dde)

Target: aarch64-unknown-linux-gnu

Thread model: posix

InstalledDir: /home/noah/code/llvm-mte/tagcfi/build//bin

CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)

541.leela r(base)

clang version 18.1.0rc (git@github.com:UT-Security/llvm-mte.git 464a00b150ec9bb9d88363bf3016ce0778bc0dde)

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MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro SPECrate2017\_int\_base = **3**.31

SPECrate2017 int peak **Not Run** 

> Aug-2025 Test Date: Hardware Availability: Aug-2024 Software Availability: Jul-2023

### Compiler Version Notes (Continued)

Target: aarch64-unknown-linux-gnu

Thread model: posix

InstalledDir: /home/noah/code/llvm-mte/tagcfi/build//bin

### Base Unknown Flags

500.perlbench\_r: "/home/noah/code/llvm-mte/tagefi/build/\bin/clang

- -std=c99 --target=aarch64-linux-ghu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mtextagcfi/buid// downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CC)
- '/home/noah/code/llvm-mte/tagcfiXbuild//bin/clang
- -std=c99 --target=aarch64-linux-gnu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/bulld// downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD)
- "-g -02 -mcpu=ampere1a -#//to=\u00e4p1\ARRAY(\u00e904455e6e38)
- "-fgnu89-inline -fcommonARRAY 0x455e64f8)
- "-static -unwindlib=ljbuhwindARRAY(0x456669e8)
- "/home/noah/code/llym-mte/tagofi/downloads/glibc/.build//libc.aARRAY(0x45665be0)

502.gcc\_r: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang-std=c99 --target-aarcht-linux-gnu

- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llym-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in CC)
- "/home/noab/code/llvm-mte/tagcfi/build//bin/clang
- std=c9& {-target=aarch64-linux-gnu
- -march = army8-a+memtag/
- gcc-toolchain=\home/noah\gode/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/" (in LD)
- g -Q2 -mcpu=amperela -flto=fullARRAY(0x455e6988)
- "-fgnu89-inline/-fcommonARRAY(0x455e71f8)
- "-static unwindlib=libunwindARRAY(0x4570c330)
- "/home/moah/code/llvm-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x4576b8f8)

505.mcf\_r: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang

- -std=c99 --target=aarch64-linux-qnu
- -march=armv8-a+memtag
- (in CC)
- "/home/noah/code/llvm-mte/tagcfi/build//bin/clang
- -std=c99 --target=aarch64-linux-gnu
- -march=armv8-a+memtag
- --gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/"

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SPECrate2017\_int\_base = 3.31

SPECrate2017 int peak

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

CPU2017 License: 001176

Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

**^Not Run** 

### Base Unknown Flags (Continued)

```
505.mcf r (continued):
(in LD)
"-q -02 -mcpu=amperela -flto=fullARRAY(0x/455c75e8)
"-fgnu89-inline -fcommonARRAY(0x4570eba8/
"-static -unwindlib=libunwindARRAY(0x4576b928)
"/home/noah/code/llvm-mte/tagcfi/downloads/g2ibcx.build//libc.aARRAY(0x4576e530)
520.omnetpp_r:"/home/noah/code/llvm-mte/tagcf1/build//bln/clang++
-std=c++03 --target=aarch64-linaxygnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build/
                                                          gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CXX)
/home/noah/code/llvm-mte/tagcfi/bulld/\bin/clang++
-std=c++03 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build X../down/oads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in LD)
'-g -02 -mcpu=amperela -f/to-fullARRAY(0x4576e770)
"-static -unwindlib=libunwindARRAY(0x45/74e928)
"/home/noah/code/llvm-mte/magefi/downloads/glibc/.build//libc.aARRAY(0x4576e5f0)
523.xalancbmk_r: "/home/hoah/code/ Nym/mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/homy/noah/code/11/m-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CXX)
//home/noah/code/llvm-mte/tagcfi/build//bin/clang++
-std=c++07 --target=aarch64-linux-gnu
-march=armv8-a+memtag
-gcc-too/cm n=/home meah weds/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/
(in LD)
 g -02 -mcpu-amperela -flto=fullARRAY(0x456684b0)
-static -unwindlab=libunwindARRAY(0x4576e0c8)
"Xhome\noah/code 11vm-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x45774080)
525.x264_r:"\hope/noah/code/llvm-mte/tagcfi/build//bin/clang
-std=c99 -- target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/
(in CC)
"/home/noah/code/llvm-mte/tagcfi/build//bin/clang
-std=c99 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--\texttt{gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/"}
"-g -02 -mcpu=amperela -flto=fullARRAY(0x455deba0)
```

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### Supermicro

SPECrate2017\_int\_base = 3.31

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECrate2017\_int\_peak Not Run

Test Pate: Aug-2025

**CPU2017 License:** 001176 **Test Sponsor:** Supermicro **Tested by:** Supermicro

Hardware Availability: Aug-2024 Software Availability: Jul-2023

```
Base Unknown Flags (Continued
525.x264_r (continued):
"-fgnu89-inline -fcommonARRAY(0x455f53e8)
"-static -unwindlib=libunwindARRAY(0x455e/6838)
531.deepsjeng_r: "/home/noah/code/llvm-mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagefi/build//../dbv
                                                   loads/gcc/g
                                                              -l<mark>;</mark>naro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CXX)
"/home/noah/code/llvm-mte/tagcfi/bui/dx/binxclang
-std=c++03 --target=aarch64-linux-g/u
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../ownloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in LD)
'-g -02 -mcpu=ampere1a -flto=fullARRAN(0x45<mark>7</mark>/9ba0)
"-static -unwindlib=libunwindARRAY(0x45777380)
"/home/noah/code/llvm-mte//cagcfi/downloads/glibc/.build//libc.aARRAY(0x457798a0)
541.leela_r:"/home/noah/code/llvm/mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch64-linux
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code 1 vm-mte/bagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CXX)
"/home/noah/code/llvm-mte/tagcfi/build//bin/clang++
-std=c++03 --target=aarch4-linux-gnu
-march=armv8-a+memtag
--gcc-toolcharn=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in LD)
         rcpu=amperela, -flto=fullARRAY(0x4577e140)
-static -unwindlib=libunwindARRAY(0x457793a8)
Xhome/noah/code/ll/m-mte/tagcfi/downloads/glibc/.build//libc.aARRAY(0x4577e128)
                  m-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu//bin/aarch64-linux-gnu-gfortranARRAY(0x4577a170)
"/home/hoah/code/llvm-mte/dagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu//bin/aarch64-linux-gnu-gfortranARRAY(0x4576e710)
"-static -unwindlib=libunwindARRAY(0x4560fc30)
557.xz_r:"/pome/noah/code/llvm-mte/tagcfi/build//bin/clang
-std=c99 -- target=aarch64-linux-gnu
-march=armv8-a+memtag
--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86_64_aarch64-linux-gnu/"
(in CC)
```

(Continued on next page)

--gcc-toolchain=/home/noah/code/llvm-mte/tagcfi/build//../downloads/gcc/gcc-linaro-12.3.1-2023.06-x86\_64\_aarch64-linux-gnu/"

"/home/noah/code/llvm-mte/tagcfi/build//bin/clang

-std=c99 --target=aarch64-linux-qnu

-march=armv8-a+memtag

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# Supermicro

SPECrate2017\_int\_base =

MegaDC ARS-211M-NR

(R13SPD, Ampere AmpereOne A192-32X)

SPECrate2017\_int\_peak Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Aug-2025 Hardware Availability: Aug-2024 Software Availability: Jul-2023

### Base Unknown Flags (Continued)

557.xz\_r (continued): (in LD)

"-g -O2 -mcpu=amperela -flto=fullARRAY(0x/45785d70)

"-fgnu89-inline -fcommonARRAY(0x45786100/

"-static -unwindlib=libunwindARRAY(0x457863e8)

"/home/noah/code/llvm-mte/tagcfi/downloads/glibck.build//libc.aARRAY(0x457866b8)

### Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_AXRCH64 -DSPEC\_LP64

502.gcc\_r: -DSPEC\_LP64 505.mcf\_r: -DSPEC\_LP64 520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64

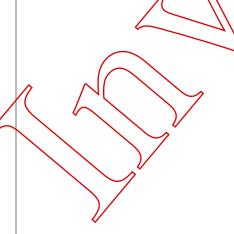
525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2025-08-19 01:39:25-0700.

Report generated on 2025-08-19 03:03:15 by CPU2017 PDF formatter v5866.