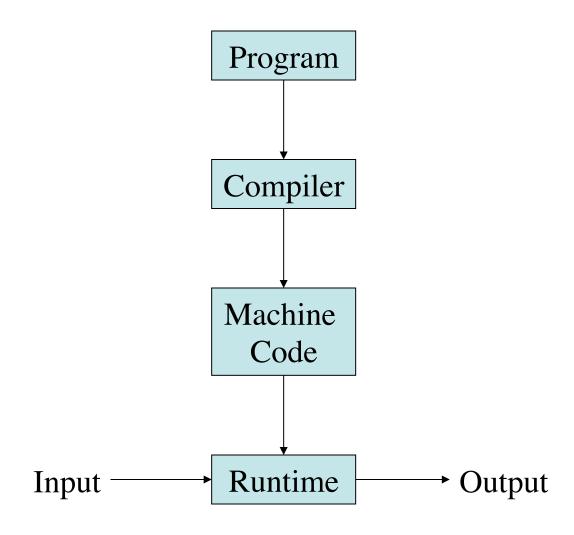
IN2: What is a Compiler

CMPT 379 Compilers

Anoop Sarkar

http://www.cs.sfu.ca/~anoop

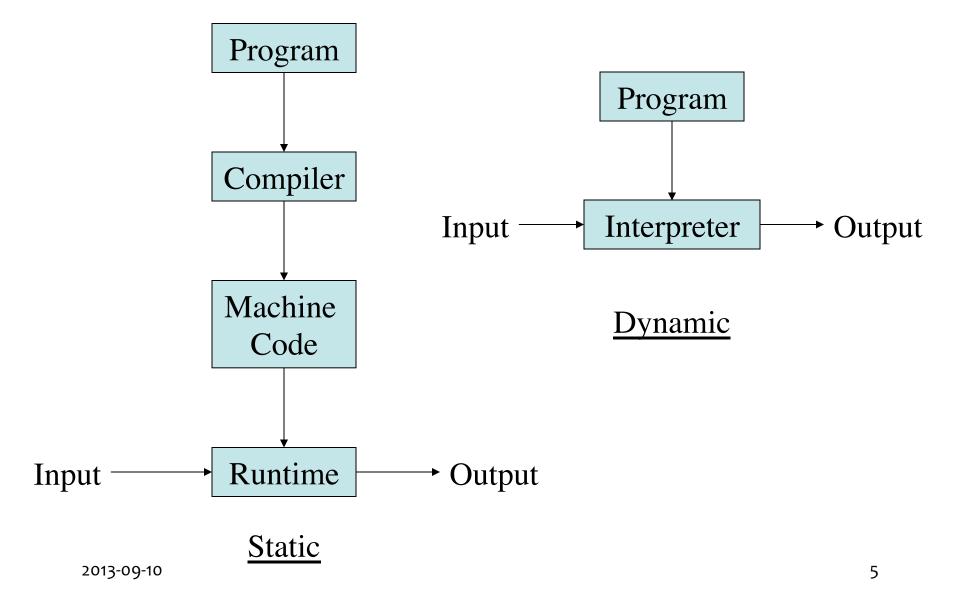


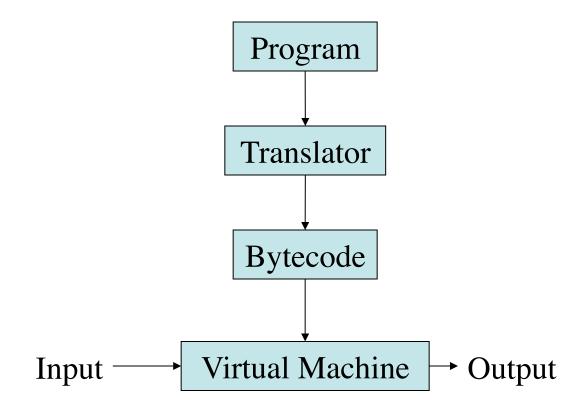
Compilers

- Analysis of the source (front-end)
- Synthesis of the target (back-end)
- The translation from user intention into intended meaning
- The requirements from a Compiler and a Programming Language are:
 - Ease of use (high-level programming)
 - Speed

Cousins of the compiler

- "Smart" editors for structured languages
 - static checkers; pretty printers
- Structured or semi-structured data
 - Trees as data: s-expressions; XML
 - query languages for databases: SQL
- Interpreters (for PLs like lisp or scheme)
 - Scripting languages: perl, python, tcl/tk
 - Special scripting languages for applications
 - "Little" languages: awk, eqn, troff, TeX
- Compiling to Bytecode (virtual machines)





Static/Dynamic

Context for the Compiler

- Preprocessor
- Compiler
- Assembler
- Linker (loader)

MIPS CPU

Program Counter

```
PC
                       EPC
                                                                     BadVaddr
                                             Cause
          00000000
                                00000000
                                                         00000000
                                                                                   00000000
Status =
                       ΗI
                                             L0
          00000000
                                00000000
                                                         00000000
                                           General registers
             00000000
                          R8
                                                                    00000000
                                                                               R24
                                                                                    (t8)
                                                                                              00000000
R0
    (r0)
          R9
R1
R1
                             $a0 to $a3 used to pass
R1
    (at)
             00000000
                                                                    00000000
                                                                               R25
                                                                                     s9)
                                                                                              00000000
R2
                                                                    00000000
                                                                               R26
    (v0)
             00000000
                                                                                     (k0)
                                                                                              00000000
                             arguments to a function
R3
                                                                               R27
    (v1)
             00000000
                                                                    00000000
                                                                                     (k1)
                                                                                              00000000
R4
    (a0)
                                                                    00000000
                                                                               R28
             00000000
                                                                                     gp)
                                                                                             00000000
                             call
R5
    (a1)
                                                                    00000000
                                                                               R29
             00000000
                                                                                             00000000
                          R1
                                                                                     sp)
          R30
                                                                                     s8)
R6
    (a2)
             00000000
                          R14
                                                                    00000000
                                                                                              00000000
                               (t7)
                                                     R23
R7
    (a3)
             00000000
                          R15
                                        00000000
                                                           (s7)
                                                                    00000000
                                                                               R31
                                                                                    (ra)
                                                                                              00000000
         Double floating-point registers
                          FP8
                                        0.000000
                                                                               FP24
FP0
             0.000000
                                                     FP16
                                                                    0.000000
                                                                                              0.000000
                         FP10
FP2
             0.000000
                                        0.000000
                                                     FP18
                                                                    0.000000
                                                                               FP26
                                                                                              0.000000
FP4
             0.000000
                         FP12
                                        0.000000
                                                     FP20
                                                                    0.000000
                                                                               FP28
                                                                                             0.000000
                          FP14
                                                     FP22
FP6
                                        0.000000
                                                                               FP30
             0.000000
                                                                    0.000000
                                                                                              0.000000
```

MIPS CPU

Text segments

Data segments

[0x10010000]	0x00000000		
0x74706563	0x206e6f69	0x636f2000	
0x72727563	0x61206465	0x6920646e	0x726f6e67
0x000a6465	0x495b2020	0x7265746e	0x74707572
0x0000205d	0x20200000	0x616e555b	0x6e67696c
0x61206465	0x65726464	0x69207373	0x6e69206e
0x642f7473	0x20617461	0x63746566	0x00205d68
0x555b2020	0x696c616e	0x64656e67	0x64646120
0x73736572	0x206e6920	0x726f7473	0x00205d65
	0x74706563 0x72727563 0x000a6465 0x0000205d 0x61206465 0x642f7473 0x555b2020	0x74706563 0x206e6f69 0x72727563 0x61206465 0x000a6465 0x495b2020 0x0000205d 0x20200000 0x61206465 0x65726464 0x642f7473 0x20617461 0x555b2020 0x696c616e	0x74706563 0x206e6f69 0x636f2000 0x72727563 0x61206465 0x6920646e 0x000a6465 0x495b2020 0x7265746e 0x0000205d 0x20200000 0x616e555b 0x61206465 0x65726464 0x69207373 0x642f7473 0x20617461 0x63746566 0x555b2020 0x696c616e 0x64656e67

What we understand

```
#include <stdio.h>
int main (int argc, char *argv[]) {
   int i;
   int sum = 0;
   for (i = 0; i <= 100; i++)
       sum = sum + i * i;
   printf ("Sum from 0..100 = %d\n", sum);
}</pre>
```

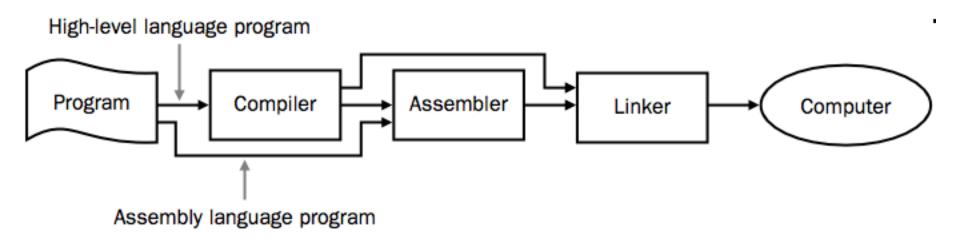
Assembly language

```
sw $t9, 24($sp)
.text
                           addu $t0, $t6, 1
.align 2
                           sw $t0, 28($sp)
.globl main
                           ble $t0, 100, loop
main:
  subu $sp, $sp, 32
                           la $a0, str
  sw $ra, 20($sp)
                           lw $a1, 24($sp)
  sd $a0, 32($sp)
                           jal printf
                           move $v0, $0
  sw $0, 24($sp)
  sw $0, 28($sp)
                           lw $ra, 20($sp)
                           addu $sp, $sp, 32
loop:
  lw $t6, 28($sp)
                           jr $ra
  mul $t7, $t6, $t6
                         .data
  lw $t8, 24($sp)
                         .align 0
  addu $t9, $t8, $t7
                         str:
                         .asciiz "The sum from 0 .. 100 is %d\n"
```

A one-one translation from assembly to machine code

Conversion into instructions for the Machine

MIPS machine language code



Linker

```
.data
str:
         .asciiz "the answer =
.text
main:
         li $v0, 4
         la $a0, str
         syscall
                          Local vs. Global labels
         li $v0, 1
         li $a0, 42
                          2-pass assembler and Linker
         syscall
```

The UNIX toolchain (as, ar, ranlib, ld, ...)

