
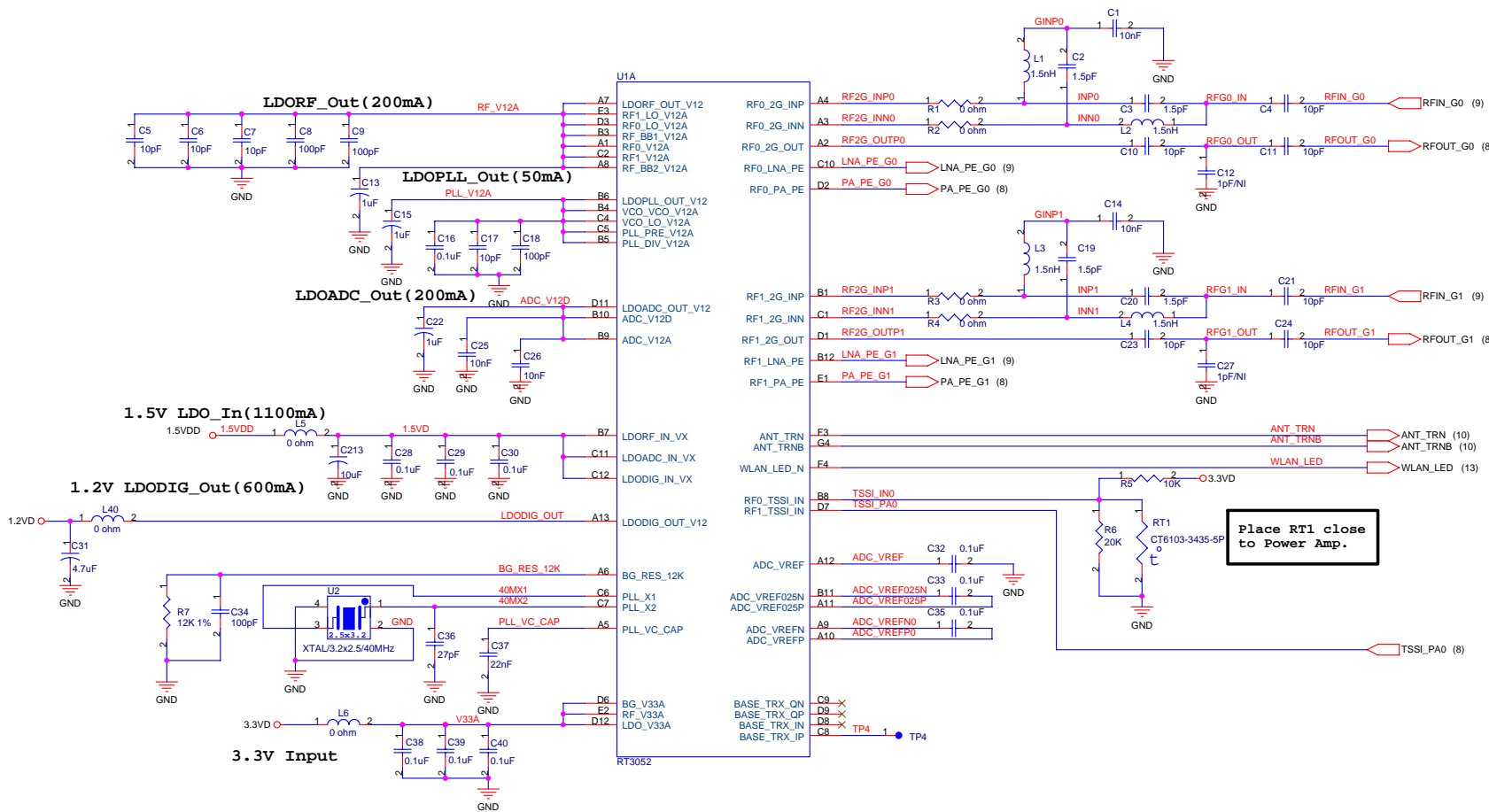

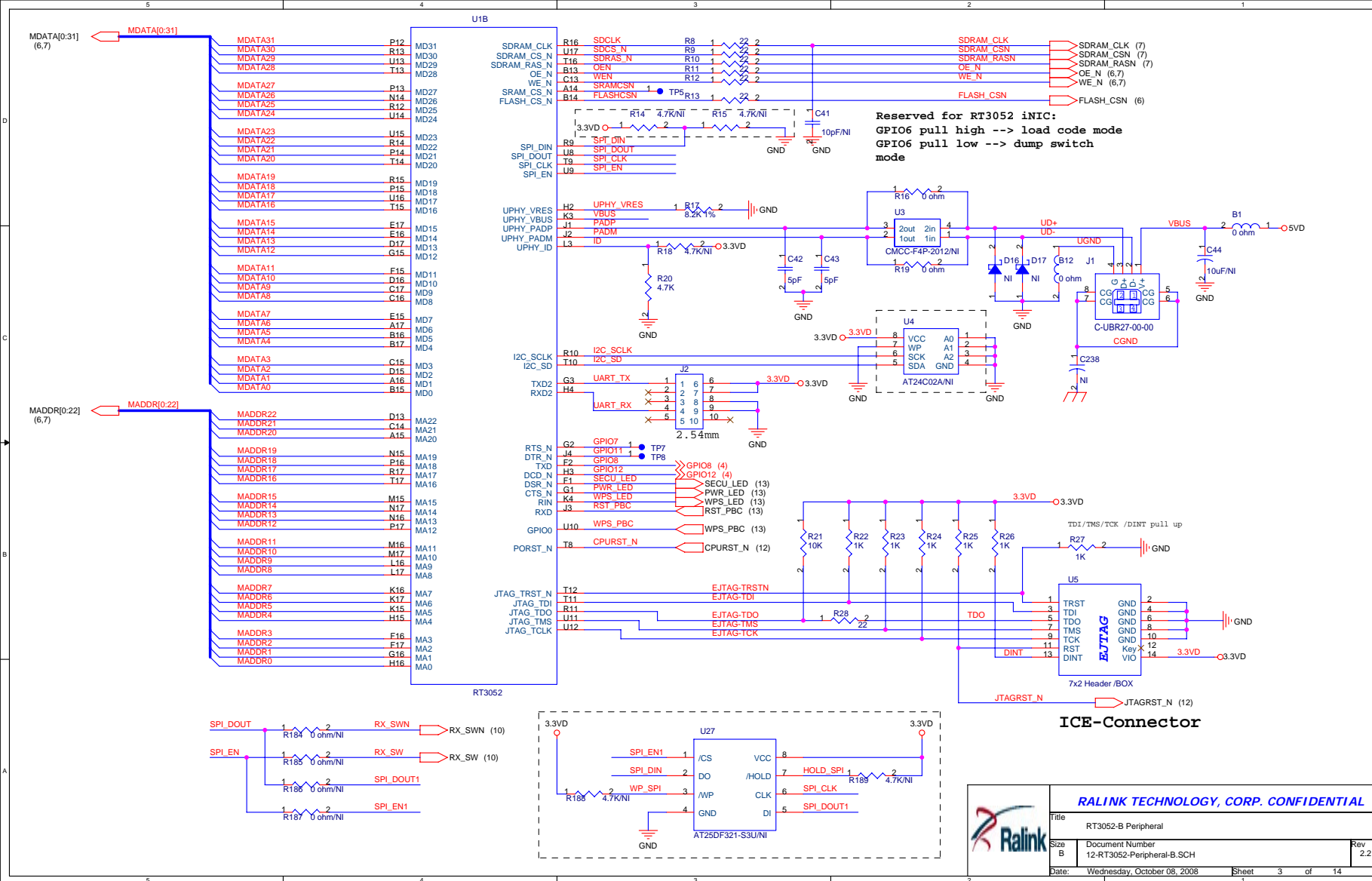


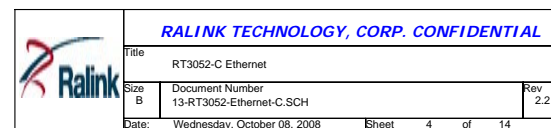
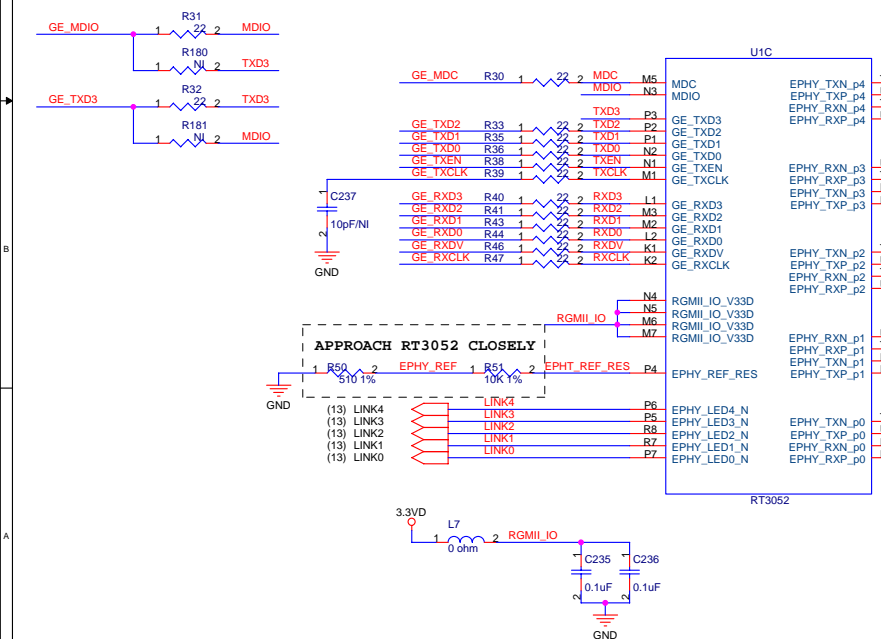
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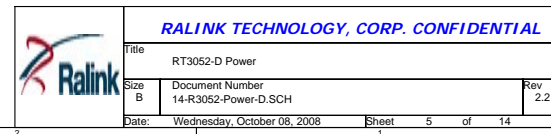
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Date: Wednesday, October 08, 2008		Sheet	1 of 14

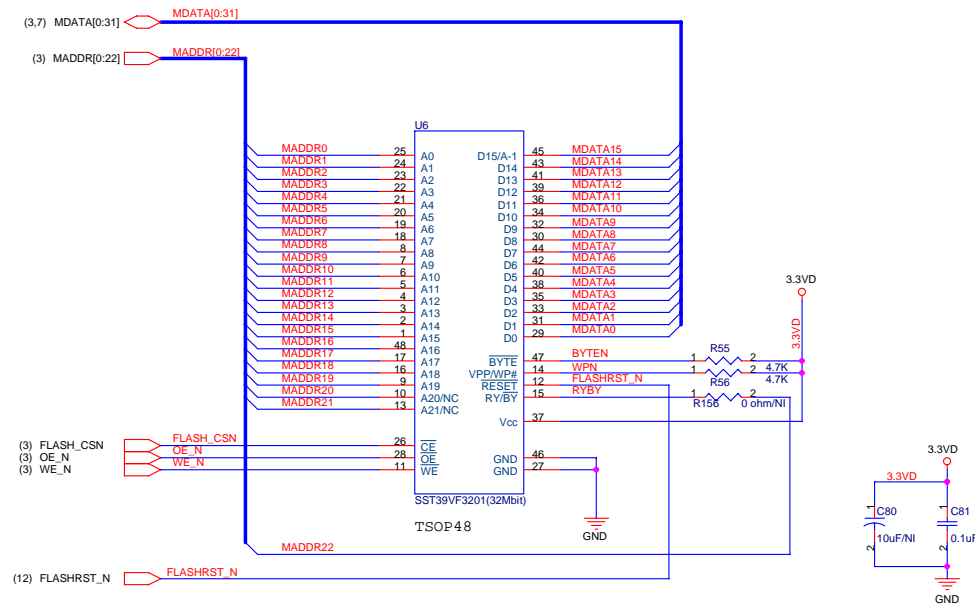



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Date: Wednesday, October 08, 2008	Sheet: 2	of 14	

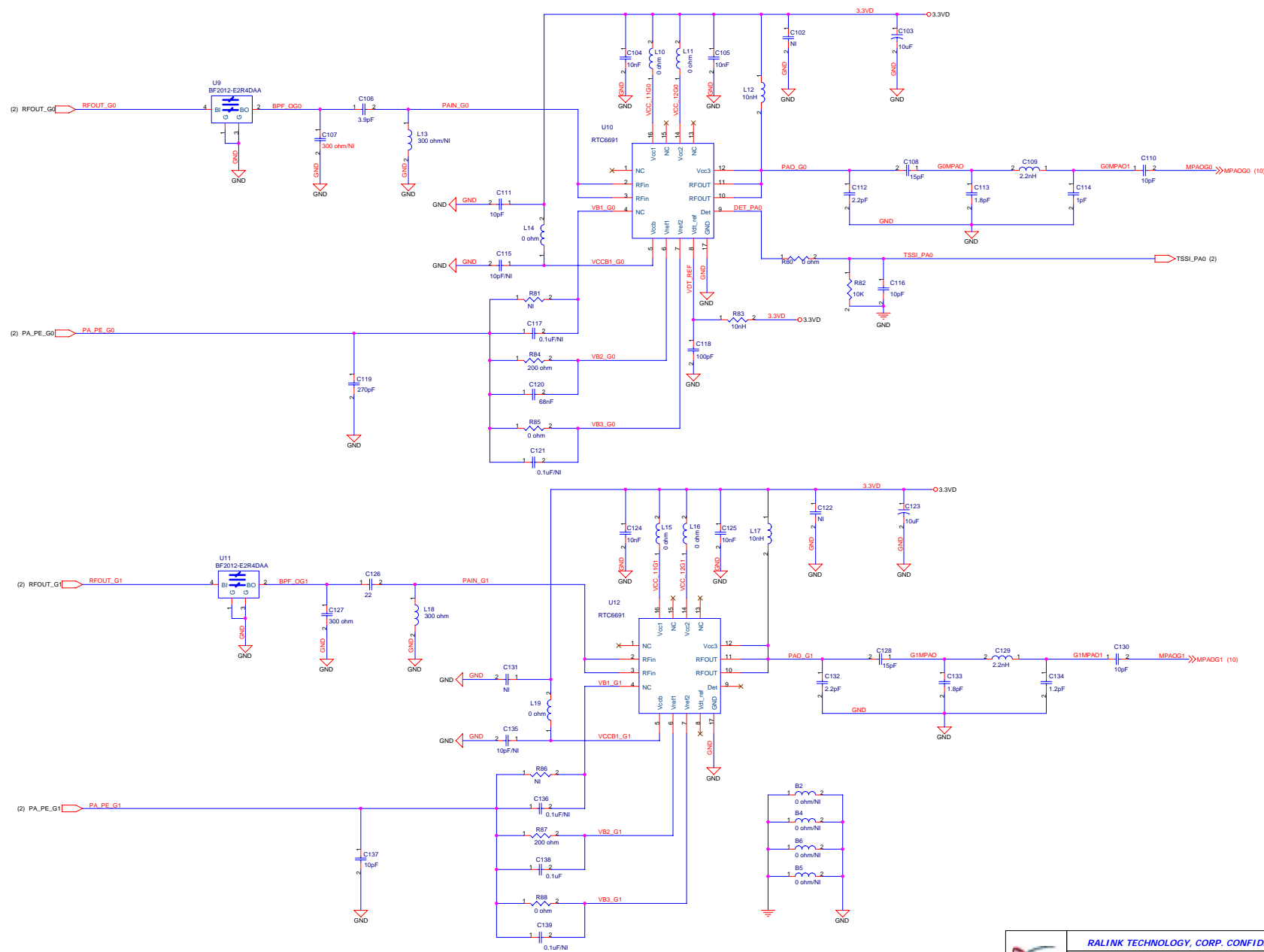


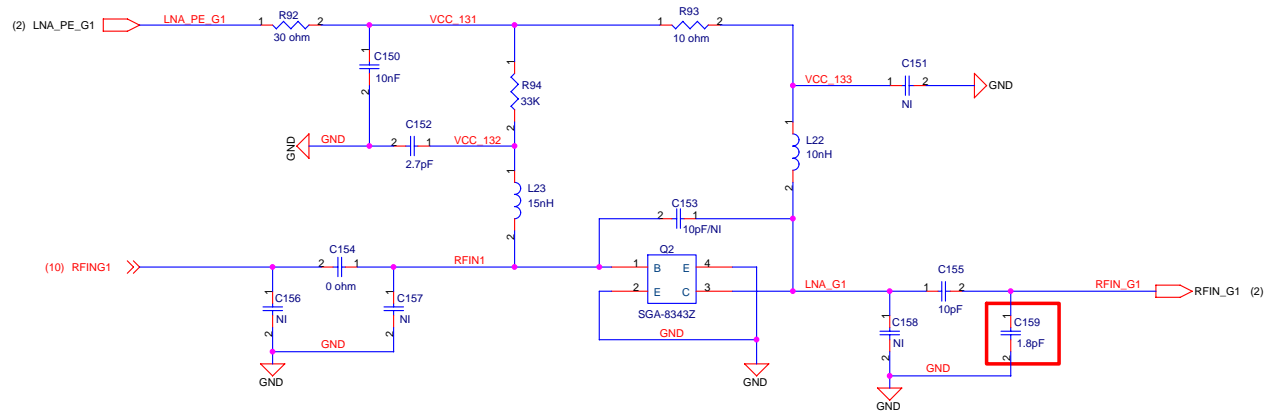
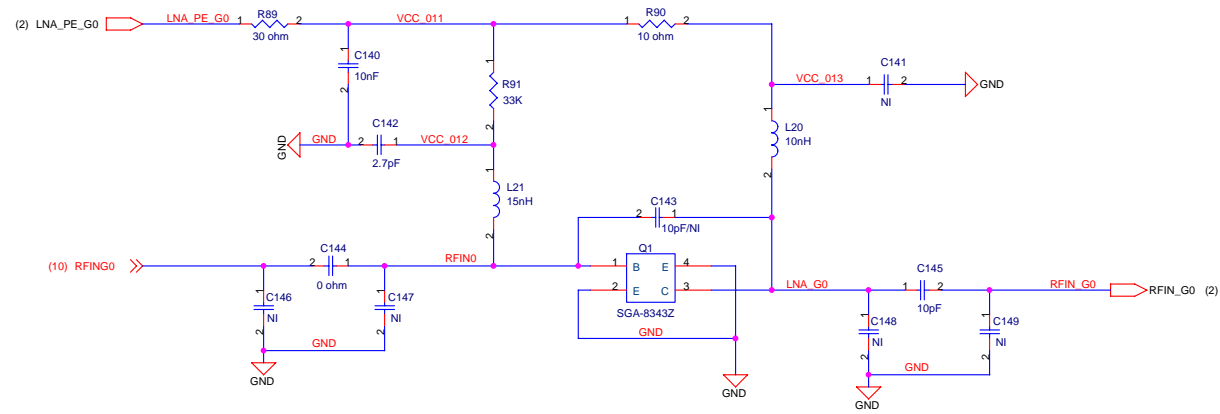






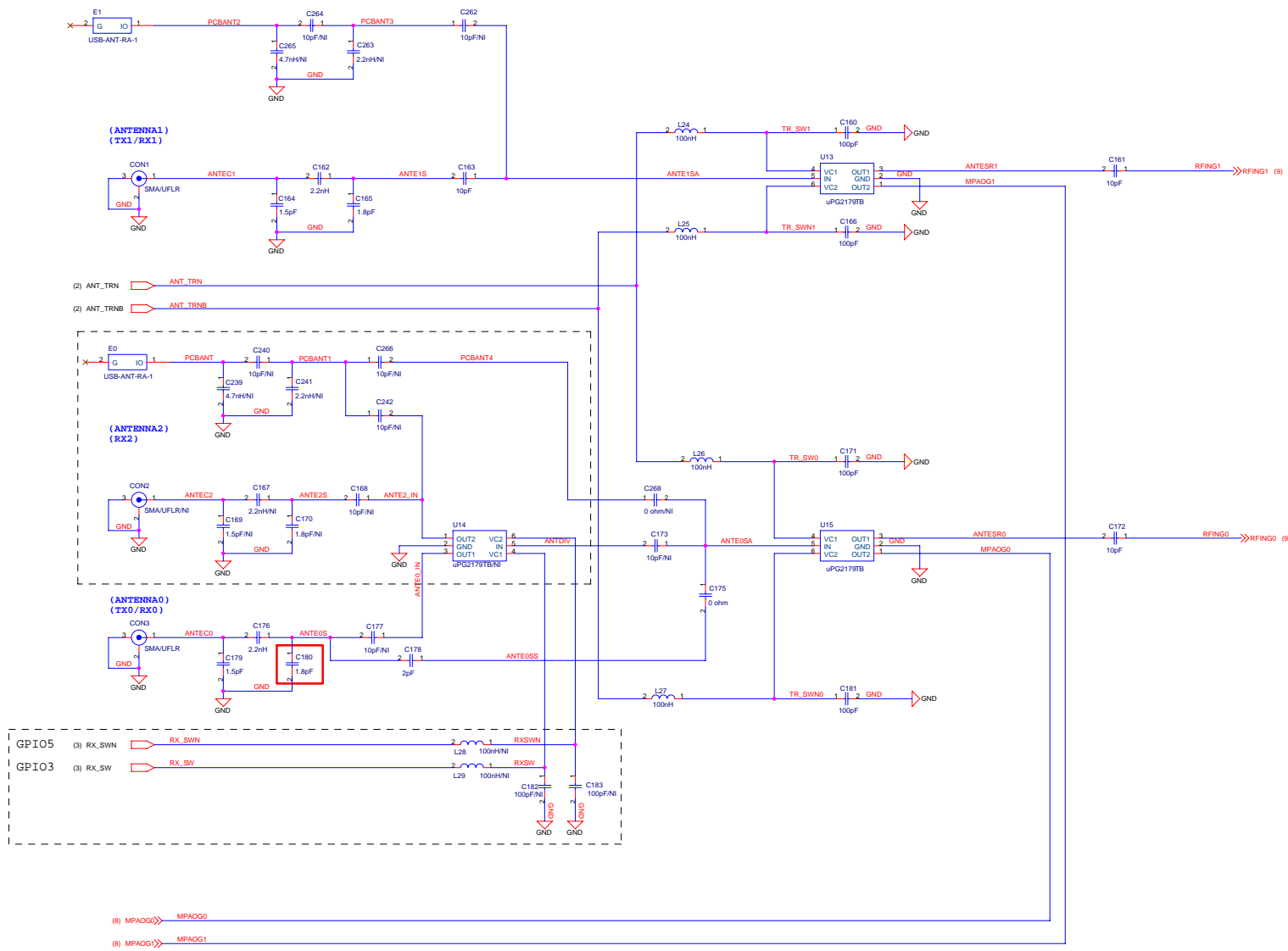
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Size	B	Document Number	21-FALSH.SCH
Date:	Wednesday, October 08, 2008	Sheet	6 of 14
		Rev	2.2





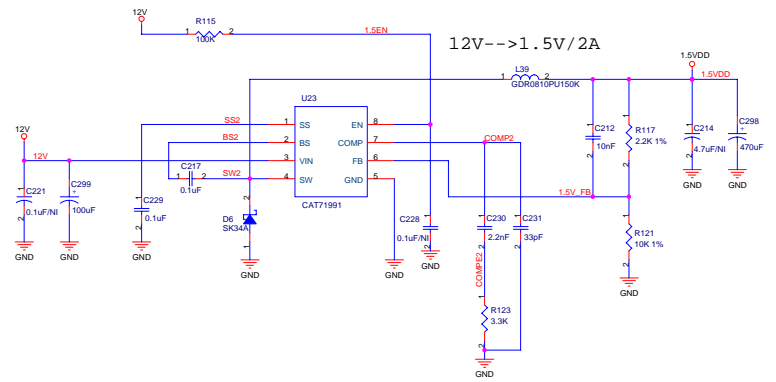
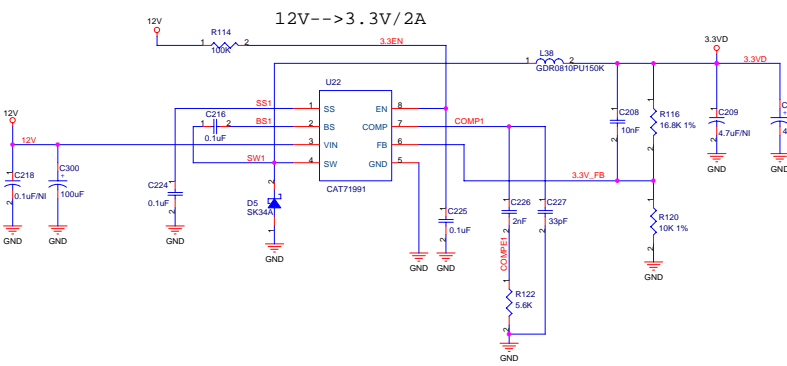
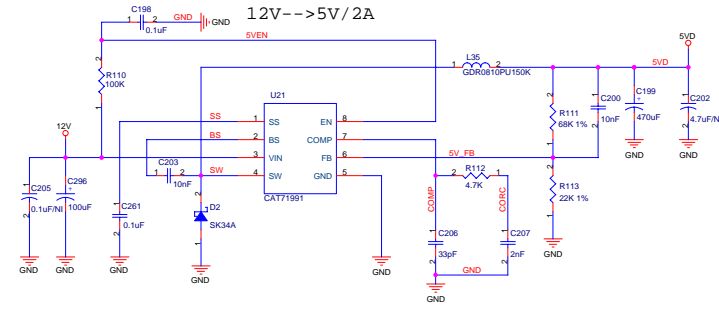
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Title	LNA		
Size	Document Number	Rev	
B	32-LNA.SCH	2.2	
Date:	Wednesday, October 08, 2008	Sheet	9 of 14

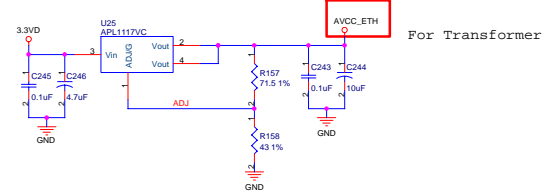


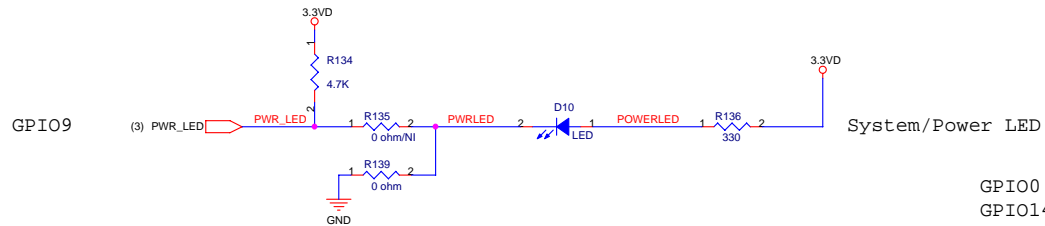
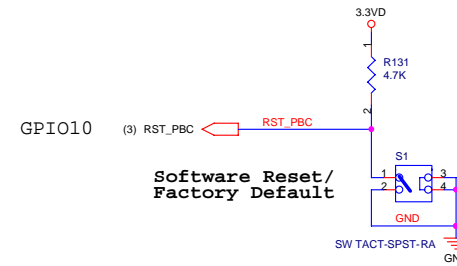
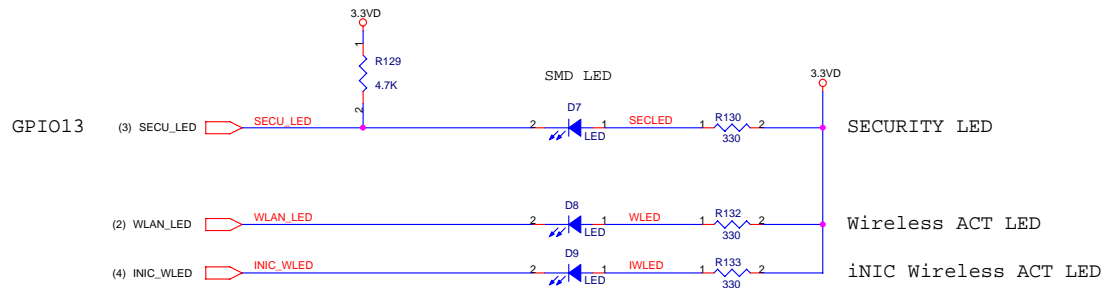
12V DC ADAPTER

The schematic diagram illustrates a 12V DC adapter circuit. The input is connected to a DC JACK (J4) at the 12VIN terminal. The signal path for the positive voltage includes a fuse (F1, PFS1812-1.5A) and a TVS diode (D1, SMCJ16) for protection. The output is taken from the +12V terminal. The ground path is labeled PGND and is connected to a common GND. The circuit includes several inductors (L36, L37, L47, L48) and bead inductors (BEADN1) for impedance matching and noise reduction. A large electrolytic capacitor (C204, 330uF/25V) is used for filtering and energy storage. The output voltage is 12V.

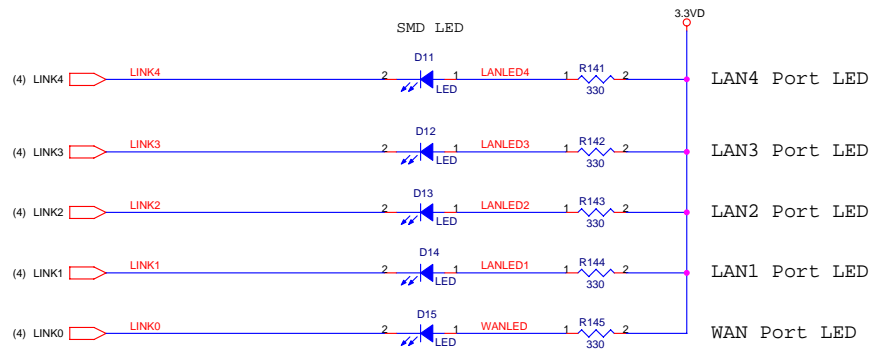
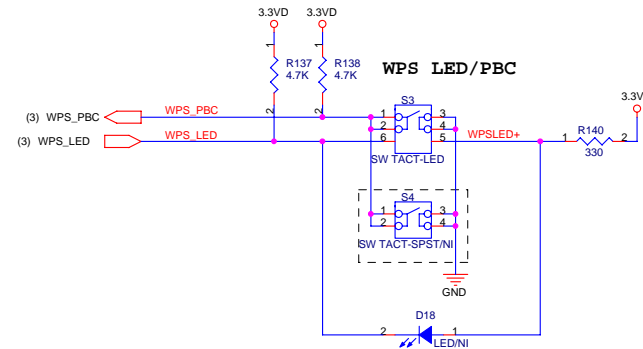


The schematic diagram illustrates the reset circuit for the G693XL R3132QNI microcontroller. The circuit is powered by a 3.3VDC source. A 10k resistor (R125) is connected between the 3.3VDC source and the RST pin of the microcontroller. A 10uF capacitor (C267) is connected between the RST pin and ground. The microcontroller is also connected to a SW TACT-SPST switch (S2) and a 0.1uF capacitor (C234) to ground. The reset signal (RESET_N) is connected to the CPU_RST pin (R126), FLASH_RST pin (R127), and INIC_RST pin (R128) of the microcontroller.





GPIO0
GPIO14



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Title	LED	
Size	Document Number	Rev
B	6-LED.SCH	2.2
Date:	Tuesday, August 26, 2008	Sheet 13 of 14

