

Anudeep N Rao F101, #90, 2nd cross, Byrappa layout, Behind Kannur Church, Kannur village, Bangalore - 562149



https://linkedin.com/in/anudeepnrao

About Me

Student at SDM Institute of Technology

Karnataka

Enthusiastic learner having belief in sustainable development and innovation. Want to develop technologies and products which aid in the better of mankind. I wish to implement my knowledge in fields like electronics, computers, communications and basic sciences in achieving the above.

Education

10th Standard

Kendriya Vidyalaya NAL Graduated, May 2013

CBSE 9.2 [CGPA]

Bangalore, Karnataka

Pre University, PCME

SMGH Jain PU College Graduated, June 2015

Karnataka PU board 74%

Bangalore, Karnataka

Bachelor of Engineering, Electronics and Communication

SDM Institute of Technology Graduated, June 2019

VTU 65%

Ujire, Karnataka

Internship

Project Intern July 2018 - August 2018

Bosch India

Bangalore, Karnataka

Experience

Lead Engineer August 2018 – June 2019

Abhyuditha Tech Solutions Pvt Ltd

Ujire, Karnataka

Training and Certifications

- Industrial Automation, Indwell Automation, 2017
- Advanced C++, Spoken Tutorial Project, IIT Bombay, 2019

Skills

- Web Development
- Microsoft Office
- Image Processing
- Vector Graphics
- Internet of Things (IoT)

- Analog and Digital Circuits
- Finite Elemental Analysis
- Computational Fluid Dynamics
- Ladder logic
- XAMPP

Computer Proficiency

Operating Systems

- Windows
- Ubuntu
- Kali
- Fedora
- Redhat Enterprise

Software

- Adobe Photoshop CC
- Autodesk Fusion
- Keil μVision
- Microsoft Office
- Adobe Premiere Pro
- Corel Graphics Suite
- Code Composer Studio
- MATLAB

Engineering Skills

Languages

- Verilog HDL
- Processing
- Sketch
- Embedded C
- x86 Assembly
- ISO C++
- ANSI C

CMS

- Drupal
- Wordpress

Database

- MariaDB
- SQL

Web Servers

- IIS
- Apache

Development Tools

- MS Visual Studio
- Eclipse

Hardware

- Xilinx Spartan
- Intel 8086
- Arduino
- MSP430

Projects and Publications

- Module Based Operating Systems NOSCONF 2016 International Open source conference
- Swastik Fractal Microstrip Antenna

2018-08 to 2019-06

Proximity coupled Microstrip Antenna with two iterations of swastik fractal implemented on copper patch

along with EBG (Electromagnetic Band Gap) structures of plus shape for noise reduction.

ATM type authentication system

2016-08 to 2016-09

Binary equivalence checker using only basic gates used as accessing device to control solenoidal locks or digital security systems

• Underwater Drone

2015-09 to 2016-04

Body and casing made using cans and galvanized iron sheets to withhold high pressure and wired control and power supply due to cost constraints.

• Rubber tapping machine

2017-11 to present

A simple to use device to tap the latex from rubber tree. It reduces the need for skilled labour and hence reducing costs.

• Digital storage oscilloscope

2017-01 to 2017-03

DSO made using Atmega 428 microcontroller having single channel, 5 KHz bandwidth.

• Digital Precision Oscilloscope

2017-03 to present

DPO implemented using 0808 8-bit high speed (60 Msps) ADC and MSP430G2 series ultra-low power microcontroller.

Languages

- English
- Kannada
- Hindi

Personal Details

Father's Name: Dr. Jala Nanjundeshwara Marital Status: Single DOB: March 14, 1997 Nationality: India

Gender: Male

Declaration

I, Anudeep N Rao, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.