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# Adam Seeto

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[anseeto.github.io](https://github.com/anseeto)

## EDUCATION

### University of Waterloo

*Bachelor of Applied Science in Honours Computer Engineering*

- University of Waterloo President's Scholarship (2013)

Waterloo, Ontario, Canada

Sep 2013 – Apr 2018

## TECHNICAL SKILLS

### Hardware

- Schematic Design
- Schematic Analysis
- Surface Mounted Soldering

### Oscilloscope Experience

- Physical Timing Analysis
- Signal Verification
- Power Testing

### Embedded Systems

- NVIDIA Jetson
- Com Express
- Arduino Nano/Uno
- NXP MPC57xx (Calypso)

### Communication Protocols

- SPI
- I<sup>2</sup>C
- UART

### Programming

- Java
- C/C++
- Python
- HTTP/CSS
- Driver analysis
- Linux Kernel Development
- ARM Device Trees

## EXPERIENCE

### Connect Tech Inc

*Technical Support Engineer*

Guelph, Ontario, Canada

Aug 2019 - Present

- Interfaced with customer engineers to support our products and debug their software and hardware issues.
- Tested specific customer hardware and systems to debug software and hardware issues.
- Worked with NVIDIA Jetson Platform modules and carriers to perform software development and hardware bring up.
- Worked with Com Express Platform modules and carriers to perform software development and hardware bring up.
- Performed linux kernel development for the NVIDIA Jetson platforms.
- Debugged and fixed linux drivers.
- Debugged hardware schematics to find bugs and provide circuit level solutions.
- Performed signal integrity analysis on various boards and suggested circuit level optimizations.
- Implemented internal system optimizations to change linear processes into scalable in house systems

### Ford Motor Company

*Hardware Verification Engineer*

Kanata, Ontario, Canada

Sep 2017 – Dec 2017, Aug 2018 – Aug 2019

- Performed worst-case circuit analysis (WCCA) for the power supply circuits (switched-mode power supplies and low-dropout regulators) leading to schematic changes for further boards.
- Followed and modified a test plan that consisted of line/load regulation, step responses, and switch node plots (for the switched-mode power supplies).
- Soldered components to modify circuits and create isolated power and voltage testing environments.
- Conducted temperature testing with temperature chambers and thermal imaging cameras.
- Worked on electronic control unit hardware design mainly focusing on power optimization.
- Discovered points of unnecessary power consumption and proposed design changes
- Assisted software teams in soldering new schematic configurations for testing.
- Worked together with hardware and software developers for case-by-case power optimizations.
- Verified PCB and schematic reworks with temperature testing.
- Created scripts to automate power testing by using GPIB connections

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## EXPERIENCE

### **Evertz Microsystems**

**Burlington, Ontario, Canada**

*Project Engineer*

**Jan 2017 – Apr 2017**

- Setup and modified customer media asset management systems.
- Networked servers and mounted file system (NFS and CIFS) storage across the servers.
- Ported and wrote scripts in Javascript and configuration files based on customer needs.

### **Lotlinux Inc**

**Hamilton, Ontario, Canada**

*Junior Developer/QA Analyst*

**Sep 2015 – Dec 2015, May 2016 – Aug 2016**

- Implemented a central log management system in Java for dynamic log notifications.
- Created cron jobs to clean up and organize storages across multiple servers.
- Performed SQL maintenance on MySQL databases.

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## ACADEMIC PROJECTS

### **SleepyZzz: Infant Health Monitor**

*An embedded device that is placed on a baby which monitors its position, heartrate, and temperature.*

- Designed an embedded system with chosen parts based on outlined system requirements.
- Worked with ARM processor Cortex M0+ to interface with GPIO.
- Designed Arduino to an ESP8266 Wifi module communications using AT commands to send data.
- Used communication protocols I<sup>2</sup>C to communicate with peripheral components.

### **Edge Detection Software**

- Designed an edge detection pipelined data flow with throughput and latency requirements.
- Implemented pipeline with synthesized hardware using VHDL.
- Used timing analysis on the signal waveforms to troubleshoot race conditions.

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## PERSONAL PROJECTS

### **Website**

- Learned HTML and CSS on my own to create my own website hosted on github  
([anseeto.github.io](https://anseeto.github.io))

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## PERSONAL INTERESTS

### **Hockey**

- Played Rep minor hockey for 10+ years.
- Taught learn to skate, power skating, and hockey drills at University of Waterloo for 2 years.

### **Astronomy**

- Own and operate my own telescope for stargazing and amateur astrophotography.