# Северо-кавказский федеральный университет кафедра прикладной математики и математического моделирования

Лабораторная работа №2 (Вариант «Образец оформления») Дисциплина: Математические модели и методы синтеза СБИС Тема: VHDL-реализация схем двоичной арифметики

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#### 1. Постановка задачи (полное условие в соответствии с выбранным вариантом).

1. Разработать и реализовать на языке VHDL в среде проектирования Xilinx ISE 14.7 модель устройства, рассчитывающего значение арифметического выражения (в соответствии с вариантом). Синтезировать схему.

Внимание! Запрещено использовать готовые библиотечные IEEE-модули двоичной арифметики (т. е. необходимо создать свои модули для выполнения арифметических операций над двоичными числами). Разрядность входных и выходных сигналов — 32 бит.

2. Создать в среде проектирования Xilinx ISE 14.7 тестовый симулятор устройства, созданного в задании 1. Получить временные диаграммы симуляции схемы.

Арифметическое выражение варианта 000:  $c = (a+b)^3 - 7a+b$ 

# 2. Подробная математическая (совокупность формул и поясняющего формулы текста) или информационной (словесное описание алгоритма) модели решения задачи.

Разобьем решение поставленной задачи на последовательность действий:

c=(a+b)^3-7a+b
1) a\_plus\_b=a+b
2) a\_plus\_b\_pow2=a\_plus\_b\*a\_plus\_b
3) a\_plus\_b\_pow3=a\_plus\_b\*a\_plus\_b\_pow2
4) a\_mul\_7=a\*7
5) apbp3 minus am7=a plus b pow3-a mul 7

6) c=apbp3 minus am7+b

Последовательное выполнение данных действий приведет к правильному вычислению значения арифметического выражения.

Элементарные операции сложения и перемножения будем осуществлять по «школьному» алгоритму - «столбиком».

# 3. Тестовые наборы исходных данных и соответствующих им правильных результатов для проверки работоспособности программы.

```
Пусть a=5, b=3, тогда:
1) a_plus_b=a+b=5+3=8
2) a_plus_b_pow2=a_plus_b*a_plus_b=8*8=64
3) a_plus_b_pow3=a_plus_b*a_plus_b_pow2=8*64=512
4) a_mul_7=a*7=5*7=35
5) apbp3_minus_am7=a_plus_b_pow3-a_mul_7=512-35=477
6) c=apbp3_minus_am7+b=477+3=480

Ответ: (5+3)³-7*5+3=480
```

#### 4. Указание имен, типов и назначения всех переменных и сигналов, входящих в математическую или информационную модель.

```
a,b — bit_vector(31 downto 0) — входные значения сигналов c — bit_vector(31 downto 0) — выходное значение (ответ) a_plus_b, a_plus_b_pow2, a_plus_b_pow3, a_mul_7, apbp3_minus_am7 - bit_vector(31 downto 0) — промежуточные сигналы (переменные).
```

### 5. Основной и вспомогательные (если есть) алгоритмы решения задачи (допустимо описание алгоритма на алгоритмическом языке, например, Pascal).

```
Ha языке программирования Pascal алгоритм решения поставленной задачи имеет вид: program LR2_V000_Ionisyan; var a,b,c, a_plus_b,a_plus_b_pow2,a_plus_b_pow3,a_mul_7,apbp3_minus_am7: LongInt; begin
```

```
writeln('a='); readln(a);
writeln('b='); readln(b);
a_plus_b:=a+b;
a_plus_b_pow2:=a_plus_b*a_plus_b;
a_plus_b_pow3:=a_plus_b*a_plus_b_pow2;
a_mul_7:=a*7;
apbp3_minus_am7:=a_plus_b_pow3-a_mul_7;
c:=apbp3_minus_am7+b;
writeln('OTBET: c=',c);
end.
```

6. Запись полных имен файлов, образующих проект с указанием назначения каждого файла (минимум указать имена и содержимое файлов с расширением .vhd (какие entity, procedure, function содержатся, что делают).

LR2\_v000\_Ionisyan.vhd – Реализация арифметического выражения в виде СБИС на языке VHDL

tb\_LR2\_v000\_Ionisyan.vhd - отладочный модуль симуляции

LR2\_V000\_Ionisyan.xise – файл управления проектом.

bin\_arith.vhd – вспомогательная библиотека компонент элементарной арифметики (созданная автором работы), включает в себя vhdl-описания:

- 1) full\_adder полный 1-битный сумматор;
- 2) bin add n-бит двоичный сумматор;
- 3) bin sub n-бит двоичный вычитатель;
- 4) bin\_mul n-бит двоичный перемножитель.
- 7. Полные исходные тексты VHDL-программ проекта (в каждом файле обязательно наличие информации о разработчике ФИО, курс, группа, специальность, университет).

#### LR2\_v000\_Ionisyan.vhd – Реализация арифметического выражения в виде СБИС на языке VHDL

```
-- Company: SKFU, 4PMI
-- Engineer: Ionisyan A.S.
-- Module Name: LR2 V000 Ionisyan
-- Project Name: LR2 var(000)
_____
                             _____
entity LR2 V000 Ionisyan is
   Port (a: in bit_vector(31 downto 0);
b: in bit_vector(31 downto 0);
          c : out bit vector(31 downto 0);
          clk: in bit);
end LR2 V000 Ionisyan;
architecture Behavioral of LR2 V000 Ionisyan is
component bin add is
Generic (n: integer);
Port (op1,op2: in bit vector(n-1 downto 0);
     res: out bit vector(n-1 downto 0);
     clk: in bit);
end component;
component bin sub is
Generic (n: integer);
Port (op1,op2: in bit_vector(n-1 downto 0);
     res: out bit vector(n-1 downto 0);
     clk: in bit);
end component;
component bin_mul is
Generic (n: integer);
Port (op1,op2: in bit vector(n-1 downto 0);
     res: out bit_vector(n-1 downto 0);
```

```
clk: in bit);
end component;
signal a plus b,a plus b pow2,a plus b pow3,apbp3 minus am7,a mul 7:bit vector(31
begin
--c = (a+b)^3-7a+b
--1) a plus b=a+b
a plus b chip: bin add generic map(32) port map(a,b,a plus b,clk);
--2) a_plus_b_pow2=a_plus_b*a_plus_b
a_plus_b_pow2_chip: bin_mul generic map(32) port
map(a plus b, a plus b, a plus b pow2, clk);
--3) a_plus_b_pow3=a_plus_b*a_plus_b_pow2
a plus b pow3 chip: bin mul generic map(32) port
map(a plus b pow2, a plus b, a plus b pow3, clk);
--4) a_mul_7=a*7
a_mul_7_chip: bin_mul generic map(32) port
map(a,"0000000000000000000000000000111",a_mul_7, clk);
--5) apbp3_minus_am7=a_plus_b_pow3-a_mul_7
apbp3 minus am7 chip: bin sub generic map(32) port
map(a_plus_b_pow3,a_mul_7,apbp3_minus_am7,clk);
--6) c=apbp3_minus_am7+b
res chip: bin add generic map(32) port map(apbp3 minus am7,b,c,clk);
end Behavioral;
______
library IEEE;
use IEEE.STD_LOGIC_1164.ALL; use IEEE.STD LOGIC ARITH.ALL;
use IEEE.STD LOGIC SIGNED.ALL;
entity LR2 V000 Ionisyan good is
    Port ( a : in STD LOGIC VECTOR (31 downto 0);
          b: in STD_LOGIC_VECTOR (31 downto 0);
c: out STD_LOGIC_VECTOR (31 downto 0);
          clk: in STD_LOGIC);
end LR2 V000 Ionisyan_good;
architecture Behavioral of LR2_V000_Ionisyan_good is
signal tmp:std logic vector(95 downto 0);
begin
  process (clk)
  begin
   if (clk'event and clk='1') then
      --tmp=(a+b)^3-7a+b
     tmp \le (a+b)*(a+b)*(a+b)-conv std logic vector (7,32)*a+b;
   end if;
   end process;
   c \le tmp(31 downto 0);
end Behavioral;
tb LR2 v000 Ionisyan.vhd – отладочный модуль симуляции
-- Company: SKFU, 4PMI
-- Engineer: Ionisyan A.S.
-- Module Name: LR2 V000 Ionisyan
-- Project Name: LR2 var(000)
                               _____
LIBRARY ieee;
use IEEE.STD_LOGIC_1164.ALL;
use IEEE.std logic arith.all;
use IEEE.numeric bit.all;
ENTITY tb LR2 Ionisyan IS
END tb LR2 Ionisyan;
ARCHITECTURE behavior OF tb LR2 Ionisyan IS
    COMPONENT LR2 V000 Ionisyan
```

PORT(a : IN bit\_vector(31 downto 0);
 b : IN bit\_vector(31 downto 0);

c : OUT bit\_vector(31 downto 0);

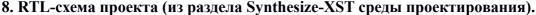
```
clk : IN bit);
    END COMPONENT;
    COMPONENT LR2_V000_Ionisyan_good is
    PORT(a: in STD_LOGIC_VECTOR (31 downto 0);
b: in STD_LOGIC_VECTOR (31 downto 0);
c: out STD_LOGIC_VECTOR (31 downto 0);
           clk: in STD_LOGIC);
    END COMPONENT;
   --Inputs/Outputs
   signal a,b,c : bit_vector(31 downto 0);
   signal clk : bit := '0';
   signal a good, b good, c good : std logic vector(31 downto 0);
   -- Clock period definitions
   constant clk_period : time := 100 ns;
BEGIN
       -- Instantiate the Unit Under Test (UUT)
   uut_lr2: LR2_V000_Ionisyan PORT MAP(a,b,c,clk);
   uut good: LR2 V000 Ionisyan good PORT MAP(a good,b good,c good,to stdulogic(clk));
   a <= bit vector(to unsigned(5,32));
   b<=bit vector(to unsigned(2,32));
   -- Clock process definitions
   clk process :process
       variable a tmp:integer:=0;
       variable b tmp:integer:=0;
   begin
      clk <= '1';
              a good <= conv std logic vector(a tmp, 32);
              b_good<=conv_std_logic_vector(b_tmp,32);</pre>
              wait for clk_period/2;
              clk <= '0';
              a_tmp:=a_tmp+1;
              if a_tmp>15 then
                 a tmp:=0;
                     b tmp:=b_tmp+1;
                     if b tmp>15 then b tmp:=0; end if;
              end if;
              wait for clk_period/2;
   end process;
END:
```

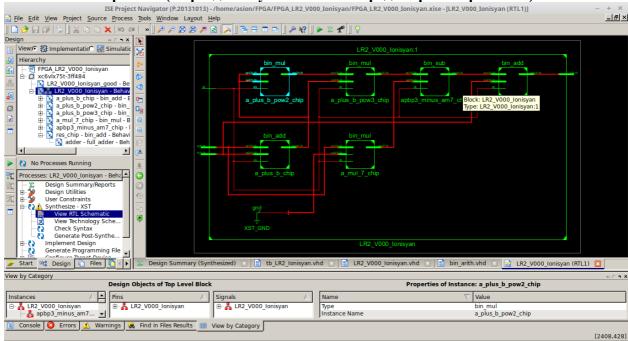
#### bin\_arith.vhd – вспомогательная библиотека компонент элементарной арифметики

```
-- Company: SKFU, 4PMI
-- Engineer: Ionisyan A.S.
-- Module Name: LR2 V000 Ionisyan
-- Project Name: LR2_var(000)
______
______
-- полный 1-битовый сумматор
entity full adder is
  Port (op1,op2,carry_in: in bit; res,carry_out: out bit; clk: in bit);
end full adder;
architecture Behavioral of full adder is
signal p:bit;
begin
process(clk)
begin
  if (clk'event and clk = '1') then
     p \le (not(op1) \text{ and op2}) \text{ or (op1 and not(op2))};
     res<=(not(p) and carry_in)or(p and not(carry_in));</pre>
     carry out<=(op1 and op2)or(p and carry in);</pre>
  end if;
```

```
end process;
end Behavioral;
______
-- n-бит двоичный сумматор
entity bin_add is
Generic (n: integer);
Port (op1,op2: in bit vector(n-1 downto 0);
     res: out bit_vector(n-1 downto 0);
     clk: in bit);
end bin add;
architecture Behavioral of bin add is
component full adder
  Port (op1,op2,carry in: in bit; res,carry out: out bit; clk: in bit);
end component;
signal carry:bit vector(n downto 0);
begin
carry(0)<='0';
gen: for i in 0 to n-1 generate
  begin
    adder: full_adder port map(op1(i),op2(i),carry(i),res(i),carry(i+1),clk);
  end generate;
end Behavioral;
______
-- n-бит двоичный вычитатель
entity bin sub is
Generic (n: integer);
Port (op1, op2: in bit vector(n-1 downto 0);
     res: out bit vector(n-1 downto 0);
     clk: in bit);
end bin sub;
architecture Behavioral of bin_sub is
component full adder
  Port (op1,op2,carry in: in bit; res,carry out: out bit; clk: in bit);
end component;
signal carry:bit vector(n downto 0);
begin
carry(0)<='1';
gen: for i in 0 to n-1 generate
  begin
    adder: full adder port map(op1(i), not(op2(i)), carry(i), res(i), carry(i+1), clk);
  end generate;
end Behavioral;
______
-- n-бит двоичный перемножитель (столбиком)
entity bin_mul is
Generic (n: integer);
Port (op1, op2: in bit vector(n-1 downto 0);
     res: out bit_vector(n-1 downto 0);
     clk: in bit);
end bin mul;
architecture Behavioral of bin mul is
component bin add
generic(n:integer);
Port(op1,op2:in bit vector(n-1 downto 0); res:out bit vector(n-1 downto 0); clk: in
bit);
end component;
type T_tmp_sum is array(0 to n) of bit_vector(n-1 downto 0);
signal tmp_sum,tmp_op1: T_tmp_sum;
tmp_sum(0) <= (others=>'0');
gen: for i in 0 to n-1 generate
  begin
    tmp op1(i)(i-1 downto 0) <= (others=>'0');
    tmp op1(i)(n-1 downto i) \leq (others=>'0') when op2(i)='0' else op1(n-i-1 downto 0);
    adder: bin_add generic map(n) port map(tmp_sum(i),tmp_op1(i),tmp_sum(i+1),clk);
```

```
end generate;
res<=tmp_sum(n);
end Behavioral;</pre>
```





#### 9. Содержимое файла .syr отчета синтеза и имплементации проекта.

```
Release 14.7 - xst P.20131013 (lin64)
Copyright (c) 1995-2013 Xilinx, Inc. All rights reserved.
Parameter TMPDIR set to xst/projnav.tmp
Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.13 secs
Parameter xsthdpdir set to xst
Total REAL time to Xst completion: 0.00 secs
Total CPU time to Xst completion: 0.13 secs
Reading design: LR2 V000 Ionisyan.prj
TABLE OF CONTENTS
  1) Synthesis Options Summary
  2) HDL Parsing
  3) HDL Elaboration
  4) HDL Synthesis
       4.1) HDL Synthesis Report
  5) Advanced HDL Synthesis
       5.1) Advanced HDL Synthesis Report
  6) Low Level Synthesis
  7) Partition Report
  8) Design Summary
       8.1) Primitive and Black Box Usage
       8.2) Device utilization summary
       8.3) Partition Resource Summary
       8.4) Timing Report
            8.4.1) Clock Information
            8.4.2) Asynchronous Control Signals Information
```

8.4.3) Timing Summary 8.4.4) Timing Details

```
______
* Synthesis Options Summary
______
---- Source Parameters
                                     : "LR2_V000_Ionisyan.prj"
Input File Name
Ignore Synthesis Constraint File : NO
---- Target Parameters
Output File Name
                                       : "LR2 V000 Ionisyan"
Output Format
                                       : NGC
                                      : xc6vlx75t-3-ff484
Target Device
---- Source Options
Automatic FSM Extraction : YES
FSM Encoding Algorithm : Auto
Safe Implementation : No
FSM Style
FSM Style
                                      : LUT
RAM Extraction
                                     : Yes
RAM Style
ROM Extraction
                                      : Yes
Shift Register Extraction : Yes
ROM Style : Autore Resource Sharing : YES
                                     : Auto
Asynchronous To Synchronous : NO
Shift Register Minimum Size : 2
Use DSP Block
                                     : Auto
Automatic Register Balancing
                                     : No
---- Target Options
LUT Combining
Reduce Control Sets : Auto
Add IO Buffers : YES
Global Maximum Fanout : 100000
Add Generic Clock Buffer(BUFG) : 32
: YES
LUT Combining
                                     : Auto
Optimize Instantiated Primitives : NO
Use Clock Enable : Auto
Use Synchronous Set : Auto
Use Synchronous T
Use Synchronous Set : Auto
Use Synchronous Reset : Auto
Pack IO Registers into IOBs : Auto
Equivalent register Removal : YES
---- General Options
Optimization Goal
Optimization Effort
                                    : Speed
                                     : 1
Power Reduction
Keep Hierarchy
                                      : NO
                                      : No
Netlist Hierarchy
                                      : As Optimized
RTL Output
                                      : Yes
Global Optimization
                                      : AllClockNets
Read Cores
Write Timing Constraints
                                      : NO
Cross Clock Analysis
Hierarchy Separator
Bus Delimiter
                                      : NO
                                      : /
                                      : <>
                                     : Maintain
Case Specifier
Slice Utilization Ratio
BRAM Utilization Ratio
DSP48 Utilization Ratio
Auto BRAM Packing
                                      : 100
                                      : 100
Auto BRAM Packing
                                     : NO
Slice Utilization Ratio Delta
                                     : 5
______
```

\_\_\_\_\_\_

```
______
Parsing VHDL file "/home/asion/FPGA/FPGA_LR2_V000_Ionisyan/bin_arith.vhd" into library
Parsing entity <full adder>.
Parsing architecture <Behavioral> of entity <full adder>.
Parsing entity <bin add>.
Parsing architecture <Behavioral> of entity <bin add>.
Parsing entity <bin sub>.
Parsing architecture <Behavioral> of entity <bin sub>.
WARNING: HDLCompiler: 946 - "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd" Line
66: Actual for formal port op2 is neither a static name nor a globally static
expression
Parsing entity <bin mul>.
Parsing architecture <Behavioral> of entity <bin mul>.
Parsing VHDL file "/home/asion/FPGA/FPGA_LR2_V000_Ionisyan/LR2_V000 Ionisyan.vhd" into
library work
Parsing entity <LR2 V000 Ionisyan>.
Parsing architecture <Behavioral> of entity <lr2 v000 ionisyan>.
Parsing entity <LR2 V000 Ionisyan good>.
Parsing architecture <Behavioral> of entity <lr2 v000 ionisyan good>.
______
                        HDL Elaboration
______
Elaborating entity <LR2 V000 Ionisyan> (architecture <Behavioral>) from library
Elaborating entity <bin add> (architecture <Behavioral>) with generics from library
<work>.
Elaborating entity <full adder> (architecture <Behavioral>) from library <work>.
Elaborating entity <br/>
<br/>
with mul> (architecture <Behavioral>) with generics from library
<work>.
WARNING: HDLCompiler: 746 - "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd" Line
90: Range is empty (null range)
WARNING: HDLCompiler: 220 - "/home/asion/FPGA/FPGA_LR2_V000_Ionisyan/bin arith.vhd" Line
90: Assignment ignored
Elaborating entity <bin sub> (architecture <Behavioral>) with generics from library
<work>.
______
                        HDL Synthesis
______
Synthesizing Unit <LR2 V000 Ionisyan>.
   Related source file is
"/home/asion/FPGA/FPGA LR2 V000 Ionisyan/LR2 V000 Ionisyan.vhd".
   Summary:
     no macro.
Unit <LR2 V000 Ionisyan> synthesized.
Synthesizing Unit <bin add>.
   Related source file is "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd".
     n = 32
INFO:Xst:3210 - "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd" line 44:
Output port <carry out> of the instance <gen[31].adder> is unconnected or connected to
loadless signal.
   Summary:
     no macro.
Unit <bin_add> synthesized.
Synthesizing Unit <full_adder>.
   Related source file is "/home/asion/FPGA/FPGA_LR2_V000_Ionisyan/bin_arith.vhd".
   Found 1-bit register for signal <res>.
   Found 1-bit register for signal <carry out>.
   Found 1-bit register for signal .
   Summary:
     inferred 3 D-type flip-flop(s).
```

```
Unit <full adder> synthesized.
Synthesizing Unit <bin mul>.
   Related source file is "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd".
      n = 32
   Summary:
     inferred 32 Multiplexer(s).
Unit <bin_mul> synthesized.
Synthesizing Unit <bin sub>.
   Related source file is "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd".
INFO:Xst:3210 - "/home/asion/FPGA/FPGA LR2 V000 Ionisyan/bin arith.vhd" line 66:
Output port <carry out> of the instance <qen[31].adder> is unconnected or connected to
loadless signal.
   Summary:
     no macro.
Unit <bin_sub> synthesized.
______
HDL Synthesis Report
Macro Statistics
                                               : 9504
# Registers
1-bit register
                                               . 9504
# Multiplexers
                                               : 96
1-bit 2-to-1 multiplexer
10-bit 2-to-1 multiplexer
                                               : 3
11-bit 2-to-1 multiplexer
12-bit 2-to-1 multiplexer
                                               : 3
13-bit 2-to-1 multiplexer
                                               : 3
14-bit 2-to-1 multiplexer
15-bit 2-to-1 multiplexer
                                               : 3
16-bit 2-to-1 multiplexer
                                                 3
17-bit 2-to-1 multiplexer
                                               : 3
18-bit 2-to-1 multiplexer
19-bit 2-to-1 multiplexer
                                               : 3
                                               : 3
2-bit 2-to-1 multiplexer
 20-bit 2-to-1 multiplexer
                                               : 3
21-bit 2-to-1 multiplexer
22-bit 2-to-1 multiplexer
                                               : 3
23-bit 2-to-1 multiplexer
24-bit 2-to-1 multiplexer
                                               : 3
 25-bit 2-to-1 multiplexer
26-bit 2-to-1 multiplexer
                                               : 3
27-bit 2-to-1 multiplexer
                                               : 3
28-bit 2-to-1 multiplexer
                                               : 3
29-bit 2-to-1 multiplexer
 3-bit 2-to-1 multiplexer
                                               : 3
 30-bit 2-to-1 multiplexer
                                                 3
                                               : 3
31-bit 2-to-1 multiplexer
32-bit 2-to-1 multiplexer
                                               : 3
4-bit 2-to-1 multiplexer
5-bit 2-to-1 multiplexer
 6-bit 2-to-1 multiplexer
7-bit 2-to-1 multiplexer
8-bit 2-to-1 multiplexer
                                               : 3
 9-bit 2-to-1 multiplexer
______
______
                   Advanced HDL Synthesis
______
```

WARNING:Xst:1293 - FF/Latch <carry\_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process. WARNING:Xst:1293 - FF/Latch <carry\_out> has a constant value of 0 in block <gen[6].adder>. This FF/Latch will be trimmed during the optimization process. WARNING:Xst:1293 - FF/Latch <carry\_out> has a constant value of 0 in block <gen[5].adder>. This FF/Latch will be trimmed during the optimization process.

```
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[30].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[29].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[28].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\langle \text{gen}[27].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[26].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[25].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[23].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{\text{gen}[22].\text{adder}\}. This \{\text{FF/Latch will}\} be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm qen}[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[15].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[26].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{\text{gen}[25].\text{adder}\}. This \text{FF/Latch will} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[19].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[18].adder)}. This FF/Latch will be trimmed during the optimization process.
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\{qen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[28].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[25].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[29].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[28].adder>. This FF/Latch will be trimmed during the optimization process.
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\{gen[26].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[25].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[24].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[23].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[21].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
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<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[7].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
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{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
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<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[15].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<qen[18].adder>. This FF/Latch will be trimmed during the optimization process.
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{\rm (gen[12].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[20].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[14].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[13].adder\}. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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\{\text{gen}[12].\text{adder}\}. This \text{FF/Latch wil}\overline{1} be trimmed during the optimization process.
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<qen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[23].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[28].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\langle \text{gen}[27].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<qen[7].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[23].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle qen[14].adder \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (qen[10].adder)}. This FF/Latch will be trimmed during the optimization process.
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<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
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{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
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<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[13].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\langle qen[15].adder \rangle. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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{\rm (qen[10].adder)}. This FF/Latch will be trimmed during the optimization process.
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<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[15].adder\}. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[22].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm qen[16].adder}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[15].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[14].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[13].adder)}. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
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\langle \text{gen}[16].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[15].adder)}. This FF/Latch will be trimmed during the optimization process.
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<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
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{\rm (gen[19].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[18].adder\}. This FF/Latch will be trimmed during the optimization process.
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<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{0\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[18].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{gen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[21].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[20].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[19].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[18].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[16].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (qen[15].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[14].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[13].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[11].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[10].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{gen[22].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{\text{gen}[20].\text{adder}\}. This \text{FF/Latch will} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[25].adder)}. This {\rm FF/Latch~wil\overline{l}} be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\langle \text{gen}[24].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch < carry out > has a constant value of 0 in block
<qen[23].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{gen[19].adder\}. This FF/Latch will be trimmed during the optimization process.
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\langle \text{gen}[18].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[17].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[16].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch < carry out > has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{gen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[13].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{qen[12].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
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{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[7].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
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<qen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[18].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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{\rm (gen[10].adder)}. This {\rm FF/Latch~wil}\overline{\rm l} be trimmed during the optimization process.
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<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch < carry out > has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[10].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[20].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[19].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch < carry out > has a constant value of 0 in block
<qen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm qen}[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[13].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[12].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[11].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[18].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[10].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[15].adder)}. This {\rm FF/Latch~wil}\overline{\rm l} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[27].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[26].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[25].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch < carry out > has a constant value of 0 in block
<gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[18].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{gen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[27].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[26].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[25].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\langle \text{gen}[23].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[19].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[18].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{gen[13].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[12].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{\text{gen}[11].\text{adder}\}. This \text{FF/Latch wil}\overline{1} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[10].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[26].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[25].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\langle \text{gen}[23].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[19].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{qen[18].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm qen[13].adder}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[27].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[26].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
\{gen[25].adder\}. This FF/Latch will be trimmed during the optimization process.
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<gen[23].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[22].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[20].adder>. This FF/Latch will be trimmed during the optimization process.
```

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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[17].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[30].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[29].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[28].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[27].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[26].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
{\rm (gen[17].adder)}. This {\rm FF/Latch~wil}\overline{\rm l} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
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WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[2].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[22].adder)}. This {\rm FF/Latch~wil}^{\overline{1}} be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[21].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[20].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[19].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[18].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
\{qen[17].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[16].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[13].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[12].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[11].adder>. This FF/Latch will be trimmed during the optimization process.
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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[10].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[9].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
<gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block <gen[29].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[28].adder)}. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\{qen[16].adder\}. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[15].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[14].adder>. This FF/Latch will be trimmed during the optimization process.
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<gen[11].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
\langle \text{gen}[10].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[9].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<qen[8].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[7].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[6].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293 - FF/Latch < carry\_out> \ has \ a \ constant \ value \ of \ 0 \ in \ block}
<gen[5].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[4].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[3].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[2].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[1].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[0].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block <gen[30].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[29].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<gen[28].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry_out> has a constant value of 0 in block
<qen[27].adder>. This FF/Latch will be trimmed during the optimization process.
WARNING: Xst: 1293 - FF/Latch <carry out> has a constant value of 0 in block
<qen[26].adder>. This FF/Latch will be trimmed during the optimization process.
```

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WARNING: Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
{\rm (gen[25].adder)}. This FF/Latch will be trimmed during the optimization process.
WARNING:Xst:1293 - FF/Latch <carry out> has a constant value of 0 in block
<gen[24].adder>. This FF/Latch will be trimmed during the optimization process.
{\tt WARNING:Xst:1293\ -\ FF/Latch\ <carry\_out>\ has\ a\ constant\ value\ of\ 0\ in\ block}
\langle \text{gen}[23].\text{adder} \rangle. This FF/Latch will be trimmed during the optimization process.
_____
Advanced HDL Synthesis Report
Macro Statistics
                                                  : 9504
# Registers
                                                  . 9504
Flip-Flops
# Multiplexers
                                                 : 96
1-bit 2-to-1 multiplexer
                                                 : 3
 10-bit 2-to-1 multiplexer
                                                  : 3
 11-bit 2-to-1 multiplexer
                                                  : 3
                                                  : 3
 12-bit 2-to-1 multiplexer
13-bit 2-to-1 multiplexer
                                                  : 3
14-bit 2-to-1 multiplexer
                                                  : 3
                                                 : 3
15-bit 2-to-1 multiplexer
                                                 : 3
 16-bit 2-to-1 multiplexer
 17-bit 2-to-1 multiplexer
                                                    3
                                                  : 3
18-bit 2-to-1 multiplexer
 19-bit 2-to-1 multiplexer
                                                  : 3
 2-bit 2-to-1 multiplexer
                                                  : 3
 20-bit 2-to-1 multiplexer
 21-bit 2-to-1 multiplexer
                                                  : 3
                                                  : 3
 22-bit 2-to-1 multiplexer
                                                  : 3
 23-bit 2-to-1 multiplexer
 24-bit 2-to-1 multiplexer
                                                  : 3
                                                 : 3
 25-bit 2-to-1 multiplexer
 26-bit 2-to-1 multiplexer
                                                  : 3
 27-bit 2-to-1 multiplexer
                                                    3
 28-bit 2-to-1 multiplexer
                                                  : 3
 29-bit 2-to-1 multiplexer
                                                  : 3
 3-bit 2-to-1 multiplexer
                                                  : 3
 30-bit 2-to-1 multiplexer
                                                  : 3
 31-bit 2-to-1 multiplexer
 32-bit 2-to-1 multiplexer
                                                  : 3
 4-bit 2-to-1 multiplexer
                                                  : 3
 5-bit 2-to-1 multiplexer
 6-bit 2-to-1 multiplexer
                                                  : 3
 7-bit 2-to-1 multiplexer
 8-bit 2-to-1 multiplexer
                                                  : 3
 9-bit 2-to-1 multiplexer
______
______
        Low Level Synthesis
______
WARNING:Xst:2677 - Node <gen[31].adder/carry out> of sequential type is unconnected in
block <bin_add>.
WARNING:Xst:2677 - Node <gen[31].adder/carry out> of sequential type is unconnected in
block <bin sub>.
Optimizing unit <LR2 V000 Ionisyan> ...
Optimizing unit <bin add> ...
Optimizing unit <bin mul> ...
Optimizing unit <bin sub> ...
WARNING:Xst:1710 - FF/Latch <a_mul_7_chip/gen[10].adder/gen[1].adder/carry_out>
(without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This
FF/Latch will be trimmed during the optimization process.
```

the optimization process.

<a mul 7 chip/gen[10].adder/gen[2].adder/carry out> (without init value) has a

constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[3].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[4].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[13].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[15].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[10].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[10].adder/gen[20].adder/carry out> (without init value) has a

 $\alpha_{10}=10$ .adder/gen[21].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ .adder/gen[22].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}_{10} = 10$ . adder/gen[23].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ .adder/gen[24].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=0. $$ adder/gen[25].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}_{10}$ , adder/gen[10].adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ .adder/gen[27].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ .adder/gen[28].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}^2 - \alpha_{10}^2 = 10$  adder/gen[29].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{100}^2 - \alpha_{100}^2 = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[10].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[9].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[11].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[11].adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 11.4$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 11.4$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[11].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[11].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[13].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[23].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[11].adder/gen[24].adder/carry out> (without init value) has a

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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[26].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[11].adder/gen[28].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[30].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[10].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[1].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[2].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[3].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[4].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[5].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[6].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[7].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[8].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[9].adder/carry out> (without init value) has a constant
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value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the

constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a\_mul\_7\_chip/gen[8].adder/gen[10].adder/carry\_out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

optimization process.

the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[15].adder/carry_out> (without init value) has a
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<a mul 7 chip/gen[8].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[8].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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<a mul 7 chip/gen[8].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[23].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[8].adder/gen[28].adder/carry out> (without init value) has a

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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[8].adder/gen[29].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[8].adder/gen[30].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7\_chip/gen[7].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[1].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[2].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[3].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[4].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[5].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[6].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[7].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[8].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[9].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[10].adder/carry_out> (without init value) has a
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 $\alpha_{mul_7\_chip/gen[9].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{mul_9}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1} = 1.2$  chip/gen[9].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  constant value of 0 in block  $\alpha_{1} = 1.2$  constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[9].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[9].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 1.2$  chip/g

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \alpha_{12} = 12$ . adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 12$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[9].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[9].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[15].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[9].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[29].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[9].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
 < a_{mul_7\_chip/gen[8].adder/gen[0].adder/carry\_out> \ (without\ init\ value)\ has\ a\ constant 
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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<a mul 7 chip/gen[14].adder/gen[1].adder/carry out> (without init value) has a

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constant value of 0 in block \mbox{LR2\_V000\_Ionisyan}. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{1,2} = 14.4$  adder/gen[2].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[14].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[14].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 14.3$  adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[14].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 14.4$  adder/gen[7].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 14.4$  adder/gen[8].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 14.4$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.0$  (without init value) has a constant value of 0 in block

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[14].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 14.4$  adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10.4$  This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[14].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[14].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[14].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ <a_mul_7\_chip/gen[14].adder/gen[15].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10} = 14.4$  adder/gen[14].adder/gen[16].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 1.4$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[14].adder/gen[17].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[14].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[23].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[14].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[29].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[15].adder/gen[2].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[15].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[4].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[15].adder/gen[5].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

 $\alpha_{1,2} = 15$ .adder/gen[6].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 15$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 15$ .adder/gen[7].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[15].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 15$ .adder/gen[9].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=15].adder/gen[10].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=1.3$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.3$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.3$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.3$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.3$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.3$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \alpha_{15} = 15$ . adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = \alpha_{15} \cdot \alpha$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}=0.000$  <a href="mailto:square;"><a\_mul\_7\_chip/gen[15].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 15$ .adder/gen[15].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 15$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[15].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[15].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[15].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=15].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=1.5$ . adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.5$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[15].adder/gen[21].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[15].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[23].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[15].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[15].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[15].adder/gen[29].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[15].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[14].adder/gen[0].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[12].adder/gen[2].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[12].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[4].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[12].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[12].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[12].adder/gen[9].adder/carry out> (without init value) has a

 $\alpha_{12} = 12$ .adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \beta_{12} = 12$ . adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 12$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[12].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[12].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=12].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 12$ .adder/gen[12].adder/gen[15].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a cons

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 12$ .adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 12$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}-\alpha_{12}.adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{12}-\alpha_{12}.adder/carry_out>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \beta_{12} = 12$ . adder/gen[19].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 12$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[12].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[12].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[12].adder/gen[22].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\min_{j=0}} (12).adder/gen[23].adder/carry_out) $$ (without init value) has a constant value of 0 in block $$ <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \alpha_{12} = 12$ . adder/gen[24].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}-\beta_{12}.adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{12}V000_{10}.$  This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[12].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[12].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[11].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[2].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[13].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[13].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[13].adder/gen[13].adder/carry out> (without init value) has a

 $\alpha_{13}.adder/gen[13].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[14].adder/$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{182}.Del{v}$  This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[13].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[13].adder/gen[17].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{17}.adder/carry_out>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\min_{j=0}}.adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block $$ <LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[13].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{182}.Del{v000}.This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{182}.D000_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{182}.Del{v000}.Del{v000}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[13].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{13}.adder/carry_out>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[13].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[13].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[13].adder/gen[26].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \aligned a mul_7\_chip/gen[13].adder/gen[27].adder/carry\_out> (without init value) has a constant value of 0 in block $$ \aligned LR2\_V000\_Ionisyan>$$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{182}.Donisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[13].adder/gen[29].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[13].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[12].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[2].adder/gen[1].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
 \begin{tabular}{ll} $$ < a_mul_7\_chip/gen[1].adder/gen[0].adder/carry\_out> (without init value) has a constant in the substant of the subst
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[0].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[1].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[2].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[3].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[4].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[5].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[6].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[7].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[8].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[9].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[13].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[0].adder/gen[14].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[20].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[0].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[26].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[0].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[1].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[2].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
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optimization process.

optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[3].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[4].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[5].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[6].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[7].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[8].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[9].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[3].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[3].adder/gen[21].adder/carry out> (without init value) has a

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[3].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[3].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[3].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[3].adder/gen[26].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a consta

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{adder/gen} = 1.2 \, \text{carry_out} = 1.2 \, \text{(without init value)} = 1.2 \, \text{(without init$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{adder/gen} = 1.2 \, \text{der/carry_out} = 1.2 \, \text{der/carry_$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[3].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[2].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[30].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[30].adder/gen[1].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow2\_chip/gen[30].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[30].adder/gen[10].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[30].adder/gen[12].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[30].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[30].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2_chip/gen[30].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[30].adder/gen[21].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[30].adder/gen[22].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow2\_chip/gen[30].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[30].adder/gen[27].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[0].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 10^{-2} - 10^{$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[31].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[31].adder/gen[8].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_pow2_chip/gen[31].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[31].adder/gen[12].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[31].adder/gen[14].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[31].adder/gen[16].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[31].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[31].adder/gen[21].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2_chip/gen[31].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[31].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[31].adder/gen[26].adder/carry\_out> (without init value) has a

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process. WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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 $<a_{plus_b_pow2\_chip/gen[31].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[31].adder/gen[28].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[6].adder/gen[1].adder/carry\_out> (without init value) has a constant
value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[6].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 - 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.00$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen[6].adder/gen[4].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2 \, \text{der/gen[4].adder/carry_out}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \times 10^{-2} \, \text{chip/gen[6].adder/gen[5].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2 \times 10^{-2} \, \text{chip/gen[6].adder/carry_out}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 10^{-2} \, \text{chip/gen[6].adder/gen[6].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 10^{-2} \, \text{chip/gen[6].adder/carry_out}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \times 10^{-2} \, \text{chip/gen[6].adder/gen[7].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2 \times 10^{-2} \, \text{chip/gen[6].adder/gen[7].adder/carry_out}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[6].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[6].adder/gen[9].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10} - \alpha_{10} = 6$ . adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 0$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[6].adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[6].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[6].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/gen[13].adder/gen$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[6].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[6].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[24].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[6].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[0].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
 < a_{mul}_7\_chip/gen[7].adder/gen[1].adder/carry\_out> \ (without init value) \ has a constant 
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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<a mul 7 chip/gen[7].adder/gen[2].adder/carry out> (without init value) has a constant

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value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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<a\_mul\_7\_chip/gen[7].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/carry_out} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/ge$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen[8].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2 \, \text{der/gen[8].adder/carry_out}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[7].adder/gen[9].adder/carry\_out> (without init value) has a constant
value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}, \alpha_{10}, \alpha_{1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[7].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}.adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{12}.adder/carry_out>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[7].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[7].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[7].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{dder/gen} = 1.2 \, \text{dder/carry_out} = 1.2 \, \text{dder/gen} = 1.2 \, \text{$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17}=0.000$ , adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{17}=0.000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[7].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[7].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[7].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[7].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[7].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[7].adder/gen[28].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[7].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/qen[7].adder/qen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[6].adder/gen[0].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[1].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[2].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[3].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[4].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
 < a_{mul_7\_chip/gen[4].adder/gen[5].adder/carry\_out> \ (without\ init\ value)\ has\ a\ constant 
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[6].adder/carry out> (without init value) has a constant
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value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[7].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
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optimization process. WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=1.2000$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2000$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[4].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}, \alpha_{10}, \alpha_{1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[4].adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13}.adder/gen[4].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{13}.adder/carry_out>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 -$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[4].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 - 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.0$  (without init value) has a constant value of 0 in b

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[4].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[4].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[4].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=0.00$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=0.00$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen} = 1$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} - \alpha_{13} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.00$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[4].adder/gen[23].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[26].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[4].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[3].adder/gen[0].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[1].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[2].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[3].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[4].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[5].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[6].adder/carry_out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[7].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[5].adder/gen[8].adder/carry out> (without init value) has a constant
value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
 < a_{mul_7\_chip/gen[5].adder/gen[9].adder/carry\_out> \ (without\ init\ value)\ has\ a\ constant 
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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<a mul 7 chip/gen[5].adder/gen[10].adder/carry out> (without init value) has a

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{11}, \alpha_{12}, \beta_{13}, \beta_{14}, \beta_{15}, \beta_{1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[5].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[5].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[5].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[5].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 10^{-2} - 10^{$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17}=0.000$ , adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{17}=0.000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 1.2 \, \text{chip/gen} = 1.2 \, \text{carry_out} > 1.2 \, \text{chip/gen} = 1.2 \, \text{constant value} > 1.2 \, \text{constant value} > 1.2 \, \text{chip/gen} = 1.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[5].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{camul} = 1.2 \, \text{c$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[5].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[5].adder/gen[23].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[5].adder/gen[24].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline"><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.0$  (be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[5].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[5].adder/gen[27].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[5].adder/gen[30].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[4].adder/gen[0].adder/carry out> (without init value) has a constant
value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during the
optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[2].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[26].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[26].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[26].adder/gen[13].adder/carry out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{1,2}=1.00$  <a\_mul\_7\_chip/gen[26].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[26].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[26].adder/gen[17].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\mathrm{mul}_7\_\mathrm{chip/gen[26].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.20$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.20$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[26].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[26].adder/gen[24].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ <a_mul_7\_chip/gen[26].adder/gen[26].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[26].adder/gen[27].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 10^{-2} \, \text{chip/gen} = 10^{-2} \, \text{adder/gen} = 10^{-2} \, \text{adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 10^{-2} \, \text{cm}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[26].adder/gen[29].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[26].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[2].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[4].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[8].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[27].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[27].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
```

<a mul 7 chip/gen[27].adder/gen[17].adder/carry out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[27].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=0 \ one \ constant value of 0 in block <$LR2_V000_Ionisyan>$.$ This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=\sup_{0 \le 1}.adder/gen[22].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.4$  (without init value) has a cons

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[27].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[27].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[27].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[27].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=0. $$ a_{\min_2^c,i}=0. $$ a_$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.24 \, \text{adder/gen} = 1.24 \, \text{der/carry_out} = 1.24 \, \text{der/carry_out} = 1.24 \, \text{der/gen} =$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.4 \, \text{adder/gen} = 1.4 \, \text{carry\_out} = 1.4 \, \text{constant value} = 1.4 \, \text{constant value}$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[24].adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[24].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[4].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[6].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[24].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[24].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[24].adder/gen[21].adder/carry out> (without init value) has a

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constant value of 0 in block \mbox{LR2\_V000\_Ionisyan}\mbox{.} This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[24].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[24].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[24].adder/gen[25].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\mathrm{mul}_7\_\mathrm{chip/gen[24].adder/gen[26].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/carry} = 1$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.4 \, \text{adder/gen} = 1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value of 0 in block } -1.4 \, \text{constant value} = 1.4 \, \text{cons$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.4 \, \text{carry_out} = 1.4 \, \text{constant value} = 1.4 \, \text{constant v$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[24].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[25].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[25].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[25].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[25].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.25$ . adder/gen[4].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.25$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{chip/gen} = 1.2 \, \text{constant value} = 1.2 \, \text{constant va$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[25].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[25].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[11].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[16].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[25].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[25].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[25].adder/gen[25].adder/carry out> (without init value) has a

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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 $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{mul}_{22} = 1.4 \, \text{mul}_{2$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.25$ . adder/gen[29].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.25$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=0. $$ adder/gen[30].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 1.2 \, \text{chip/gen} = 1.30 \, \text{adder/gen} = 1.30 \, \text{der/carry_out} = 1.30 \, \text{der/carry_out} = 1.30 \, \text{der/gen} = 1.30 \, \text{der/carry_out} = 1.30 \, \text{der/carry_out}$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 10^{-2} - 10^{-2} = 10^{-2} = 10^{-2} - 10^{-2} = 10^{-2} - 10^{-2} = 10^{$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[30].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[30].adder/gen[4].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[30].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[30].adder/gen[7].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 10^{-2} - 10^{-2} = 10^{-2} - 10^{-2} = 10^{$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}^2-\alpha_{10}^2=100$ . adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}^2-\alpha_{10}^2=1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1^c,i}=0. $$ adder/gen[30].adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block $$ \end{carry_out>}$. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[12].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[14].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[18].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[20].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[30].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[30].adder/gen[28].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[30].adder/gen[29].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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 $\alpha_{10}^2 - \alpha_{10}^2 = 130$ .adder/gen[30].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{100}^2 - \alpha_{100}^2 = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[31].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[31].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{carry_out} > \text{(without init value)} \ \text{has a constant value of 0 in block } \ \text{LR2_V000_Ionisyan} > \text{.} \ \text{This FF/Latch will be trimmed during the optimization process.}$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[31].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{carry_out} > \text{(without init value)}$  has a constant value of 0 in block  $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/carry_out} = 1.2 \, \text{der/carry_out$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{carry_out} > \text{(without init value)}$  has a constant value of 0 in block  $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[31].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[31].adder/gen[8].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[31].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.00$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[31].adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=1. adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.4$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[31].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[31].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[16].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[22].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[24].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[31].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[31].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[0].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[1].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[28].adder/gen[2].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.8 \, \text{adder/gen} = 1.2 \, \text{adder/carry_out} = 1.2 \, \text{(without init value)} = 1.2 \, \text{(withou$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.2 \, \text{chip/g$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[28].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.00$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[28].adder/gen[7].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{chip/gen} = 1.8 \, \text{adder/gen} = 1.8 \, \text{adder/carry_out} = 1.8 \, \text{der/gen} = 1.8 \, \text{d$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.8 \, \text{constant value} = 1.8 \, \text{const$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ , adder/gen[28].adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[28].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 128 \, \text{adder/gen} = 121 \, \text{adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2 \, \text{chip/gen} = 1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[28].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[28].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[28].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[28].adder/gen[16].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17} = 10^{-2} \, \text{chip/gen} = 10^{-2} \, \text{adder/gen} = 10^{-2} \, \text{adder/carry} = 10^{-2} \, \text{without init value}$  has a constant value of 0 in block  $\alpha_{17} = 10^{-2} \, \text{cm}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[28].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[28].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[28].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[28].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[28].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[29].adder/gen[0].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[1].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[2].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[29].adder/gen[3].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[29].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[5].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[29].adder/gen[6].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (block value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (block value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (block value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constan

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=1. $$ adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = \alpha_{13} \cdot \alpha$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[29].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[29].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}-\beta_{13}=0$  (without init value) has a constant value of 0 in block  $\alpha_{13}=0$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[29].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[24].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[26].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[29].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[28].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[29].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[29].adder/gen[30].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[1].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[2].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[3].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[5].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[6].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[8].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[18].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[18].adder/gen[11].adder/carry out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{12} = 18$ .adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[18].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=18].adder/gen[16].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{182} = 1000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[18].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 1.81.4$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant value of 0 in block  $\alpha_{13} = 1.8$  (without init value) has a constant val

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[18].adder/gen[22].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[18].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[18].adder/gen[24].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[18].adder/gen[25].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 18$ .adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[18].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[18].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[29].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[18].adder/gen[30].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[1].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[2].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[3].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[5].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[8].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[19].adder/gen[13].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[14].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
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the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a mul 7 chip/gen[19].adder/gen[15].adder/carry out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{10} = 19$ .adder/gen[19].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=1.00$  <a\_mul\_7\_chip/gen[19].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = 19$ .adder/gen[19].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{13} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[19].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=1, \ adder/gen[20].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=1.2$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 19.4$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 100$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[19].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 10^{-2} \cdot 10^{-$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[19].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[19].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[19].adder/gen[28].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ <a_mul_7\_chip/gen[19].adder/gen[29].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ , adder/gen[30].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[18].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[16].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[2].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[3].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[4].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[5].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[6].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[8].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[10].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[13].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[14].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[15].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[16].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[16].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[16].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[16].adder/gen[19].adder/carry out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
```

 $\alpha_{10} = 16$ .adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[16].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[16].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10} = 16$ .adder/gen[23].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 16$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=[24].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1} = 16$ .adder/gen[25].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1} = 16$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}^2 - \alpha_{10}^2 = 16$ .adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}^2 - \alpha_{10}^2 = 16$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2}=16].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{1,2}=16$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[16].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ , adder/gen[16].adder/gen[29].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[16].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[15].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[17].adder/gen[2].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant value of 0 in block  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a constant  $\alpha_{17} = 17.4$  (without init value) has a consta

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[17].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_mul\_7\_chip/gen[17].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[6].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[7].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[8].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[13].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[14].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[15].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[16].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[20].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[17].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[17].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
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<a mul 7 chip/gen[17].adder/gen[23].adder/carry out> (without init value) has a

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{17}=1.17$ . adder/gen[24].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{17}=1.17$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{17}=1.2$  (without init value) has a cons

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[17].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=1,\ adder/gen[27].adder/carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ a_{\min_1,\ldots,n}=1,\ adder/gen[28].adder/carry_out> (without init value) has a constant value of 0 in block $$ LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17}=1.17$ .adder/gen[29].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{17}=1.17$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{17}=0.000$  (without init value) has a constant value of 0 in block  $\alpha_{17}=0.000$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10} = 16$ .adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10} = 10$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[22].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[22].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[22].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[22].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}=1.20$  (without init value) has a constant value of 0 in block  $\alpha_{12}=0.00$  [onisyan]. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[22].adder/gen[7].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[22].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[9].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[11].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[12].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[14].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[15].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[16].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[17].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[20].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[21].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[22].adder/gen[24].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[22].adder/gen[25].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[22].adder/gen[26].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process. WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
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 $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (but init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.00$  (without init value) has a constant  $\alpha_{12} = 1.00$  (with

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[22].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12}=1.2$  (without init value) has a cons

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\mathrm{mul}_7\_\mathrm{chip/gen[23].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a consta

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.3 \, \text{adder/gen} = 1.3 \, \text{adder/carry_out} = 1.3 \, \text{der/gen} = 1.3 \, \text{de$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a constant  $\alpha_{1,2} = 1.2$  (without init value) has a consta

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[23].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[23].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_mul_7\_chip/gen[23].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[23].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[23].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12}=1.20$  (without init value) has a constant value of 0 in block  $\alpha_{12}=0.00$  [onisyan]. This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10} = 10^{-2} \, \mathrm{chip/gen} = 10^{-2} \, \mathrm{chip/ge$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[23].adder/gen[11].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[23].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[13].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[14].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan >. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[15].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[16].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[17].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[19].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[20].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[23].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[24].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[25].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[26].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[27].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[23].adder/gen[28].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[23].adder/gen[29].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[23].adder/gen[30].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{1,2} = 1.20$ . adder/gen[1].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.20$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[20].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{carry_out} > 1.2 \, \text{chip/gen} = 1.2 \, \text{constant value} = 1.2 \, \text{constant value} = 1.2 \, \text{chip/gen} = 1.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.20$ . adder/gen[4].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.20$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ \sim_{\mathrm{mul}_7\_\mathrm{chip/gen[20].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 10^{-2} \, \text{chip/gen} = 10^{-2} \, \text{adder/gen} = 10^{-2} \, \text{adder/carry_out} = 10^{-2} \, \text{(without init value)} = 10^{-2} \,$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.20$ . adder/gen[7].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.20$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[20].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}=10$ . adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}=10$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[20].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[20].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[20].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[20].adder/gen[14].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[20].adder/gen[16].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[20].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[18].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[19].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[20].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[21].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[22].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[23].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[24].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[25].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[26].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[27].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[28].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[20].adder/gen[29].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[20].adder/gen[30].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[19].adder/gen[0].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[21].adder/gen[0].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch
<a mul 7 chip/gen[21].adder/gen[1].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_mul_7_chip/gen[21].adder/gen[2].adder/carry_out> (without init value) has a
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constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during

<a mul 7 chip/gen[21].adder/gen[3].adder/carry out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

the optimization process.

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $\alpha_{1,2} = 1.2 \, \text{der/gen}[21].adder/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{1,2} = 1.2 \, \text{der/gen}[4].adder/carry_out> (without init value) has a constant value of 0 in block (without init value) has a constant value of 0 in bloc$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/gen} = 1.2 \, \text{der/carry_out} = 1.2 \, \text{der/carry_out$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[21].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2 \, \text{chip/gen} = 1.2 \, \text{chip/gen} = 1.2 \, \text{constant value} = 1.2 \, \text{constant va$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{10}^2 - \alpha_{10}^2 = 1.4$  adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block  $\alpha_{10}^2 - \alpha_{10}^2 = 1.4$  be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[21].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{13} = \alpha_{13} \cdot \alpha$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[21].adder/gen[16].adder/carry_out> (without init value) has a constant value of 0 in block LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camulation} $$ <a_mul_7\_chip/gen[21].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:camul_7_chip/gen[21].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.2$  (without init value) has a constant value

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{mul_7\_chip/gen[21].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = \alpha_{13} = \alpha$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init value) has a constant value of 0 in block  $\alpha_{1,2} = 1.2$  (without init

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{12} = 1.4$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.0$  (without init value) has a constant value of 0 in block  $\alpha_{12} = 1.0$  (be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{mul_7\_chip/gen[21].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{LR2\_V000\_Ionisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[25].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[25].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[25].adder/gen[19].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[25].adder/gen[20].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[25].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[25].adder/gen[24].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carplus_b_pow3_chip/gen[24].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[24].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[24].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/gen[$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[24].adder/gen[9].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[24].adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[24].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[26].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[26].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3_chip/gen[26].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[26].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[26].adder/gen[19].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[26].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[26].adder/gen[25].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[25].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/gen[2$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[25].adder/gen[6].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[25].adder/gen[7].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[25].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[25].adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[25].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[25].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[22].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[22].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[22].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[22].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[22].adder/gen[16].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[22].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[22].adder/gen[18].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[22].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow3\_chip/gen[21].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[21].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/gen[$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[21].adder/gen[6].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[21].adder/gen[9].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[21].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[24].adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[24].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[24].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[23].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[23].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[23].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[23].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[23].adder/gen[14].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[23].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow3\_chip/gen[23].adder/gen[18].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[23].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[23].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[23].adder/gen[22].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0]. This $$ FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[22].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[29].adder/gen[1].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[29].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[29].adder/gen[4].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[29].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[29].adder/gen[9].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_b_pow3\_chip/gen[29].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[29].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[29].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[29].adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_b_pow3\_chip/gen[29].adder/gen[21].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[29].adder/gen[22].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[29].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[29].adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[29].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[29].adder/gen[28].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[30].adder/gen[4].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[30].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[30].adder/gen[7].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

 $<a_plus_b_pow3_chip/gen[30].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[30].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[30].adder/gen[11].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[30].adder/gen[13].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[30].adder/gen[15].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[30].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[30].adder/gen[20].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[30].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[30].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[30].adder/gen[25].adder/carry\_out> (without init value) has a

constant value of 0 in block  $\ensuremath{<} LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[30].adder/gen[26].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[30].adder/gen[27].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[30].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3_{\text{chip}/\text{gen}[30].adder/\text{gen}[29].adder/\text{carry}_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2}} = 1000_{\text{lonisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carplus_b_pow3_chip/gen[27].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[27].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[27].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[27].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[27].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[27].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[26].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[26].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[26].adder/gen[8].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{\text{LR2}\_\text{V000}\_\text{Ionisyan}}. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[28].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[28].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\mathrm{pow3\_chip/gen[28].adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[28].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[28].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[28].adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[28].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[28].adder/gen[22].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[28].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[28].adder/gen[26].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[28].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[27].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3_chip/gen[11].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[11].adder/gen[7].adder/carry\_out> (without init value) has a

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constant value of 0 in block \ensuremath{<} LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.
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 $<a_plus_b_pow3\_chip/gen[11].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow3\_chip/gen[10].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\mathrm{pow3\_chip/gen[10].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[10].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[10].adder/gen[9].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[9].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[9].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[9].adder/gen[2].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

 $\alpha_{plus_b_pow3\_chip/gen[9].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[9].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[9].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[9].adder/gen[7].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[9].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[8].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[8].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[8].adder/gen[2].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[8].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[8].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[13].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[13].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[13].adder/gen[5].adder/carry\_out> (without init value) has a

constant value of 0 in block  $\ensuremath{<} LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[13].adder/gen[9].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[13].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[13].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[12].adder/gen[6].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[12].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[12].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[11].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[0].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[0].adder/gen[1].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[0].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[0].adder/gen[4].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[0].adder/gen[5].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[0].adder/gen[6].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[0].adder/gen[8].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_b_pow3_chip/gen[0].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[0].adder/gen[11].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carplus_b_pow3_chip/gen[0].adder/gen[16].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \ag{$a_plus_b_pow3\_chip/gen[0].adder/gen[20].adder/carry_out> (without init value) has a constant value of 0 in block $$ \ag{$LR2\_V000\_Ionisyan>}$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[0].adder/gen[25].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[0].adder/gen[28].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[0].adder/gen[30].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[8].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[8].adder/gen[6].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[8].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[7].adder/gen[0].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[7].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[7].adder/gen[2].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[7].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[7].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[7].adder/gen[5].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[7].adder/gen[6].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[6].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[6].adder/gen[1].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[6].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[6].adder/gen[3].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[6].adder/gen[4].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[6].adder/gen[5].adder/carry\_out> (without init value) has a

 $\ag{a_plus_b_pow3\_chip/gen[5].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[5].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[5].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus_b_pow3\_chip/gen[5].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[4].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[4].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[4].adder/gen[2].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow3\_chip/gen[4].adder/gen[3].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[3].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[3].adder/gen[1].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[3].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[2].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[2].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[1].adder/gen[0].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[19].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[19].adder/gen[4].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[19].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[19].adder/gen[12].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[19].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[19].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3_chip/gen[18].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[18].adder/gen[3].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[18].adder/gen[5].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow3\_chip/gen[18].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[18].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[18].adder/gen[11].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\mathrm{pow3\_chip/gen[18].adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[18].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[21].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[21].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[21].adder/gen[16].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[21].adder/gen[18].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[21].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[21].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[20].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\mathrm{pow3\_chip/gen[20].adder/gen[10].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[20].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[20].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[20].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_pow3_chip/gen[20].adder/gen[14].adder/carry_out}$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[20].adder/gen[15].adder/carry\_out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[20].adder/gen[17].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[20].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[20].adder/gen[19].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow3\_chip/gen[19].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[19].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[19].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\begin{tabular}{ll} $<$a_plus_b_pow3\_chip/gen[16].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block $<$LR2_V000_Ionisyan>$.$ This FF/Latch will be trimmed during the optimization process. \\ \end{tabular}$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[16].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[15].adder/gen[2].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[15].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[15].adder/gen[5].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[15].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[15].adder/gen[12].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[15].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[15].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[14].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[14].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[14].adder/gen[8].adder/carry\_out> (without init value) has a

 $<a_{plus_b_pow3\_chip/gen[14].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[14].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[18].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[18].adder/gen[17].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[17].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[17].adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[2].adder/gen[$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[17].adder/gen[3].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[17].adder/gen[7].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[17].adder/gen[9].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[17].adder/gen[10].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[17].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[17].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[16].adder/gen[0].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[16].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3_chip/gen[16].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[16].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[16].adder/gen[11].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local-constant} $$ \adder/gen[23].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[3].adder/ge$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[23].adder/gen[7].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[23].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[23].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[23].adder/gen[21].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[23].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[22].adder/gen[1].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[25].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[16].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[22].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0]. This $$ FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[24].adder/gen[5].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[24].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[24].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[21].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2_chip/gen[21].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[21].adder/gen[19].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow2\_chip/gen[20].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \ag{$a_plus_b_pow2\_chip/gen[20].adder/gen[7].adder/carry_out} $$ (without init value) has a constant value of 0 in block $$ \ag{$LR2\_V000\_Ionisyan}$$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[20].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[20].adder/gen[15].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{plus_pow2_chip/gen[20].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{plus_pout}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[20].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[22].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[22].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[22].adder/gen[14].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[22].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \ag{$a_plus_b_pow2\_chip/gen[21].adder/gen[1].adder/carry_out} $$ (without init value) has a constant value of 0 in block $$ \ag{$LR2\_V000\_Ionisyan}$$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[21].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[4].adder/gen[$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[21].adder/gen[6].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[21].adder/gen[9].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[21].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[28].adder/gen[10].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[28].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2_chip/gen[28].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[28].adder/gen[14].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[28].adder/gen[15].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[28].adder/gen[16].adder/carry\_out> (without init value) has a

 $<a_{plus_b_pow2\_chip/gen[28].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[28].adder/gen[22].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b\_pow2\_chip/gen[28].adder/gen[25].adder/carry\_out> \mbox{ (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[28].adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[27].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[30].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[29].adder/gen[7].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[29].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[29].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[29].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[29].adder/gen[16].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[29].adder/gen[18].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[29].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\begin{tabular}{ll} $<$a_plus_b_pow2\_chip/gen[29].adder/gen[27].adder/carry_out> (without init value) has a constant value of 0 in block $<$LR2_V000_Ionisyan>$.$ This FF/Latch will be trimmed during the optimization process. \\ \end{tabular}$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[29].adder/gen[28].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[28].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_{plus_b_pow2\_chip/gen[26].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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<a\_plus\_b\_pow2\_chip/gen[26].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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<a\_plus\_b\_pow2\_chip/gen[26].adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[26].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[25].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[5].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[25].adder/gen[11].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[25].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[27].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/gen[5].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[5].adder/gen$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[27].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[27].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2_chip/gen[27].adder/gen[15].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[18].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[20].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[27].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[24].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[27].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[27].adder/gen[26].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[26].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[26].adder/gen[4].adder/carry\_out> (without init value) has a

 $<a_{plus_b_pow2\_chip/gen[26].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[26].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus_b_pow2\_chip/gen[8].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[8].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[8].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[7].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[7].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[7].adder/gen[2].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[7].adder/gen[3].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[7].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[7].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[7].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[6].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[6].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[6].adder/gen[2].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[6].adder/gen[3].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[6].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[6].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[5].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[5].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[5].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[5].adder/gen[3].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[5].adder/gen[4].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[4].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[4].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[4].adder/gen[2].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[4].adder/gen[3].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[3].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[3].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[3].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[2].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[2].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[11].adder/gen[3].adder/carry\_out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow2\_chip/gen[11].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[10].adder/gen[6].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[10].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $\ag{a_plus_b_pow2\_chip/gen[9].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[9].adder/gen[1].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[9].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[9].adder/gen[3].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[9].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[9].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[9].adder/gen[6].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[9].adder/gen[7].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[9].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[8].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[8].adder/gen[1].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[8].adder/gen[2].adder/carry_out> (without init value) has a constant value of 0 in block <math display="inline">\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out> (without init value) has a constant value of 0 in block $$ \LR2_V000_Ionisyan>$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \ag{acc} plus_b_pow2\_chip/gen[0].adder/gen[30].adder/carry_out> (without init value) has a constant value of 0 in block $$ \arrowvert LR2_V000_Ionisyan>$$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3_chip/gen[31].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[31].adder/gen[3].adder/carry\_out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[31].adder/gen[4].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3_{\text{chip}/\text{gen}[31]}.adder/\text{gen}[7].adder/\text{carry}_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2}} = pounds$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow3\_chip/gen[31].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[31].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[31].adder/gen[11].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[31].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[12].adder/gen$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[31].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[31].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow3\_chip/gen[31].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[31].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow3\_chip/gen[31].adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[21].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3_chip/gen[31].adder/gen[22].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2_v000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[31].adder/gen[24].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[25].adder/carry_out> (without init value) has a constant value of 0 in block <math><LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[26].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow3\_chip/gen[31].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow3\_chip/gen[31].adder/gen[28].adder/carry\_out> (without init value) has a constant value of 0 in block <math display="inline">\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus}} = pow3\_ chip/gen[31].adder/gen[29].adder/carry_out> (without init value) has a constant value of 0 in block <math>\alpha_{\text{LR2}} = 0.000$  This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[1].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[0].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[0].adder/gen[1].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[2].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[3].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[0].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[0].adder/gen[5].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[6].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[0].adder/gen[7].adder/carry\_out> (without init value) has a

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[8].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carplus_b_pow2_chip/gen[0].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[18].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[19].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[0].adder/gen[20].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[0].adder/gen[21].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[22].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[23].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[0].adder/gen[24].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[25].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\ag{a_plus_b_pow2\_chip/gen[0].adder/gen[26].adder/carry_out>}$  (without init value) has a constant value of 0 in block  $\ag{LR2\_V000\_Ionisyan>}$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[27].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[28].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[0].adder/gen[29].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[18].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[17].adder/gen[3].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[17].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[17].adder/gen[8].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\text{plus\_b\_pow2\_chip/gen[17].adder/gen[12].adder/carry\_out}$  (without init value) has a constant value of 0 in block  $\alpha_{\text{LR2\_V000\_Ionisyan}}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[17].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/gen[0].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[0].adder/gen[0]$ 

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[16].adder/gen[5].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[19].adder/gen[7].adder/carry\_out> (without init value) has a
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WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[19].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\rm plus_b_pow2\_chip/gen[19].adder/gen[13].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[19].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[16].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[19].adder/gen[17].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[19].adder/gen[18].adder/carry\_out> (without init value) has a

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow2\_chip/gen[18].adder/gen[4].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \adder/gen[18].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/gen[8].adder/carry_out> (without init value) has a constant value of 0 in block $$ \adder/gen[8].adder/gen$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[18].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[18].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_plus_b_pow2\_chip/gen[18].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

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 $<a_{plus_b_pow2\_chip/gen[18].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\alpha_{\rm plus\_b\_pow2\_chip/gen[14].adder/gen[11].adder/carry\_out>$  (without init value) has a constant value of 0 in block  $\alpha_{\rm LR2\_v000\_lonisyan}$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[14].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[14].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[13].adder/gen[6].adder/carry\_out> (without init value) has a
constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[13].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{local_constant} $$ \sim_{\rm plus_b_pow2\_chip/gen[13].adder/gen[12].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[12].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[12].adder/gen[4].adder/carry\_out> (without init value) has a

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \sim_{\rm pow2\_chip/gen[12].adder/gen[9].adder/carry_out> (without init value) has a constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[12].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $\label{lem:carry_out} $$ \ag{$a_plus_b_pow2\_chip/gen[11].adder/gen[1].adder/carry_out} $$ (without init value) has a constant value of 0 in block $$ \ag{$LR2\_V000\_Ionisyan}$$. This FF/Latch will be trimmed during the optimization process.$ 

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[11].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[16].adder/gen[13].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[16].adder/gen[15].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[15].adder/gen[0].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[3].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[4].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[5].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[6].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[7].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[8].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2\_chip/gen[15].adder/gen[9].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[10].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[11].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[12].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[15].adder/gen[13].adder/carry\_out> (without init value) has a constant value of 0 in block <LR2\_V000\_Ionisyan>. This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[15].adder/gen[14].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_{plus_b_pow2_chip/gen[14].adder/gen[0].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2_V000_lonisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[14].adder/gen[1].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch

 $<a_plus_b_pow2\_chip/gen[14].adder/gen[2].adder/carry_out>$  (without init value) has a constant value of 0 in block  $<LR2\_V000\_Ionisyan>$ . This FF/Latch will be trimmed during the optimization process.

WARNING: Xst: 1895 - Due to other FF/Latch trimming, FF/Latch

<a\_plus\_b\_pow2\_chip/gen[14].adder/gen[3].adder/carry\_out> (without init value) has a

```
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a plus b pow2 chip/gen[14].adder/gen[4].adder/carry out> (without init value) has a
constant value of 0 in block <LR2_V000_Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_plus_b_pow2_chip/gen[14].adder/gen[5].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING:Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a plus b pow2 chip/gen[14].adder/gen[6].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a plus b pow2 chip/gen[14].adder/gen[7].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a plus b pow2 chip/gen[14].adder/gen[8].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a plus b pow2 chip/gen[14].adder/gen[9].adder/carry out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
WARNING: Xst:1895 - Due to other FF/Latch trimming, FF/Latch
<a_plus_b_pow2_chip/gen[14].adder/gen[10].adder/carry_out> (without init value) has a
constant value of 0 in block <LR2 V000 Ionisyan>. This FF/Latch will be trimmed during
the optimization process.
Mapping all equations...
Building and optimizing final netlist ...
Found area constraint ratio of 100 (+ 5) on block LR2 V000 Ionisyan, actual ratio is
23.
FlipFlop a plus b chip/gen[1].adder/res has been replicated 1 time(s)
FlipFlop a plus b chip/gen[2].adder/res has been replicated 1 time(s)
Final Macro Processing ...
Processing Unit <LR2 V000 Ionisyan>:
      Found 3-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[30].adder/res>.
      Found 5-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[29].adder/res>.
      Found 7-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[28].adder/res>.
      Found 9-bit shift register for signal
<a_plus_b_pow3_chip/gen[31].adder/gen[27].adder/res>.
      Found 11-bit shift register for signal
<a plus_b_pow3_chip/gen[31].adder/gen[26].adder/res>.
      Found 13-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[25].adder/res>.
      Found 15-bit shift register for signal
<a_plus_b_pow3_chip/gen[31].adder/gen[24].adder/res>.
      Found 17-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[23].adder/res>.
      Found 19-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[22].adder/res>.
      Found 21-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[21].adder/res>.
      Found 23-bit shift register for signal
<a_plus_b_pow3_chip/gen[31].adder/gen[20].adder/res>.
      Found 25-bit shift register for signal
<a_plus_b_pow3_chip/gen[31].adder/gen[19].adder/res>.
      Found 27-bit shift register for signal
<a plus b pow3 chip/gen[31].adder/gen[18].adder/res>.
      Found 29-bit shift register for signal
<a_plus_b_pow3_chip/gen[31].adder/gen[17].adder/res>.
      Found 31-bit shift register for signal
```

<a\_plus\_b\_pow3\_chip/gen[31].adder/gen[16].adder/res>.

```
Found 33-bit shift register for signal
```

- <a\_plus\_b\_pow3\_chip/gen[31].adder/gen[1].adder/res>.
   Found 63-bit shift register for signal

- <a\_plus\_b\_pow2\_chip/gen[31].adder/gen[27].adder/res>.
   Found 11-bit shift register for signal

- <a\_plus\_b\_pow2\_chip/gen[31].adder/gen[23].adder/res>.
  Found 19-bit shift register for signal

- <a\_plus\_b\_pow2\_chip/gen[31].adder/gen[12].adder/res>.

```
Found 41-bit shift register for signal
<a_plus_b_pow2_chip/gen[31].adder/gen[11].adder/res>.
      Found 43-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[10].adder/res>.
      Found 45-bit shift register for signal
<a_plus_b_pow2_chip/gen[31].adder/gen[9].adder/res>.
      Found 47-bit shift register for signal
<a_plus_b_pow2_chip/gen[31].adder/gen[8].adder/res>.
      Found 49-bit shift register for signal
<a plus_b_pow2_chip/gen[31].adder/gen[7].adder/res>.
      Found 51-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[6].adder/res>.
      Found 53-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[5].adder/res>.
      Found 55-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[4].adder/res>.
      Found 57-bit shift register for signal
<a plus_b_pow2_chip/gen[31].adder/gen[3].adder/res>.
      Found 59-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[2].adder/res>.
      Found 61-bit shift register for signal
<a_plus_b_pow2_chip/gen[31].adder/gen[1].adder/res>.
      Found 64-bit shift register for signal
<a plus b pow2 chip/gen[31].adder/gen[0].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[31].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[30].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[29].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[28].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[27].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[26].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[25].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[24].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[23].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[22].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[21].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[20].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[19].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[18].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[17].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[16].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[15].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[14].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[13].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[12].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[11].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[10].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[9].adder/res>.
```

```
Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[8].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[7].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[6].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[5].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[4].adder/res>.
      Found 2-bit shift register for signal
<a_mul_7_chip/gen[0].adder/gen[3].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[2].adder/res>.
      Found 2-bit shift register for signal
<a mul 7 chip/gen[0].adder/gen[1].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[31].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[30].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[29].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[28].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[27].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[26].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[25].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[24].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[23].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[22].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[21].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[20].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[19].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[18].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[17].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[16].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[15].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[14].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[13].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[12].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[11].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[10].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[9].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[8].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[7].adder/res>.
      Found 58-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[6].adder/res>.
      Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[5].adder/res>.
```

```
<a_mul_7_chip/gen[31].adder/gen[4].adder/res>.
     Found 58-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[3].adder/res>.
     Found 59-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[2].adder/res>.
     Found 61-bit shift register for signal
<a_mul_7_chip/gen[31].adder/gen[1].adder/res>.
     Found 64-bit shift register for signal
<a mul 7 chip/gen[31].adder/gen[0].adder/res>.
Unit <LR2_V000_Ionisyan> processed.
______
Final Register Report
Macro Statistics
# Registers
                                             : 3445
Flip-Flops
                                            : 125
# Shift Registers
11-bit shift register
13-bit shift register
15-bit shift register
                                            : 2
17-bit shift register
19-bit shift register
 2-bit shift register
21-bit shift register
                                             : 2
 23-bit shift register
                                             : 2
 25-bit shift register
27-bit shift register
                                            : 2
29-bit shift register
3-bit shift register
                                             : 2
31-bit shift register
 33-bit shift register
 35-bit shift register
37-bit shift register
                                             : 2
39-bit shift register
41-bit shift register
43-bit shift register
                                            : 2
45-bit shift register
47-bit shift register
49-bit shift register
                                            : 2
5-bit shift register
                                            : 2
51-bit shift register
 53-bit shift register
55-bit shift register
57-bit shift register
 58-bit shift register
59-bit shift register
                                             : 3
 61-bit shift register
                                            : 3
 63-bit shift register
 64-bit shift register
                                             : 2
 7-bit shift register
 9-bit shift register
______
                     Partition Report
______
Partition Implementation Status
______
 No Partitions were found in this design.
______
                      Design Summary
______
```

Found 58-bit shift register for signal

```
| BUFGP | 3757 |
Asynchronous Control Signals Information:
No asynchronous control signals found in this design
Timing Summary:
Speed Grade: -3
  Minimum period: 1.257ns (Maximum Frequency: 795.545MHz)
  Minimum input arrival time before clock: 0.565ns
  Maximum output required time after clock: 0.562ns
  Maximum combinational path delay: No path found
Timing Details:
_____
All values displayed in nanoseconds (ns)
______
Timing constraint: Default period analysis for Clock 'clk'
 Clock period: 1.257ns (frequency: 795.545MHz)
 Total number of paths / destination ports: 10721 / 3692
______
           1.257ns (Levels of Logic = 0)
Delav:
 Source: a_plus_b_pow3_chip/gen[31].adder/gen[14].adder/Mshreg_res_0 (FF)
Destination: a_plus_b_pow3_chip/gen[31].adder/gen[14].adder/Mshreg_res_1 (FF)
Source Clock: clk rising
 Destination Clock: clk rising
 Data Path: a_plus_b_pow3_chip/gen[31].adder/gen[14].adder/Mshreg_res_0 to
a plus b pow3 chip/gen[31].adder/gen[14].adder/Mshreg res 1
                          Gate Net
   Cell:in->out fanout Delay Delay Logical Name (Net Name)
   _____
    SRLC32E:CLK->Q31 1 1.099 0.000
a plus b pow3 chip/gen[31].adder/gen[14].adder/Mshreg res 0
(a_plus_b_pow3_chip/gen[31].adder/gen[14].adder/Mshreg_res_0)
    SRLC32E:D
                          0.158
a plus b pow3 chip/gen[31].adder/gen[14].adder/Mshreg res 1
   ______
   Total
                     1.257ns (1.257ns logic, 0.000ns route)
                                  (100.0% logic, 0.0% route)
______
Timing constraint: Default OFFSET IN BEFORE for Clock 'clk'
Total number of paths / destination ports: 341 / 278
                  0.565ns (Levels of Logic = 2)
Offset:
 Source: a<1> (PAD)
Destination: a_plus_b_chip/gen[1].adder/carry_out (FF)
 Destination Clock: clk rising
 Data Path: a<1> to a plus b chip/gen[1].adder/carry out
                           Gate Net
   Cell:in->out fanout Delay Delay Logical Name (Net Name)
   _____
             7 0.003 0.509 a_1_IBUF (a_1_IBUF)
1 0.053 0.000 a plus b chip/gen[1
    IBUF:I->O
                          0.053
                                 0.000 a plus b chip/gen[1].adder/op1 p OR 3 o1
(a plus b chip/gen[1].adder/op1 p OR 3 o)
                         -0.012
                                       a_plus_b_chip/gen[1].adder/carry out
   _____
                          0.565ns (0.056ns logic, 0.509ns route)
   Total
                                 (9.9% logic, 90.1% route)
Timing constraint: Default OFFSET OUT AFTER for Clock 'clk'
 Total number of paths / destination ports: 32 / 32
```

```
ffset: 0.562ns (Levels of Logic = 1)
Source: res_chip/gen[31].adder/res (FF)
Destination: c<31> (PAD)
Source Clock: clk rising
Offset:
 Data Path: res chip/gen[31].adder/res to c<31>
                           Gate
                                  Net
                 fanout Delay Delay Logical Name (Net Name)
   Cell:in->out
            1 0.280 0.279 res_chip/gen[31].adder/res
   FD:C->0
(res_chip/gen[31].adder/res)
                          0.003 c_31_OBUF (c<31>)
   OBUF:I->O
   Total
                         0.562ns (0.283ns logic, 0.279ns route)
                                 (50.3% logic, 49.7% route)
Cross Clock Domains Report:
Clock to Setup on destination clock clk
           | Src:Rise| Src:Fall| Src:Rise| Src:Fall|
Source Clock | Dest:Rise|Dest:Rise|Dest:Fall|Dest:Fall|
-----
      | 1.257| |
clk
______
Total REAL time to Xst completion: 66.00 secs
Total CPU time to Xst completion: 61.35 secs
Total memory usage is 480128 kilobytes
Number of errors : 0 ( 0 filtered)
Number of warnings : 3573 ( 0 filtered)
Number of infos : 2 ( 0 filtered)
```

## 10. Энергопотребление схемы в Ваттах (содержимое таблиц XPower Analyzer среды проектирования).

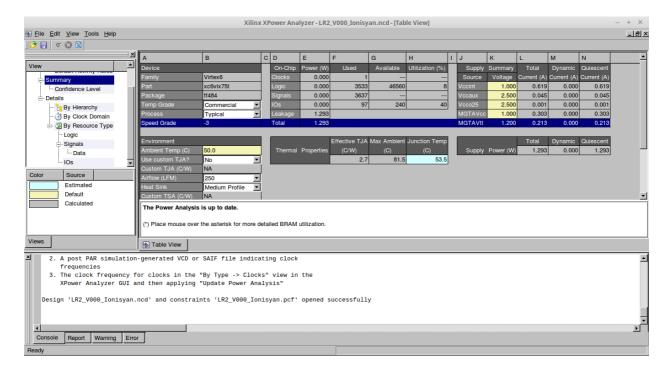
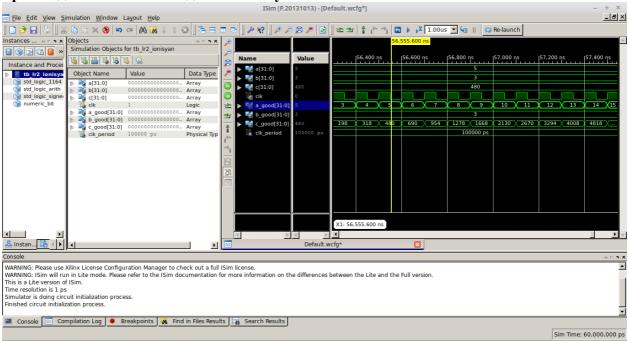


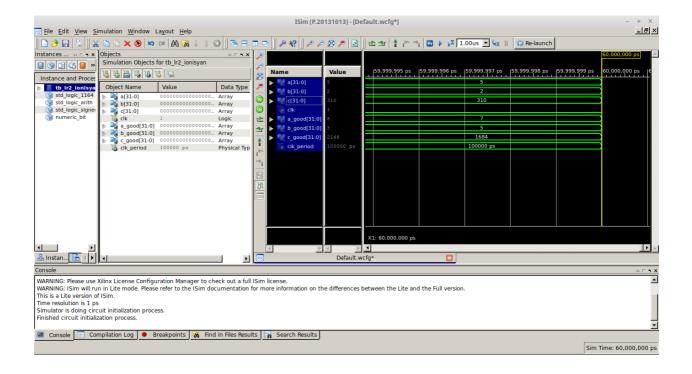
Схема имеет энергопотребление 1,293 Ватта

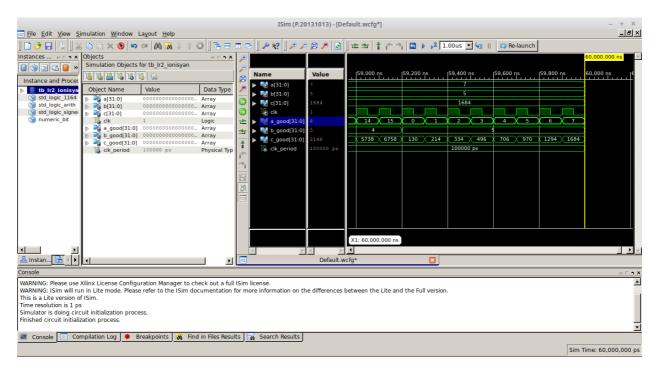
11. Экранный снимок или распечатка результата работы компьютерной программы при введенных тестовых данных из пункта 3 отчета.



Результат совпадает с тестовым решением

12. Исследование модели





## 13. Выводы о проделанной работе.

Мы разработали и реализовали в среде проектирования СБИС Xilinx ISE 14.7 принципиальную схему устройства рассчитывающую значения арифметической функции, синтезировали схему, проверили ее работоспособность на нескольких тестовых наборах входных данных. Существенным недостатком данной схемы является неоптимальная работа библиотеки элементарной арифметики, особенно реализация операции умножения, из-за чего схема не может быть использована в реальных приложениях.