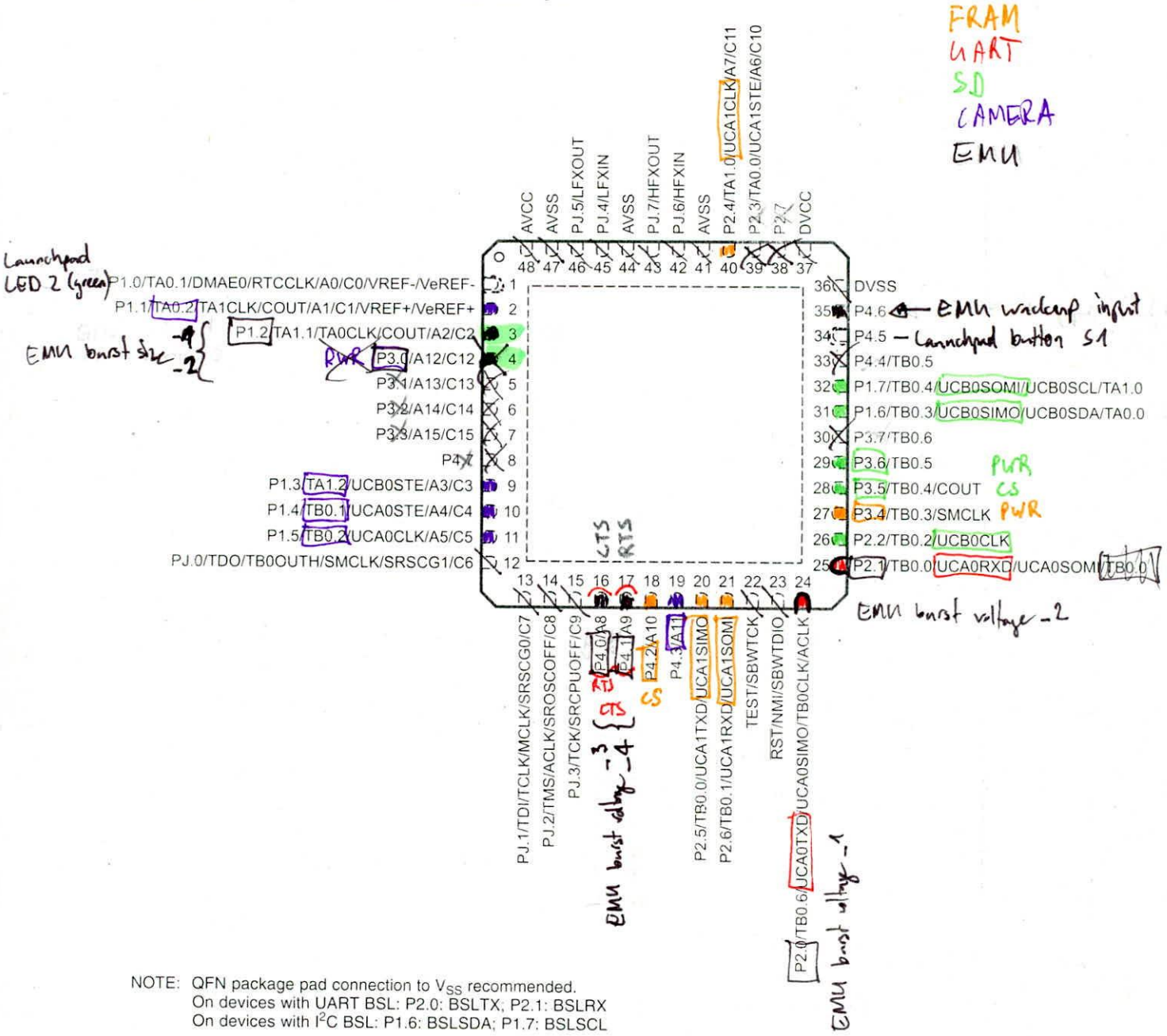
4 Terminal Configuration and Functions

4.1 Pin Diagram – RGZ Package – MSP430FR596x and MSP430FR596x1

Figure 4-1 shows the 48-pin RGZ package.



NOTE: QFN package pad connection to V_{SS} recommended.
On devices with UART BSL: P2.0: BSLTX; P2.1: BSLRX
On devices with I^2C BSL: P1.6: BSLSDA; P1.7: BSLSCL

Figure 4-1. 48-Pin RGZ Package (Top View) – MSP430FR596x and MSP430FR596x1