

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Drawing title:

MSP432 Demoboard MCU

Drawing number: *

Rev: *

Format:

Laboratory: TIK

Project: MCU.SchDoc

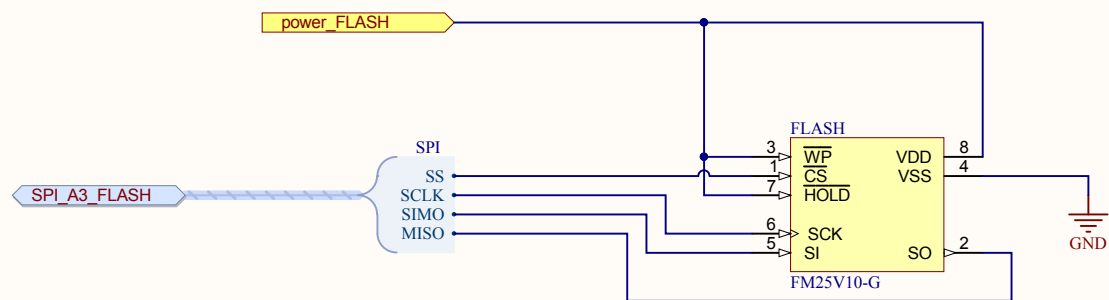
Date: 17.05.2016 12:30:33

A3

Drawn by: Thomas Schallch

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File: C:\Users\user\Dropbox\Masterthesis\PCB Design Course\cad13\MCU.SchDoc



Eidgenössische Technische Hochschule Zürich
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Drawing title:

MSP432 Demoboard FLASH memory

Drawing number: *

Rev: *

Format: Laboratory: TIK

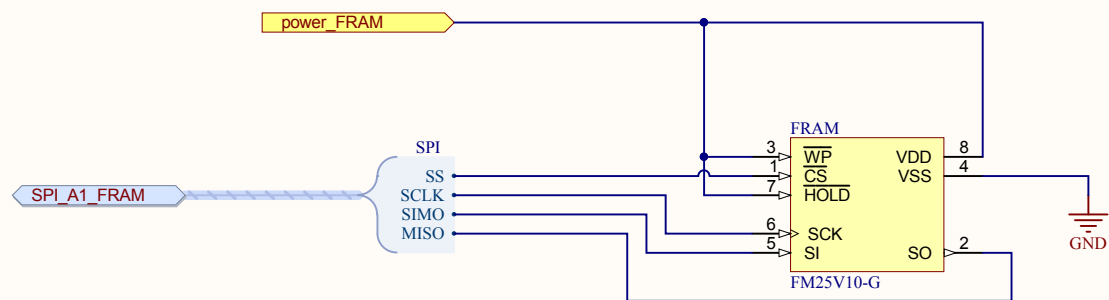
Project: FLASH.SchDoc

Date: 17.05.2016 12:30:33

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File: C:\Users\user\Dropbox\Masterthesis\PCB Design Course\cad13\FLASH.SchDoc



Eidgenössische Technische Hochschule Zürich
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Drawing title:

MSP432 Demoboard FRAM memory

Drawing number: *

Rev: *

Format: Laboratory: TIK

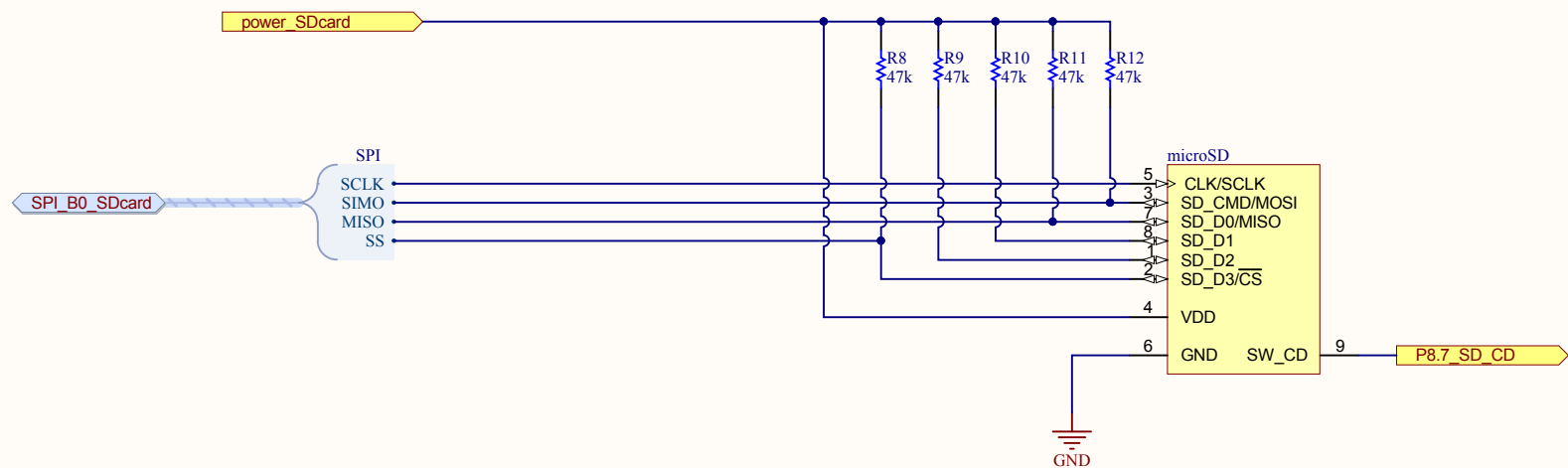
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A4 Q Drawn by: Thomas Schalch

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Eidgenössische Technische Hochschule Zürich
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Drawing title:

MSP432 Demoboard microSD card

Drawing number: *

Rev: *

Format: Laboratory: TIK

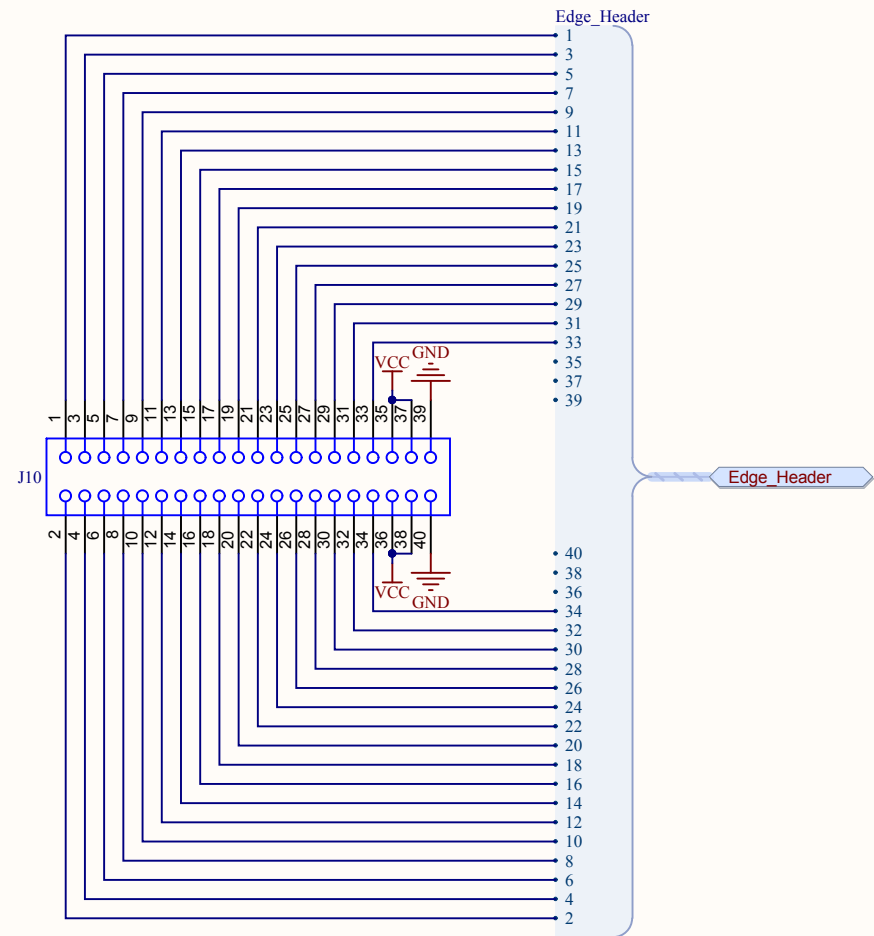
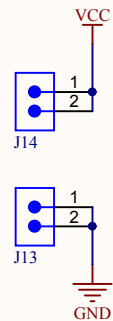
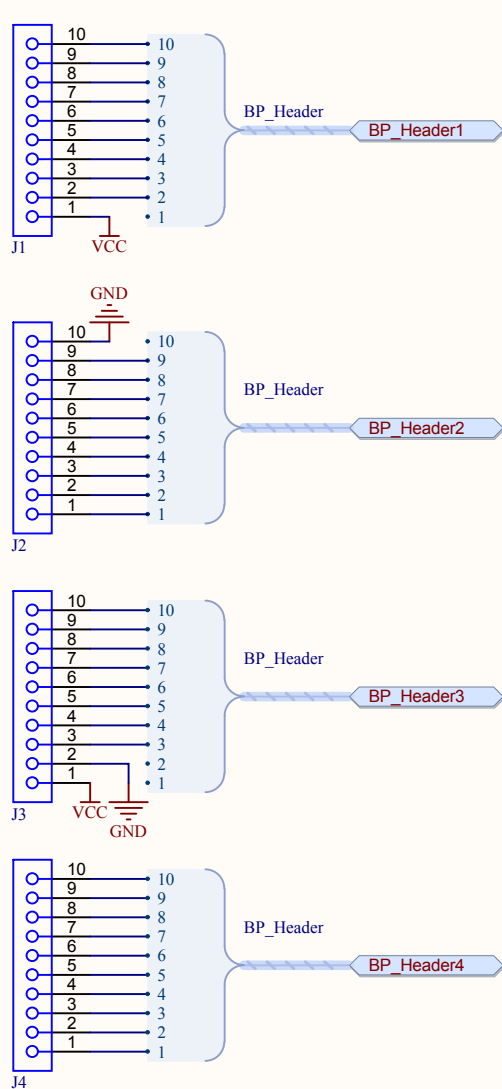
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Date: 17.05.2016 12:30:34

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Eidgenössische Technische Hochschule Zürich
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Drawing title:

MSP432 Demoboard Connection Headers

Drawing number: *

Rev: *

Format:

Laboratory: TIK

Project: Connectors.SchDoc

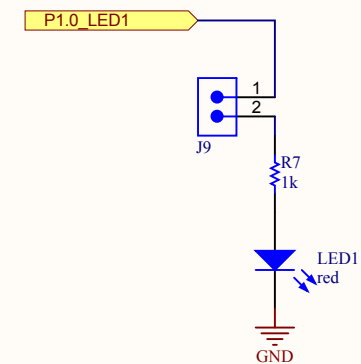
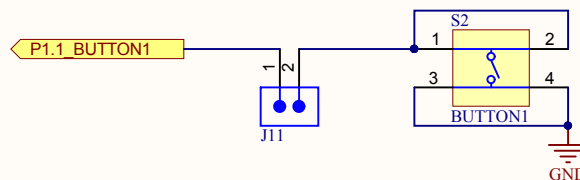
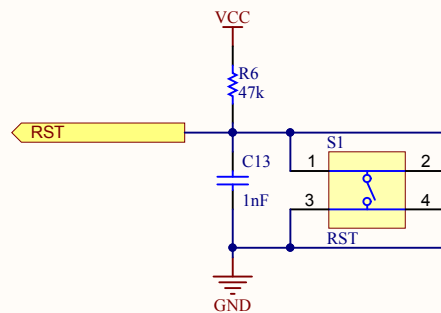
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M1
Mounting Hole

M2
Mounting Hole

M3
Mounting Hole

M4
Mounting Hole

ETH

Eidgenössische Technische Hochschule Zürich
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Drawing title:

MSP432 Demoboard User Interfaces

Drawing number: *

Rev: *

Format: Laboratory: TIK

Project: User_Interfaces.SchDoc

Date: 17.05.2016 12:30:34

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File: C:\Users\user\Dropbox\Masterthesis\PCB Design Course\cad13\User_Interfaces.SchDoc

