



छत्तीसगढ़ स्वामी विवेकानन्द तकनीकी विश्वविद्यालय, भिलाई CHHATTISGARH SWAMI VIVEKANAND TECHNICAL UNIVERSITY, BHILAI

STATEMENT OF MARKS & GRADES

AME OF EXAMINATION:

BE (COMP. SC. & ENGG.)

SEMESTER: THIRD

SESSION:

2008-2009

AME OF CANDIDATE:

ANSHU AGRAWAL

ROLL NO.: 3032207009

ENROLL NO. : AB9158

ATHER'S/HUSBAND'S NAME: RAMNIWAS AGRAWAL

STATUS:

REGULAR

IAME OF INSTITUTION:

CHHATRAPATI SHIVAJI INSTITUTE OF TECHNOLOGY - DURG

S. CODE	SUBJECT	EXAMINATION						TOTAL		LETTER	CDADE	CREDITS
		ESE		СТ		TA		MARKS		GRADE	POINT	EARNED
		MA	X OBT	MAX	OBT	MAX	ОВТ	MAX C	ВТ	III C	U III W II	
322311(14)	Mathematics-III	80	56	20	10	20	16	120	82	B+	8	5
328313(28)	Basic Electronics	80	60	20	07	20	17	120	84	B+	8	4
328314(28)	Network Analysis And Synthesis	80	47	20	05	20	16	120	68	В	7	4
322315(22)	Problem Solving & Logic Building Using C	80	45	20	17	20	18	120	80	B+	8	4
322312(22)	Computer Fundamentals	80	37	20	18	20	17	120	72	В	7	4
333312(33)	Digital Electronics & Logic Design	80	60	20	04	20	19	120	83	B+	8	4
328322(28)	Basic Electronics (Lab)	40	35			20	17	60	52	A	9	2
322322(22)	Programming with C (Lab)	40	38			20	18	60	56	A+	10	2
333323(33)	Digital Electronics & Logic Design (Lab)	40	34			20	19	60	53	A	9	2
322324(22)	Web Technology (HTML/DHTML) (Lab)	40	36			20	17	60	53	A	9	2
300325(46)	Value Education					40	37	40	37	A +	10	1
	322311(14) 328313(28) 328314(28) 322315(22) 322312(22) 333312(33) 328322(28) 322322(22) 333323(33) 322324(22)	322311(14) Mathematics-III 328313(28) Basic Electronics 328314(28) Network Analysis And Synthesis 322315(22) Problem Solving & Logic Building Using C 322312(22) Computer Fundamentals 333312(33) Digital Electronics & Logic Design 328322(28) Basic Electronics (Lab) 322322(22) Programming with C (Lab) 333323(33) Digital Electronics & Logic Design (Lab)	322311(14) Mathematics-III 80 328313(28) Basic Electronics 80 328314(28) Network Analysis And Synthesis 80 322315(22) Problem Solving & Logic Building Using C 322312(22) Computer Fundamentals 80 333312(33) Digital Electronics & Logic Design 80 328322(28) Basic Electronics (Lab) 40 322322(22) Programming with C (Lab) 40 333323(33) Digital Electronics & Logic Design 40 (Lab) 322324(22) Web Technology (HTML/DHTML) (Lab) 40	322311(14) Mathematics-III 80 56 328313(28) Basic Electronics 80 60 328314(28) Network Analysis And Synthesis 80 47 322315(22) Problem Solving & Logic Building 80 45 Using C 322312(22) Computer Fundamentals 80 37 333312(33) Digital Electronics & Logic Design 80 60 328322(28) Basic Electronics (Lab) 40 35 322322(22) Programming with C (Lab) 40 38 333323(33) Digital Electronics & Logic Design 40 34 (Lab) 322324(22) Web Technology (HTML/DHTML) (Lab) 40 36	SUBJECT ESE COMMAX OBT MAX	SUBJECT ESE CT MAX OBT MAX	SUBJECT ESE CT TO	SUBJECT ESE CT TA MAX OBT MAX OBT	SUBJECT ESE CT TA MARK	SUBJECT ESE CT TA MARKS MAX OBT MAX OBT	SUBJECT ESE CT TA MARKS LETTER	SUBJECT SUBJECT SUBJECT SUBJECT SESE CT TA MARKS MAX OBT MAX OBT

Total Marks In Words: Seven Hundred Twenty Only

Total Credits Earned: 34

Semester Total Of Marks Obtained Maximum

720 1000

CUMULATIVE TOTAL OF MARKS

Obtained Division Maximum **FIRST** 2069 3000

Checked By:

In Figure:

720

8.11 SPI=

CPI = 7.81

RESULT: PASS

Abbreviation

Indicates the number of extra attempts after the 1st regular one Indicates Marks/Result after Retotalling @ / RT Indicates Marks/Result after Revaluation # / RV Indicates Marks/Result after Re-revaluation \$ / RRV Indicates grace marks in the Subject(s) Indicates 'Absent' in the Subject(s) ABS Indicates failure in the Subject(s) Total ESE **End Semester Exam** Class Tests TΑ Teacher's Assessment-Semester Performance Index Cumulative Performance Index

Issued through the Principal (Seal of the Institution)

stitute

Grace Marks

Subject Code Subject Code

Date: 25/04/2009

For details of Grading System & other rules see overleaf

1/c Dy. Registrar