



Anshuman Rath

Mobile No: +91 6371000130

Email: anshumanrath2026@gmail.com

Link: www.linkedin.com/in/anshuman-rath-551549251

Technical Skills: Embedded C | Python | MATLAB | Simulink | Quartus Prime | Modelsim (FPGA) | Arduino IDE | Proteus | Circuit Design I

Certification: Digital Electronics Circuits (NPTEL), VLSI Design Methods & Embedded Systems (NIELIT/Maven Silicon), Cyber-Security (IBM), C & Python Language (CTTC), Applied Machine Learning in Python (COURSERA)

EDUCATION			
Board	Tenure	Educational Institutes	CGPA/Percentage
B. Tech: - ECE AI & Cybernetics)	Oct 2022 –Ongoing	VIT Bhopal University	8.63/10
Class XII	July 2022	DAV PUBLIC School CSPUR	90.4%
Class X	July 2020	DAV PUBLIC School CSPUR	91.8%

ACADEMIC PROJECTS

Accident Prevention System Aug.2023 -Oct.2023	Objective: This project was to deter accidents that occur owing to drowsiness & naps by a fatigued driver. The main objective of this project is to prevent the accident which happens due to Drowsiness of driver, sudden asleep of driver due to continuous fatigue and strain. Technology Used: Arduino IDE, Proteus, IR sensor, Arduino UNO Microcontroller Team: Three Members; Role: Designing hardware and simulation
Accident Detection System Feb.2024- May 2024	Objective: This Project was to undertake an in-depth study at Accident Detection System with integration of various sensors, modules and algorithms to identify potential incidents and trigger appropriate actions such as alerting emergency services or deploying safety measures. Technology Used: Arduino IDE, Proteus, IR Sensors, GPS and GSM Modules, Arduino-UNO & IoT Team: Three members; Role: Designing & coding the controller

INTERNSHIP

Web Design at IIT Jammu (Virtual) June 2023 - July 2023	<ul style="list-style-type: none">Trained in creating proficiency as a Web Developer using Java Script and CSS with a strong foundation in dynamic and visually appealing websites.Skilled with translating design concepts having responsive & user-friendly interfaces.
SURE INTERN at IIT -Hyderabad (Physical) June 2024	<ul style="list-style-type: none">Worked as a project Intern (One Month Paid) under Prof. Siva Rama Krishna Vanjari on Signal analyzation and processing of audio signals.Takeaway: The project on Signal Analyzation and Extraction of Clear Audio focuses on developing a comprehensive process to enhance audio signals by removing noise and interference.
RTL Design & Verification at VLSIPRO TECH. (Physical) Nov.25- Jan. 25	<ul style="list-style-type: none">Worked as a Paid Intern (Two Months) at VLSIPRO TECHNOLOGIES, Bhubaneswar in the field of Verification and Design of SemiconductorsTakeaway: The project focused on designing an 8b/10b Encoder/Decoder, where I explored the logic behind code mapping and disparity calculation. Understanding on how data is encoded/decoded to maintain DC balance by ensuring reliable transmission was the key finding of the project.
Embedded System EMERTXE (Virtual) May. – June.25	<ul style="list-style-type: none">Got selected for a 4-week Embedded Systems Online Internship offered by Emertxe Information Technologies. It augmented my hands-on skills in embedded C, microcontrollers, and real-time systems.Skilled with Embedded C proگرامing Language and Simulations of different embedded systems (Washing Machine and Microwave) using PICsimLab.

EXTRA-CURRICULARS AND RESPONSIBILITEIS

Responsibilities	Content Team Member- IoT Club VIT Bhopal; Student Member of IEEE
Extracurricular	Handled the IOT Unleashed event as a Content Team Member, VIT Bhopal (Advitya-2023)

ADDITIONAL INFORMATION

Hobbies	Playing Musical Instruments:Guitar; Playing Cricket, Badminton and Carrom; Listening Music and Reading Books
Languages	English: Full Professional; Hindi: Full Professional