

# A Machine-Checked C Implementation of Dijkstra’s Shortest Path Algorithm

## (Short Paper)

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**Abstract.** We report on a machine-checked Coq proof of correctness for Dijkstra’s one-to-all shortest path algorithm. We prove full functional correctness. We use classic textbook code written in CompCert C, and since our code is executable and realistic our verification must deal with real-world complications. In particular, we encounter and explain an overflow issue in Dijkstra’s algorithm. The precise bound in the relevant precondition is nontrivial: we show that the intuitive guess fails and provide a workable refinement. This work fits into an ongoing exploration of verified graph-manipulating algorithms in a realistic setting.

**Keywords:** Dijkstra’s shortest-path · verification · CompCert · VST

## 1 Introduction

Dijkstra’s eponymous shortest-path algorithm [1] finds the cost-minimal paths from a distinguished *source* vertex *source* to all reachable vertices in a finite directed graph. The algorithm is classic and ubiquitous, appearing widely in textbooks [2] and in real routing protocols. Further, the algorithm has been in use for over 60 years, suggesting that for all practical purposes its safety and correctness have been verified by decades of application.

Here we verify a C implementation of Dijkstra’s one-to-all shortest path algorithm. We implement textbook C code [2] in CompCert C [3] so that we can use the Verified Software Toolchain [4] and Wang *et al.*’s recent framework for verifying graph algorithms [5] to state and prove the full functional correctness of the code using the Coq proof assistant [6]. We expose a subtle overflow issue in the C code and make a nontrivial refinement to the precondition of the algorithm so that users know when the code will calculate the correct paths.

The remainder of this paper is organized as follows:

- §2 We briefly present and explain our C implementation of Dijkstra’s algorithm and the key loop invariants we use to certify its specification.
- §3 We explain how overflow can occur and give a refinement to the precondition of the function that restricts edge weights and the standard `INF` definition appropriately in a way that prevents said overflow.
- §4 We conclude by putting this result in the context of our previous and ongoing work. We discuss related work in certifying Dijkstra’s algorithm, and revisit standard algorithms textbook presentations.

```

1 #define INF INT_MAX - INT_MAX/SIZE
2 void dijkstra (int graph[SIZE][SIZE], int src,
3               int *dist, int *prev) {
4   // { DijkGraph( $\gamma$ ) * array(dist, -) * array(prev, -) }
5   int pq[SIZE]; int i, j, u, cost;
6   for (i = 0; i < SIZE; i++)
7   { dist[i] = INF; prev[i] = INF; pq[i] = INF; }
8   dist[src] = 0; pq[src] = 0; prev[src] = src;
9   // {  $\exists priq, dist, prev. \text{DijkGraph}(\gamma) * \text{PQ}(pq, priq) * \text{array}(dist, dist) *$ 
10  //    $\text{array}(prev, prev) \wedge \text{dijk\_correct}(\gamma, src, prev, dist, priq)$  }
11   while (!pq_emp(pq)) {
12     u = popMin(pq);
13     // {  $\exists prev', dist', priq'. \text{DijkGraph}(\gamma) * \text{PQ}(pq, priq') * \text{array}(dist, dist') *$ 
14     //    $\text{array}(prev, prev') \wedge u \in \text{popped}(priq') \wedge$ 
15     //    $\text{dijk\_correct\_weak}(\gamma, src, prev', dist', priq', i, u)$  }
16     for (i = 0; i < SIZE; i++) {
17       cost = graph[u][i];
18       if (cost < INF) {
19         if (dist[i] > dist[u] + cost)
20         { dist[i] = dist[u] + cost; prev[i] = u; pq[i] = dist[i]; }
21       } // {  $\exists prev', dist', priq'. \text{DijkGraph}(\gamma) * \text{PQ}(pq, priq') * \text{array}(dist, dist') *$ 
22       //    $\text{array}(prev, prev') \wedge \text{dijk\_correct\_weak}(\gamma, src, prev', dist', priq', i, u)$  }
23     } // {  $\exists prev', dist', priq'. \text{DijkGraph}(\gamma) * \text{PQ}(pq, priq') * \text{array}(dist, dist') *$ 
24     //    $\text{array}(prev, prev') \wedge \text{dijk\_correct}(\gamma, src, prev', dist', priq')$  }
25   } // {  $\exists priq, dist, prev. \text{DijkGraph}(\gamma) * \text{PQ}(pq, priq) * \text{array}(dist, dist) *$ 
26   //    $\text{array}(prev, prev) \wedge \forall dst \in priq. priq[dst] \not\leq INF \wedge$ 
27   //    $\text{dijk\_correct}(\gamma, src, prev, dist, priq)$  }
28   return; }

```

Fig. 1: Clight code and proof sketch for Dijkstra's Algorithm

## 2 Verified Dijkstra in C

Figure 1 shows the code and proof sketch of Dijkstra's algorithm. Our code is implemented exactly as suggested by CLRS [2] so we refer readers there for a general discussion of the algorithm. The adjacency-matrix-represented graph  $\gamma$  of SIZE vertices is passed as the parameter graph along with the source vertex src and two allocated arrays dist and prev. The details of the spatial predicates  $\text{array}(x, v)$ , connecting an array pointer  $x$  with its contents  $v$ , and the internals of the priority queue PQ are unexciting. Our spatial graph predicate is more interesting, in that it nests arrays and connects the concrete memory values to an abstract mathematical graph  $\gamma$ , which in turn exposes an interface in the language of graph theory (vertices, edges, labels, etc.).

$$\text{list\_addr}((\text{block}, \text{offset}), \text{index}) \triangleq (\text{block}, \text{offset} + (\text{index} \times \text{sizeof}(\text{int}) \times \text{SIZE}))$$

$$\text{list\_rep}(\gamma, i, \text{base\_ptr}) \triangleq \text{array}(\text{graph2matrix}(\gamma)[i], \text{list\_addr}(\text{base\_ptr}, i))$$

$$\text{graph\_rep}(\gamma, g\_addr) \triangleq \bigstar_{v \in \gamma} v \mapsto \text{list\_rep}(\gamma, v, g\_addr)$$

In general, these spatial representations are simple enough that they pose no special challenge in the proof. Therefore, in our discussion below, we will not focus on issues such as making sure an array dereference is in bounds.

The key to the verification is the pure part of the loop invariants on lines 9 and 12. The `while` invariant  $dijk\_correct(\gamma, src, prev, dist, priq)$  has three parts:

$$\begin{aligned} \forall dst. 0 \leq dst < SIZE \rightarrow & inv\_popped(\gamma, src, prev, dist, priq, dst) \wedge \\ & inv\_unpopped(\gamma, src, prev, dist, priq, dst) \wedge \\ & inv\_unseen(\gamma, prev, dist, priq, dst) \end{aligned}$$

A destination vertex  $dst$  falls into one of three categories:

1. *inv\_popped*:  $dst$  has been fully processed, and has been popped from the priority queue. A globally optimal path from  $src$  to  $dst$  exists, the cost of this path is logged in the  $dist$  array, and all the vertices visited by the path are also popped. Further, the links of this path are logged in the  $prev$  array.
2. *inv\_unpopped*:  $dst$  is reachable in one hop from a popped “mom” vertex. This route is locally optimal: we cannot improve the cost with a *different* popped vertex. The  $prev$  array logs  $mom$  as the best-known way to reach  $dst$ , and the  $dist$  array logs the path cost via  $mom$  as the best-known cost.
3. *inv\_unseen*: no path is currently known from  $src$  to  $dst$ .

After popping the lowest-cost vertex in line 11, we reach the invariant of the `for` loop  $dijk\_correct\_weak(\gamma, src, prev, dist, priq, i, u)$ :

$$\begin{aligned} \forall dst. 0 \leq dst < SIZE \rightarrow & inv\_popped(\gamma, src, prev, dist, priq, dst) \wedge \\ & inv\_unseen(\gamma, prev, dist, priq, dst) \wedge \\ \forall dst. 0 \leq dst < i \rightarrow & inv\_unpopped(\gamma, src, prev, dist, priq, dst) \wedge \\ \forall dst. i \leq dst < SIZE \rightarrow & inv\_unpopped\_weak(\gamma, src, prev, dist, priq, dst, u) \end{aligned}$$

We now have four cases, many of which are familiar from *dijk\_correct*:

1. *inv\_popped*:  $dst$  has been fully processed, and has been popped from the priority queue. For all popped vertices *except for*  $u$ , this is trivial from *dijk\_correct*. For  $u$  itself, we reach the heart of Dijkstra's correctness: the cost to the fringe vertex with minimum cost cannot be further improved.
2. *inv\_unseen*: no path is known from  $src$  to  $dst$ . This is straightforward.
3. *inv\_unpopped* (less than  $i$ ):  $dst$  is reachable in one hop from a “mom” vertex, which is itself popped. Initially this is trivial since  $i = 0$ , and we restore it as  $i$  climbs by updating costs when they can be improved, as in line 17.
4. *inv\_unpopped\_weak* (between than  $i$  and  $SIZE$ ):  $dst$  is reachable in one hop from a popped “mom” vertex. However, the path to  $dst$  specifically *does not go via*  $u$ . This fact is key to restoring *inv\_unpopped* as  $i$  increments.

The formal definitions for the above predicates can be found in the Appendix.

At the end of the `for` loop, the fourth case will fall away ( $i = SIZE$ ), allowing us to infer the `while` invariant *dijk\_correct* from *dijk\_correct\_weak* and thus continue the `while` loop. The `while` loop will break precisely when no item in the priority queue has cost less than  $INF$ . The second clause of the `while` loop invariant *dijk\_correct* then falls away: all reachable vertices have been optimized, and the rest are unreachable altogether. We are done.

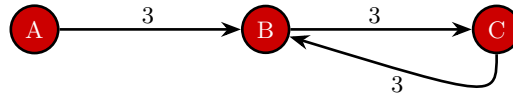


Fig. 2: A graph that will result in overflow on a 3-bit machine

### 3 I’m sorry Dave, I’m afraid I can’t do that: Overflow

A little thought makes it clear that Dijkstra’s algorithm cannot work when the cumulative path cost is greater than or equal to `INT_MAX`. A reasonable restriction would seem to be allowing edge costs to be no more than  $\left\lfloor \frac{\text{INT\_MAX}}{\text{SIZE}-1} \right\rfloor$ , since the longest path can have no more than `SIZE-1` links and so the maximum cumulative path cost will be no more than `INT_MAX`. However, this has two flaws. First, since we are writing real code in C, rather than pseudocode in an idealized setting, we must reserve some concrete `int` value for “infinity” `INF`, which has the special semantics that when a vertex  $x$ ’s distance is `INF` then  $x$  must be unreachable; if a reached destination vertex could have a legitimate path cost of `INF` then we would have an unpleasant ambiguity. Second, even though path costs start at `INF` and only decrease, the code can overflow in lines 16 and 17.

Consider applying Dijkstra’s algorithm on a hypothetical 3-bit unsigned machine to the graph in figure 2. The `SIZE` of the graph is 3 nodes, and so the naïve edge-weight upper bound is  $\left\lfloor \frac{\text{INT\_MAX}}{\text{SIZE}-1} \right\rfloor = \left\lfloor \frac{7}{3-1} \right\rfloor = 3$ , exactly as pictured in figure 2. Indeed, a glance at the diagram is enough to tell that the true distance from the source A to vertices B and C are 3 and 6, respectively—both of which are representable with 3 bits, and so naïvely all seems well. Indeed, after processing vertices A and B, 3 and 6 **are** the costs reflected in the `dist` array for B and C, respectively—but *unfortunately vertex C is still in the priority queue* `pq`. After vertex C is popped on line 11, we fetch its neighbors in the `for` loop; vertex B’s cost of 3 will be fetched on line 14. On line 16 the currently optimal cost of B (3) is compared against the sum of the optimal cost of C (6) plus the just-retrieved cost of the edge from C to B (3). Since  $6 + 3$  overflows in 3-bit arithmetic, the comparison is not between 3 and 9 but in fact between 3 and 1! Thus the code decides that a new cheaper path from A to B exists (in particular,  $A \rightsquigarrow B \rightsquigarrow C \rightsquigarrow B$ ) and then trashes the `dist` and `prev` arrays on line 17.

Our code uses signed `int` rather than unsigned `int` so we have undefined behavior rather than defined-but-wrong behavior, but the essence of the overflow is identical. Our solution is twofold. First, we restrict the maximum edge cost to  $\left\lfloor \frac{\text{INT\_MAX}}{\text{SIZE}} \right\rfloor$ , which in the 3-bit setting just described forces an edge cost of no more than 2. Second, on line 1 we choose `INF` to be `INT_MAX -  $\left\lfloor \frac{\text{INT\_MAX}}{\text{SIZE}} \right\rfloor$` , which in the 3-bit setting is 5. Consider modifying figure 2 to have edge weights of 2 rather than 3. After processing vertices A and B, the distances to B and C are no more than 2 and 4, respectively. When we process vertex C, the comparison on line 16 is thus between the previous best cost to B of 2, and the candidate best cost to B via C of 6; there is no overflow and the code behaves as advertised.

## 4 Concluding thoughts: Future and Related Work

*Our previous work.* We have long been interested in the verification of graph-manipulating programs written in C [7]. We fortified our techniques to handle realistic (CompCert [3]) C to a machine-checked level of rigour [5]. Novel features of the present result include a previously-untried adjacency matrices spatial graph representation as well as non-trivial edge labels between graph nodes.

*Ongoing and future work.* We are investigating techniques to increase the automation of such verifications. Although we benefit from some automation at the Hoare-logic level provided by the Verified Software Toolchain [4], building these proofs is still highly labor intensive. We see potential for automation in four areas: (A) the Hoare level; (B) the spatial level; (C) the mathematical level; and (D) the interface between the spatial and the mathematical levels. Our ongoing work on these challenges include (A) improved tactics for VST for common cases we encounter in graph algorithms; (B) an expanded library of existing graph constructions such as the adjacency-matrix representation used in this result, as well as associated lemmas; (C) better lemmas about common mathematical graph patterns, investigations into reachability techniques based on regular expressions over matrices and related semirings [8,9,10,11]; and (D) improved modularity in our constructions and automation of common cases, *e.g.* we often compare C pointers to heap-represented graph nodes for equality, and due to the nature of our representations this equality check will be well-defined in C when the associated nodes are present in the mathematical graph. The key advantage of having end-to-end machine-checked examples such as the one we presented above is that they guide the automation efforts by providing precise goals that are known to be strong enough to verify real code.

*Other verifications of shortest-path.* Chen verified Dijkstra in Mizar [12], Gordan *et al.* formalized the reachability property in HOL [13], Moore and Zhang verified it in ACL2 [14], Mange and Kuhn verified it in Jahob [15], and Klasen verified it in KeY [16]. Liu *et al.* took an alternative SMT-based approach to verify a Java implementation of Dijkstra [17]. In general the above work operates within idealized formal environments and thus gloss over certain real-world issues, and so as best we can tell do not *e.g.* handle the overflow issue we identified.

*Shortest-path in algorithms textbooks.* We were not able to find a standard textbook that gives a robust, precise, and full description of the overflow issue we describe in §3. The landmark CLRS [2] does not address overflow, although—arguably—this is unnecessary in pseudocode. Sedgewick’s book on graph algorithms in C [18] likewise does not address overflow, and moreover gives C code that contains exactly the bug we identify. Skiena’s *Algorithm Design Manual* likewise glosses over the issue, both in pseudocode and C [19]. To its credit, Heineman *et al.*’s *Algorithms in a Nutshell* [20] mentions overflow as a possibility and proposes casting certain computations to long to avoid them in C. However, Heineman *et al.* do not give a nontrivial bound on the input edge weights, leaving users in the fog for exactly when the algorithm works.

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## A Formal definitions used in decorated proof

In this appendix we put more formal and complete versions of the definitions discussed in the paper proper. Figure 3 shows how we represent graphs in memory using adjacency matrices. Figure 4 shows the pure mathematical definitions that were discussed in §2, and exposes several of the internal definitions that make them work.

$$\begin{aligned}
 \text{list\_addr}((\text{block}, \text{offset}), \text{index}) &\triangleq (\text{block}, \text{offset} + (\text{index} \times \text{sizeof}(\text{int}) \times \text{SIZE})) \\
 \text{list\_rep}(\gamma, i, \text{base\_ptr}) &\triangleq \text{array}(\text{graph2matrix}(\gamma)[i], \text{list\_addr}(\text{base\_ptr}, i)) \\
 \text{graph\_rep}(\gamma, g\_addr) &\triangleq \star_{v \in \gamma} v \mapsto \text{list\_rep}(\gamma, v, g\_addr)
 \end{aligned}$$

Fig. 3: Spatial representation of the graph

$$\begin{aligned}
\text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{mom}, p) &\triangleq \text{valid\_path}(\gamma, p) \wedge \text{path\_ends}(\gamma, p, \text{src}, \text{dst}) \wedge \\
&\quad \text{path\_cost}(\gamma, p) \neq \text{INF} \wedge \text{dist}[\text{dst}] = \text{path\_cost}(\gamma, p) \wedge \forall a, b. (a, b) \in p \rightarrow \text{prev}[b] = a \\
\text{path\_globally\_optimal}(\gamma, \text{src}, \text{dst}, p) &\triangleq \forall p'. \text{valid\_path}(\gamma, p') \rightarrow \text{path\_ends}(\gamma, p', \text{src}, \text{dst}) \rightarrow \\
&\quad \text{path\_cost}(\gamma, p) \leq \text{path\_cost}(\gamma, p') \\
\text{all\_hops\_in\_popped}(p, \text{priq}) &\triangleq \forall a, b. (a, b) \in p \rightarrow a \in \text{popped}(\text{priq}) \wedge b \in \text{popped}(\text{priq}) \\
\text{all\_hops\_in\_popped\_weak}(p, \text{priq}, u) &\triangleq \forall a, b. (a, b) \in p \rightarrow a \in \text{popped}(\text{priq}) \wedge \\
&\quad b \in \text{popped}(\text{priq}) \wedge a \neq u \wedge b \neq u \\
\text{dijk\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}) &\triangleq \\
&\quad \forall \text{dst}. 0 \leq \text{dst} < \text{SIZE} \rightarrow \text{inv\_popped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \wedge \\
&\quad \text{inv\_unpopped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \wedge \\
&\quad \text{inv\_unseen}(\gamma, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \\
\text{dijk\_correct\_weak}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, i, u) &\triangleq \\
&\quad \forall \text{dst}. 0 \leq \text{dst} < \text{SIZE} \rightarrow \text{inv\_popped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \wedge \\
&\quad \text{inv\_unseen}(\gamma, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \wedge \\
&\quad \forall \text{dst}. 0 \leq \text{dst} < i \rightarrow \text{inv\_unpopped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) \wedge \\
&\quad \forall \text{dst}. i \leq \text{dst} < \text{SIZE} \rightarrow \text{inv\_unpopped\_weak}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}, u) \\
\text{inv\_popped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) &\triangleq \text{dst} \in \text{popped}(\text{priq}) \rightarrow \\
&\quad \exists p2\text{dst}. \text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{dst}, p2\text{dst}) \wedge \\
&\quad \text{all\_hops\_in\_popped}(p2\text{dst}, \text{priq}) \wedge \text{path\_globally\_optimal}(\gamma, \text{src}, \text{dst}, p2\text{dst}) \\
\text{inv\_unpopped}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) &\triangleq \text{priq}[\text{dst}] < \text{INF} \rightarrow \\
&\quad \text{let } \text{mom} := \text{prev}[\text{dst}] \text{ in } \exists p2\text{mom}. \text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{mom}, p2\text{mom}) \wedge \\
&\quad \text{all\_hops\_in\_popped}(p2\text{mom}, \text{priq}) \wedge \text{path\_globally\_optimal}(\gamma, \text{src}, \text{mom}, p2\text{mom}) \wedge \\
&\quad \forall \text{mom}', p2\text{mom}'. \text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{mom}', p2\text{mom}') \rightarrow \\
&\quad \text{all\_hops\_in\_popped}(p2\text{mom}', \text{priq}) \rightarrow \text{path\_globally\_optimal}(\gamma, \text{src}, \text{mom}', p2\text{mom}') \rightarrow \\
&\quad \text{path\_cost}(p2\text{mom} + (\text{mom}, \text{dst})) \leq \text{path\_cost}(p2\text{mom}' + (\text{mom}', \text{dst})) \\
\text{inv\_unpopped\_weak}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{priq}, \text{dst}) &\triangleq \text{priq}[\text{dst}] < \text{INF} \rightarrow \\
&\quad \text{let } \text{mom} := \text{prev}[\text{dst}] \text{ in } \exists p2\text{mom}. \text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{mom}, p2\text{mom}) \wedge \\
&\quad \text{all\_hops\_in\_popped\_weak}(p2\text{mom}, \text{priq}) \wedge \text{path\_globally\_optimal}(\gamma, \text{src}, \text{mom}, p2\text{mom}) \wedge \\
&\quad \forall \text{mom}', p2\text{mom}'. \text{path\_correct}(\gamma, \text{src}, \text{prev}, \text{dist}, \text{mom}', p2\text{mom}') \rightarrow \\
&\quad \text{all\_hops\_in\_popped\_weak}(p2\text{mom}', \text{priq}) \rightarrow \text{path\_globally\_optimal}(\gamma, \text{src}, \text{mom}', p2\text{mom}') \rightarrow \\
&\quad \text{path\_cost}(p2\text{mom} + (\text{mom}, \text{dst})) \leq \text{path\_cost}(p2\text{mom}' + (\text{mom}', \text{dst})) \\
\text{inv\_unseen}(\gamma, \text{prev}, \text{dist}, \text{priq}, \text{dst}) &\triangleq \text{priq}[\text{dst}] = \text{INF} \rightarrow (\text{dist}[\text{dst}] = \text{INF} \wedge \text{prev}[\text{dst}] = \text{INF})
\end{aligned}$$

Fig. 4: Key mathematical definitions used in the decorated proof