



Figure 10 is a schematic diagram of the J11 connector. It shows a 22-pin connector with various signals and power connections. The signals are as follows:

- Pin 1: CAM1_DN0
- Pin 2: CAM1_DP0
- Pin 3: CAM1_DN1
- Pin 4: CAM1_DP1
- Pin 5: CAM1_CN
- Pin 6: CAM1_CP
- Pin 7: CAM1_DN2
- Pin 8: CAM1_DP2
- Pin 9: CAM1_DN3
- Pin 10: CAM1_DP3
- Pin 11: CAM1_I00
- Pin 12: CAM1_I01
- Pin 13: CAM1_DN0
- Pin 14: CAM1_DP0
- Pin 15: CAM1_DN1
- Pin 16: CAM1_DP1
- Pin 17: CAM1_CN
- Pin 18: CAM1_CP
- Pin 19: CAM1_DN2
- Pin 20: CAM1_DP2
- Pin 21: CAM1_DN3
- Pin 22: CAM1_DP3

The diagram also shows resistors R16 (4.7K) and R17 (4.7K) connected to the CM_3V3 line. The CM_3V3 line is connected to pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, and 22. The SCL5 and SDA6 lines are connected to pins 1 and 2 respectively. The CAM1_I00 and CAM1_I01 lines are connected to pins 11 and 12 respectively. The CAM1_DN0, CAM1_DP0, CAM1_DN1, CAM1_DP1, CAM1_CN, CAM1_CP, CAM1_DN2, CAM1_DP2, CAM1_DN3, CAM1_DP3, CAM1_I00, and CAM1_I01 lines are connected to pins 1 through 12 respectively. The CAM1_DN0, CAM1_DP0, CAM1_DN1, CAM1_DP1, CAM1_CN, CAM1_CP, CAM1_DN2, CAM1_DP2, CAM1_DN3, CAM1_DP3, CAM1_I00, and CAM1_I01 lines are connected to pins 13 through 22 respectively. The CAM1_DN0, CAM1_DP0, CAM1_DN1, CAM1_DP1, CAM1_CN, CAM1_CP, CAM1_DN2, CAM1_DP2, CAM1_DN3, CAM1_DP3, CAM1_I00, and CAM1_I01 lines are connected to pins 1 through 12 respectively. The CAM1_DN0, CAM1_DP0, CAM1_DN1, CAM1_DP1, CAM1_CN, CAM1_CP, CAM1_DN2, CAM1_DP2, CAM1_DN3, CAM1_DP3, CAM1_I00, and CAM1_I01 lines are connected to pins 13 through 22 respectively.

Figure 10 shows the pin connections for the J26 connector. The connector has 22 pins. The connections are as follows:

- Pin 1: CAM0_DN0
- Pin 2: CAM0_DP0
- Pin 3: CAM0_DN1
- Pin 4: CAM0_DP1
- Pin 5: CAM0_CN
- Pin 6: CAM0_CP
- Pin 7: CAM0_CN
- Pin 8: CAM0_CP
- Pin 10: CM_3V3
- Pin 11: CM_3V3
- Pin 12: CM_3V3
- Pin 13: CM_3V3
- Pin 14: CM_3V3
- Pin 15: CM_3V3
- Pin 16: CM_3V3
- Pin 17: CAM0_I00
- Pin 18: CAM0_I01
- Pin 19: CAM0_I00
- Pin 20: CAM0_I01
- Pin 21: CAM0_I00
- Pin 22: CAM0_I01

The SCL5 and SDA5 signals are connected to pins 21 and 22 respectively. The CM_3V3 signal is connected to pins 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, and 21. The CAM0 signals are connected to pins 1 through 8 and 17 through 22.

The schematic diagram illustrates the 7P3S power supply circuit. It starts with a 3V3 input connected to a network of resistors (R3066, R3067, R88, R89, R90, R91, R93) and capacitors (C37, C74, C75, C76, C78, C28, C29, C25). The main power stage is a TPS61165DCV buck converter (U8) configured with an inductor L5 (10uH/20%) and a Schottky diode D10 (SS14-M361T). The output of the converter is filtered by capacitors C76 and C78 (4.7uF/50V) and connected to the VLED output. The circuit also includes test points TP30 and TP33, and a connection for VLED.

[illegible]

The schematic diagram illustrates the HDMI interface circuit for the J13 connector. The J13 connector (467651001) is connected to the HDMI IC (U4, DMG1012T) and the Micro HDMI connector. The J13 connector pins are numbered 1 through 19. The Micro HDMI connector pins are numbered 1 through 19. The HDMI IC (U4) is connected to the J13 connector via a 10K resistor (R3062) and a 10K resistor (R18). The HDMI IC is also connected to the Micro HDMI connector via a 10K resistor (R3058) and a 10K resistor (R3059). The J13 connector is labeled 'J13 467651001'.