

ARM

**SDR DRIVER (new sdr.ko)**

- Generates the obfuscation mask
- Passes the mask down to the openofdm\_tx Verilog module

OBFUSCATION MASK:  
<64 coefficients, each encoded in 2 bits>

**Generates obfuscated frames**

- LTF computed relying on *LTF\_GENERATOR*
- Obfuscation mask further applied to DATA symbols of each transmitted frame

**OPENOFDM\_TX**

DOT11\_tx

**LTF\_GENERATOR****LTF\_PHASED\_ROM****IFFT****Generates obfuscated LTF**

- Frequency LTF terms loaded from LTF\_PHASED\_ROM
- Obfuscation applied on LTF before IFFT

FPGA