
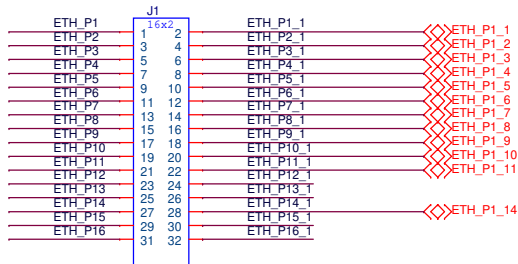
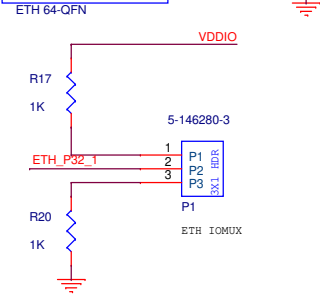


ETH IC Evaluation Board (Salinas)

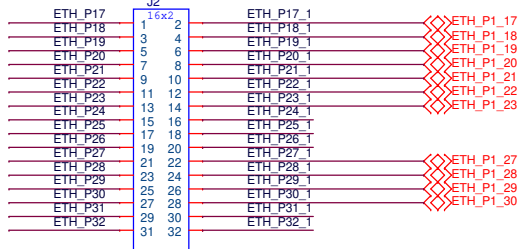
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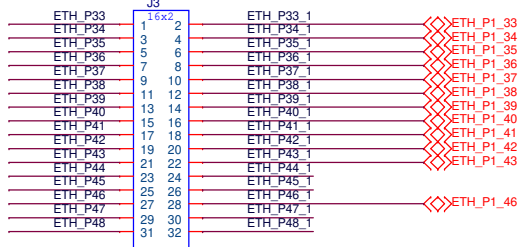
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PAD_SPIM_SDOIO	3	ETH_P3	
PAD_SPIM_SDOIO	4	ETH_P4	
PAD_SPIM_SDOIO	5	ETH_P5	
PAD_SPIM_SDOIO	6	ETH_P6	
PAD_SPIM_CSNO	7	ETH_P7	
PAD_SPIM_CSNO	8	ETH_P8	
PAD_SPIM_SCK	9	ETH_P9	
PAD_UART_RX	10	ETH_P10	
PAD_UART_TX	11	ETH_P11	
PAD_CAM_PCLK	12	ETH_P12	
VDD_FPGA0	13	ETH_P13	
VDDIO0	14	ETH_P14	
PAD_CAM_VSYNC	15	ETH_P15	
NB	16	ETH_P16	
PB	17	ETH_P17	
PAD_HYPER_CK	18	ETH_P18	
PAD_JTAG_TCK	19	ETH_P19	
PAD_JTAG_TDI	20	ETH_P20	
PAD_JTAG_TDO	21	ETH_P21	
PAD_JTAG_TMS	22	ETH_P22	
PAD_JTAG_TRST	23	ETH_P23	
PAD_FPGA_GPIO36	24	ETH_P24	
VDDIO	25	ETH_P25	
VDD_FPGA1	26	ETH_P26	
PAD_FPGA_GPIO37	27	ETH_P27	
PAD_FPGA_GPIO38	28	ETH_P28	
PAD_FPGA_GPIO39	29	ETH_P29	
PAD_FPGA_GPIO39	30	ETH_P30	
PAD_FPGA_GPIO40	31	ETH_P31	
PAD_STM	32	ETH_P32	
PAD_IOMUX	33	ETH_P33	
PAD_HYPER_DQ0	34	ETH_P34	
PAD_HYPER_DQ1	35	ETH_P35	
PAD_HYPER_DQ2	36	ETH_P36	
PAD_HYPER_DQ3	37	ETH_P37	
PAD_HYPER_DQ4	38	ETH_P38	
PAD_HYPER_DQ5	39	ETH_P39	
PAD_HYPER_DQ6	40	ETH_P40	
PAD_HYPER_DQ7	41	ETH_P41	
PAD_HYPER_CSNO	42	ETH_P42	
PAD_HYPER_CSNO	43	ETH_P43	
PAD_HYPER_CSNO	44	ETH_P44	
VDDIO2	45	ETH_P45	
VDD_FPGA2	46	ETH_P46	
PAD_HYPER_CKN	47	ETH_P47	
VFW_P	48	ETH_P48	
PAD_CAM_DATA0	49	ETH_P49	
PAD_CAM_DATA1	50	ETH_P50	
PAD_CAM_DATA2	51	ETH_P51	
PAD_CAM_DATA3	52	ETH_P52	
PAD_CAM_DATA4	53	ETH_P53	
PAD_CAM_DATA5	54	ETH_P54	
PAD_CAM_DATA6	55	ETH_P55	
PAD_CAM_DATA7	56	ETH_P56	
VDD1	57	ETH_P57	
VDDIO3	58	ETH_P58	
VDD_FPGA3	59	ETH_P59	
PAD_I2C0_SDA	60	ETH_P60	
PAD_I2C0_SCL	61	ETH_P61	
PAD_RESET_N	62	ETH_P62	
PAD_XTAL_IN	63	ETH_P63	
PAD_XTAL_IN	64	ETH_P64	
PAD_MLATCH	65		
GND_1	66		
GND_2	67		
GND_3	68		
GND_4			



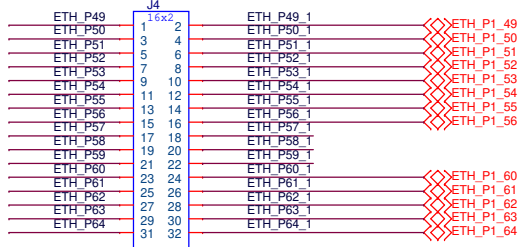
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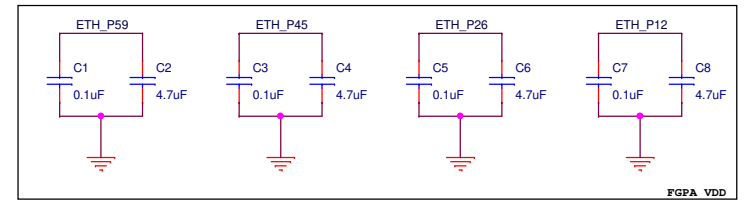
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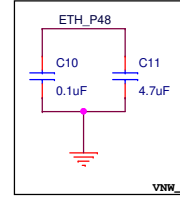
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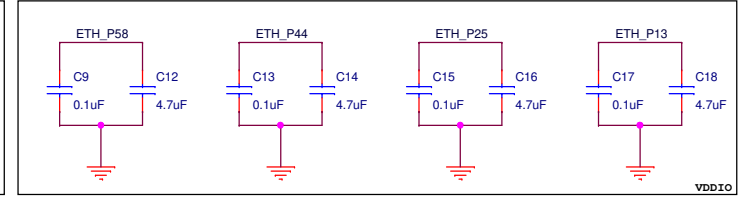
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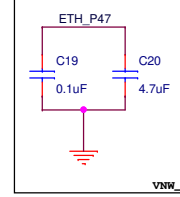
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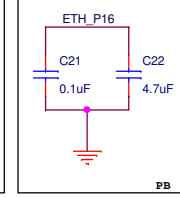
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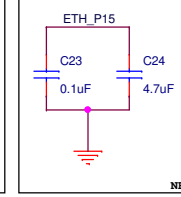
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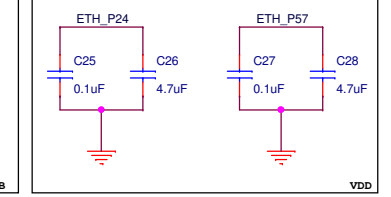
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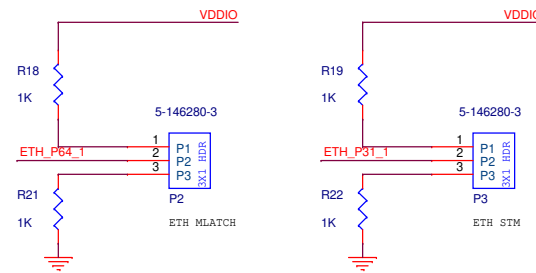
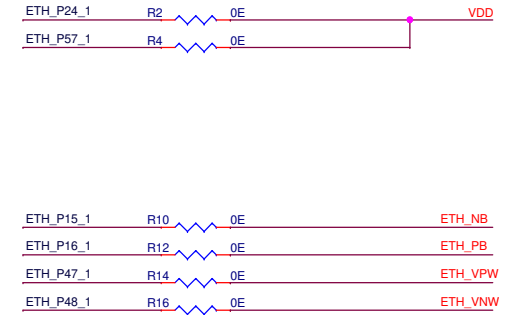
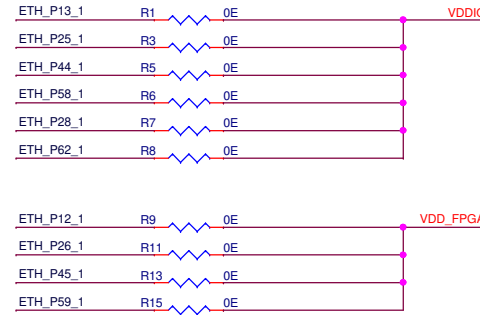
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NB



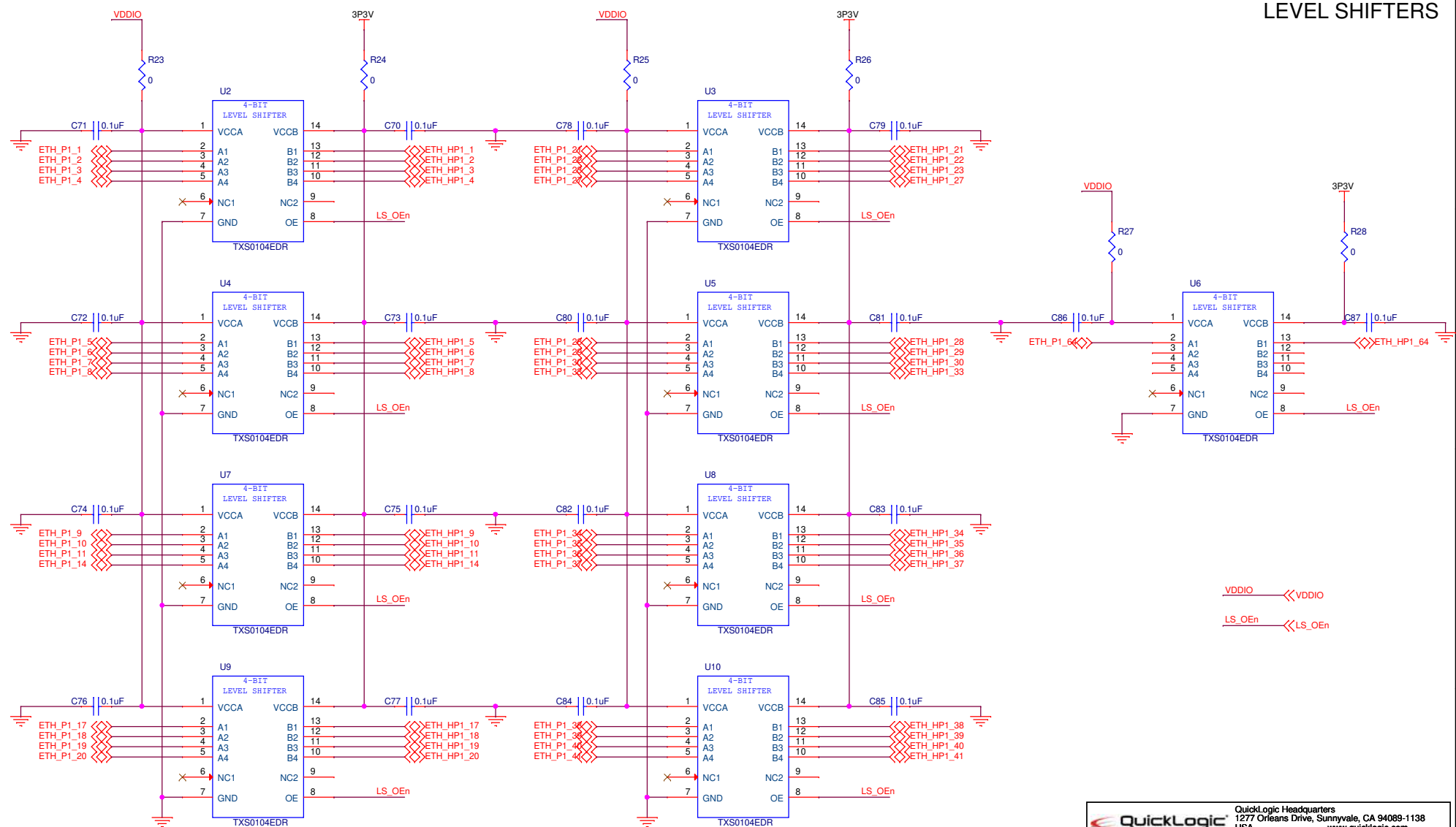
VDD



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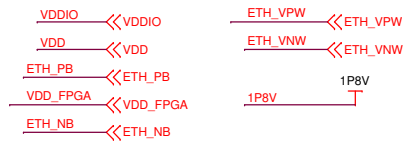
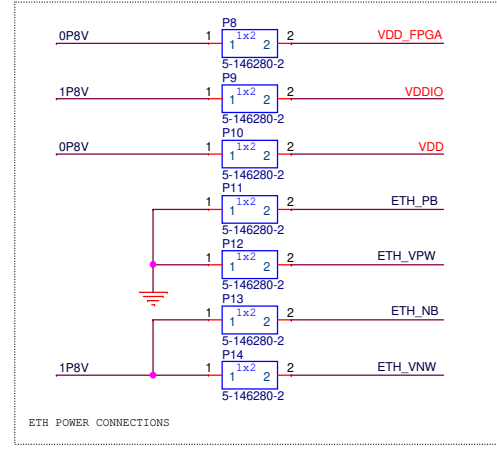
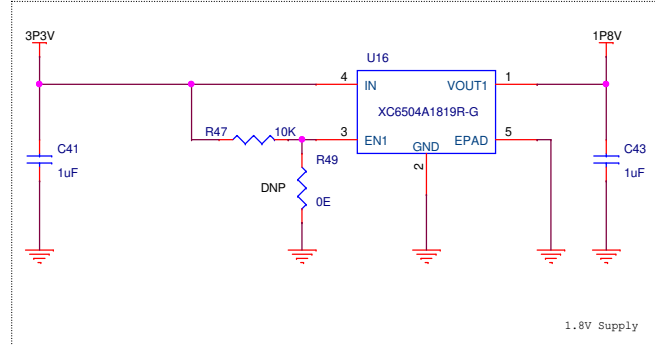
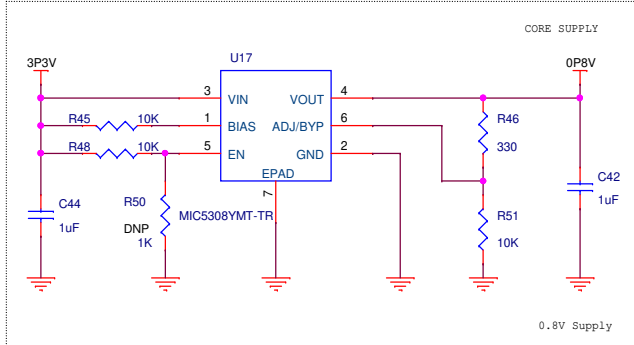
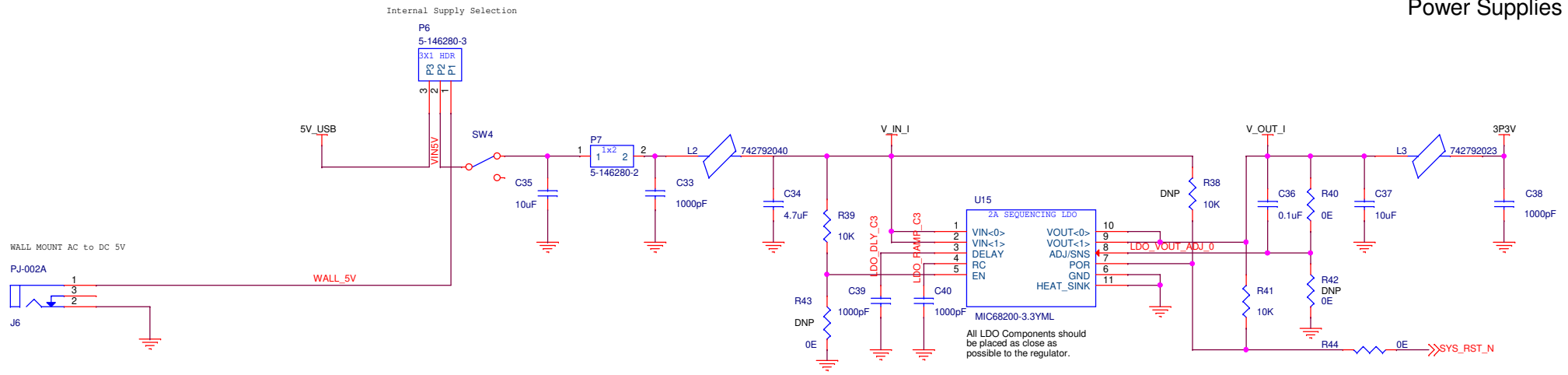
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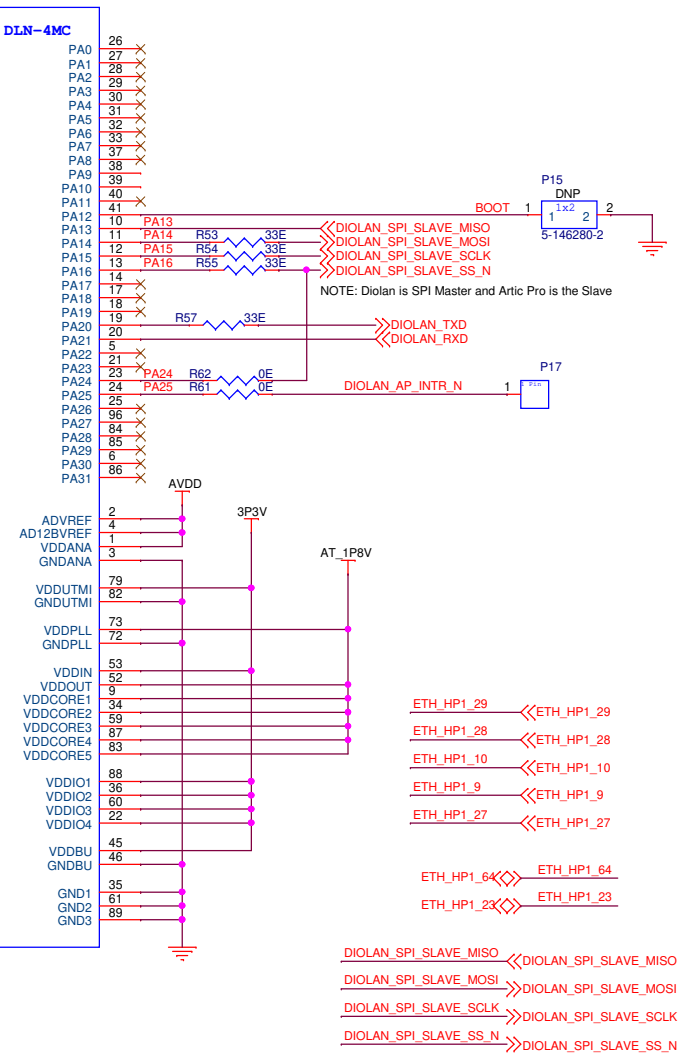
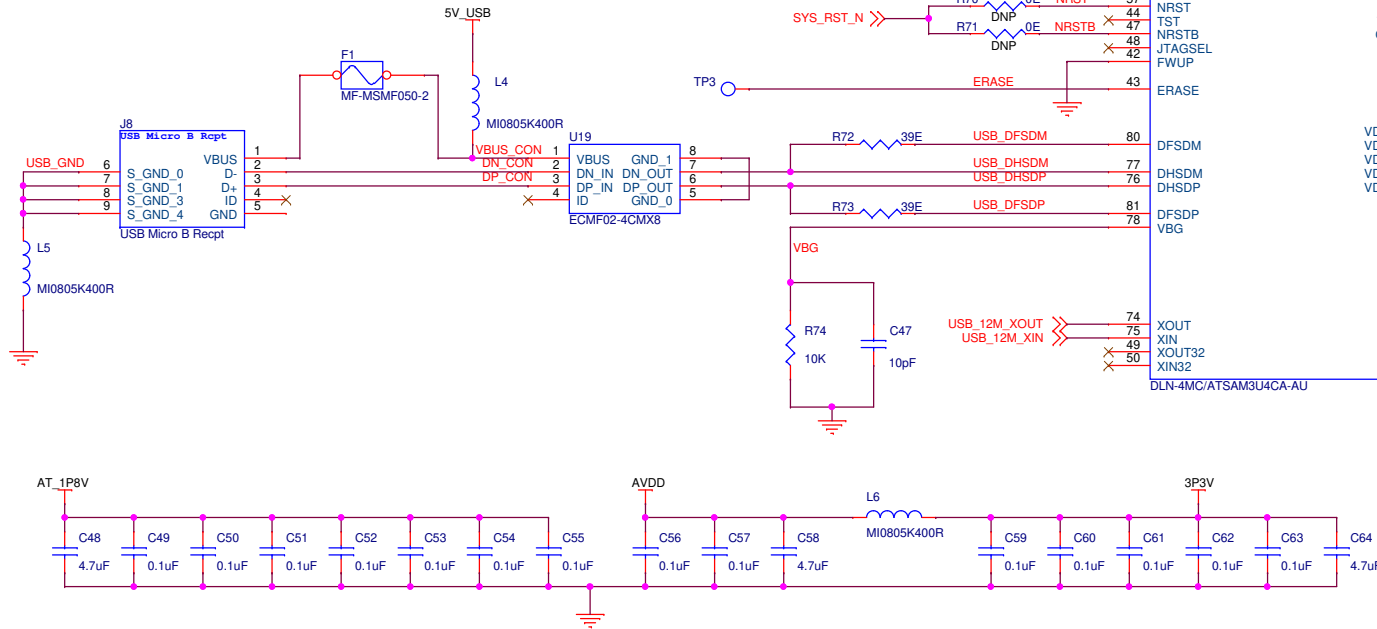
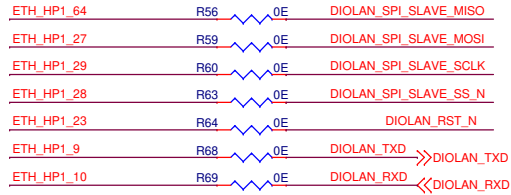
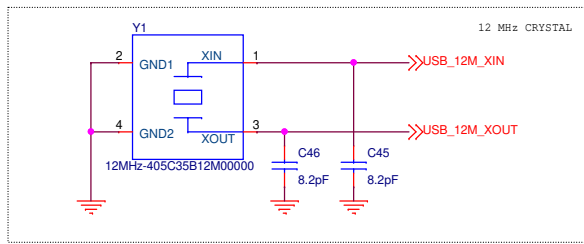
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Power Supplies



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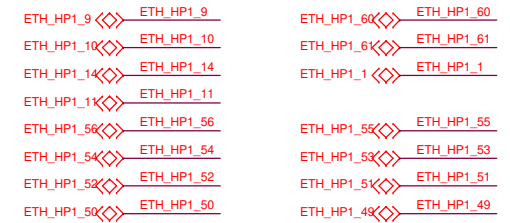
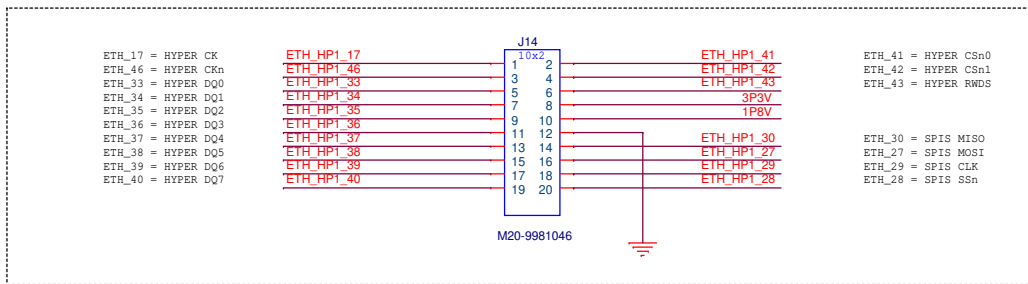
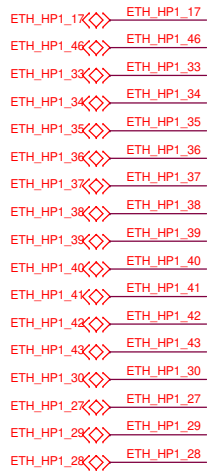
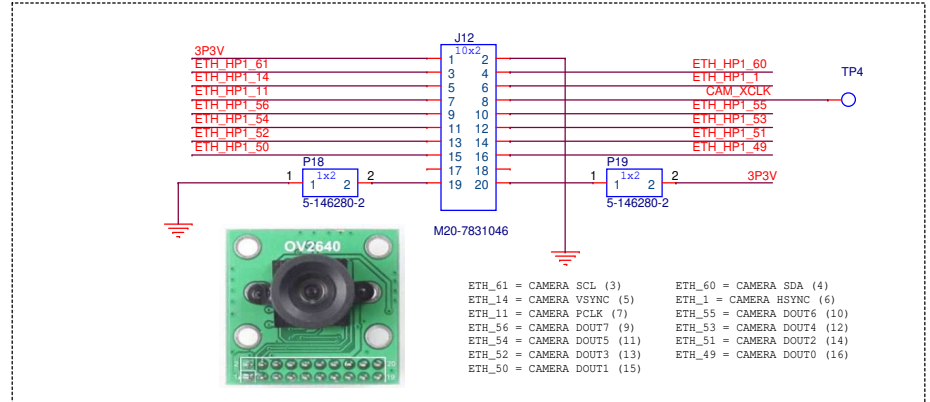
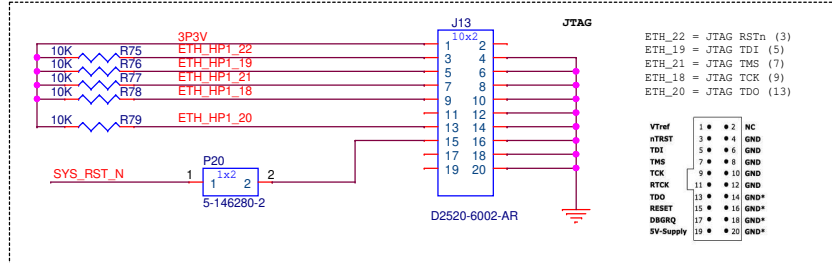
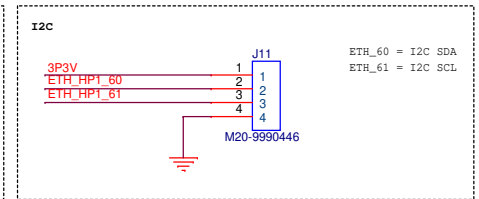
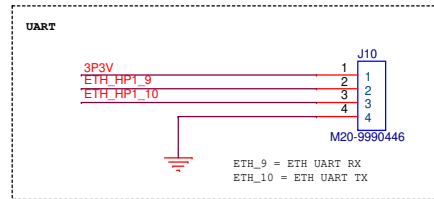
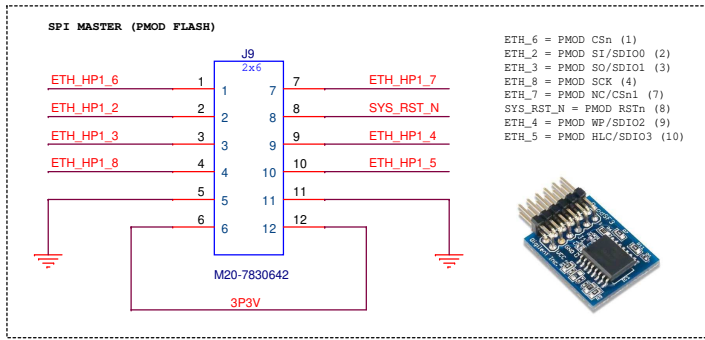
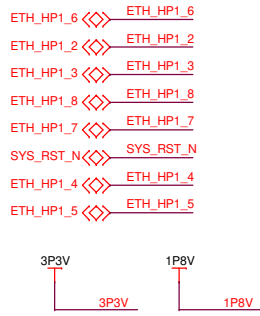


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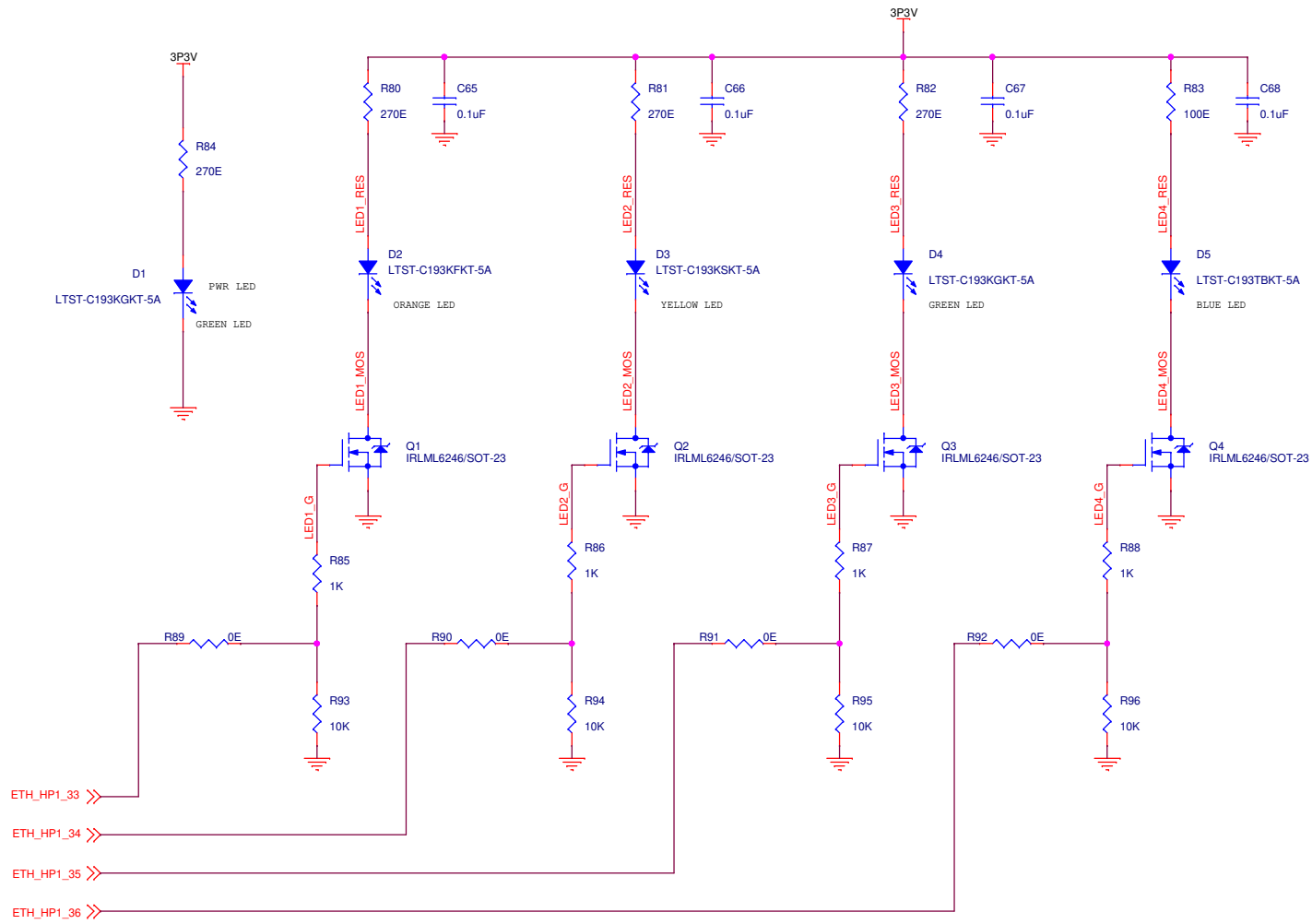
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USB to SPI Converter (DIOLAN)

EXPANSION HEADERS



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ETH_33 = FPGA_22
 ETH_34 = FPGA_23
 ETH_35 = FPGA_24
 ETH_36 = FPGA_25

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