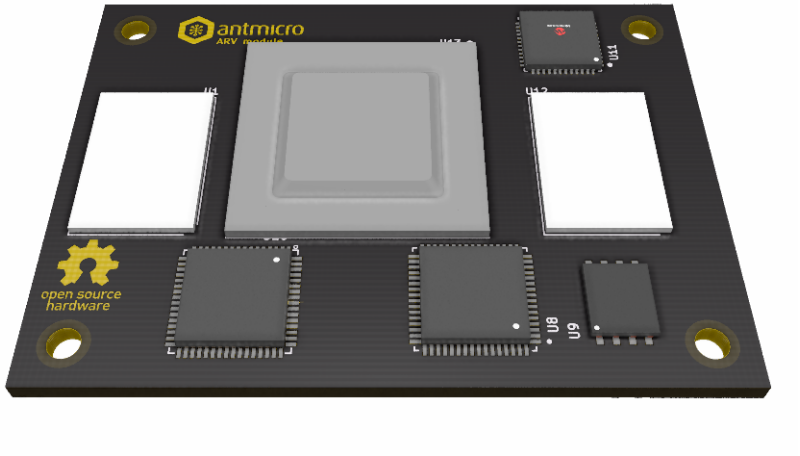

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File: interfaces.kicad_sch

Ethernet
File: ethernet.kicad_sch

LPDDR4
File: lpddr4.kicad_sch

B2B connectors
File: B2B-connectors.kicad_sch

VIC7100 power
File: VIC7100-power.kicad_sch

VIC7100 config
File: VIC7100-config.kicad_sch

VIC7100 interfaces
File: VIC7100-interfaces.kicad_sch

VIC7100 DDR
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Logo N1
antmicro_logo

Logo N2
oshw_logo

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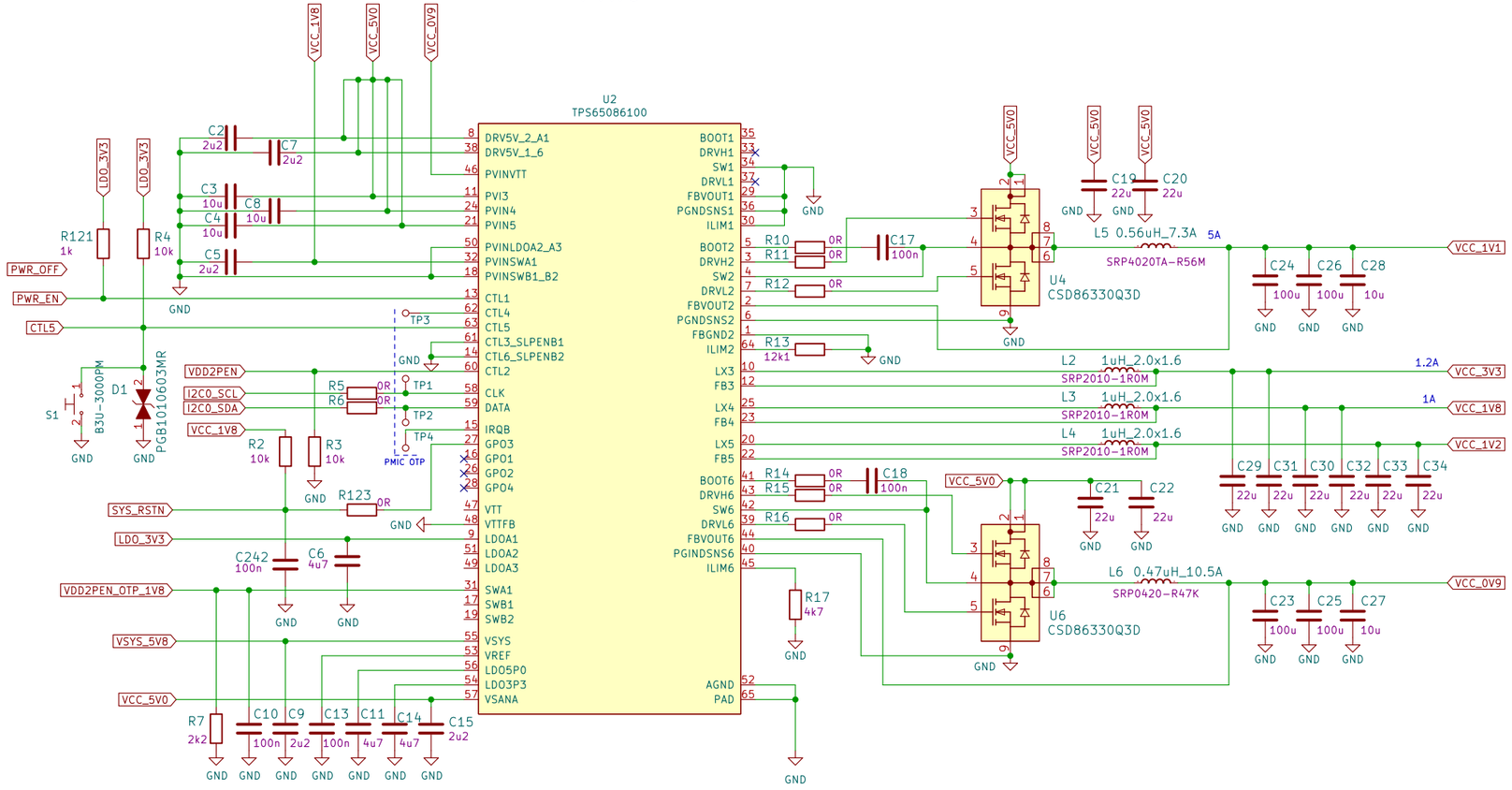
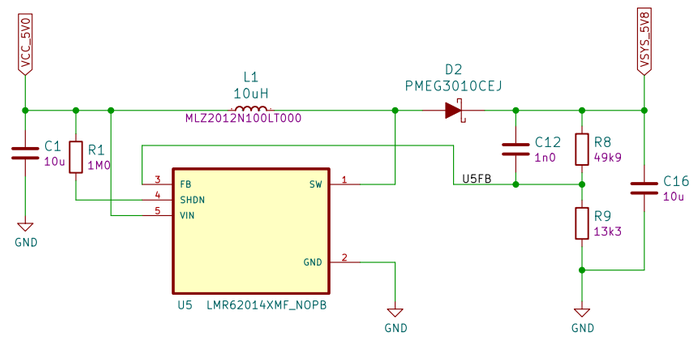


5V8 boost regulator



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ARV SoM – Power Mangement	
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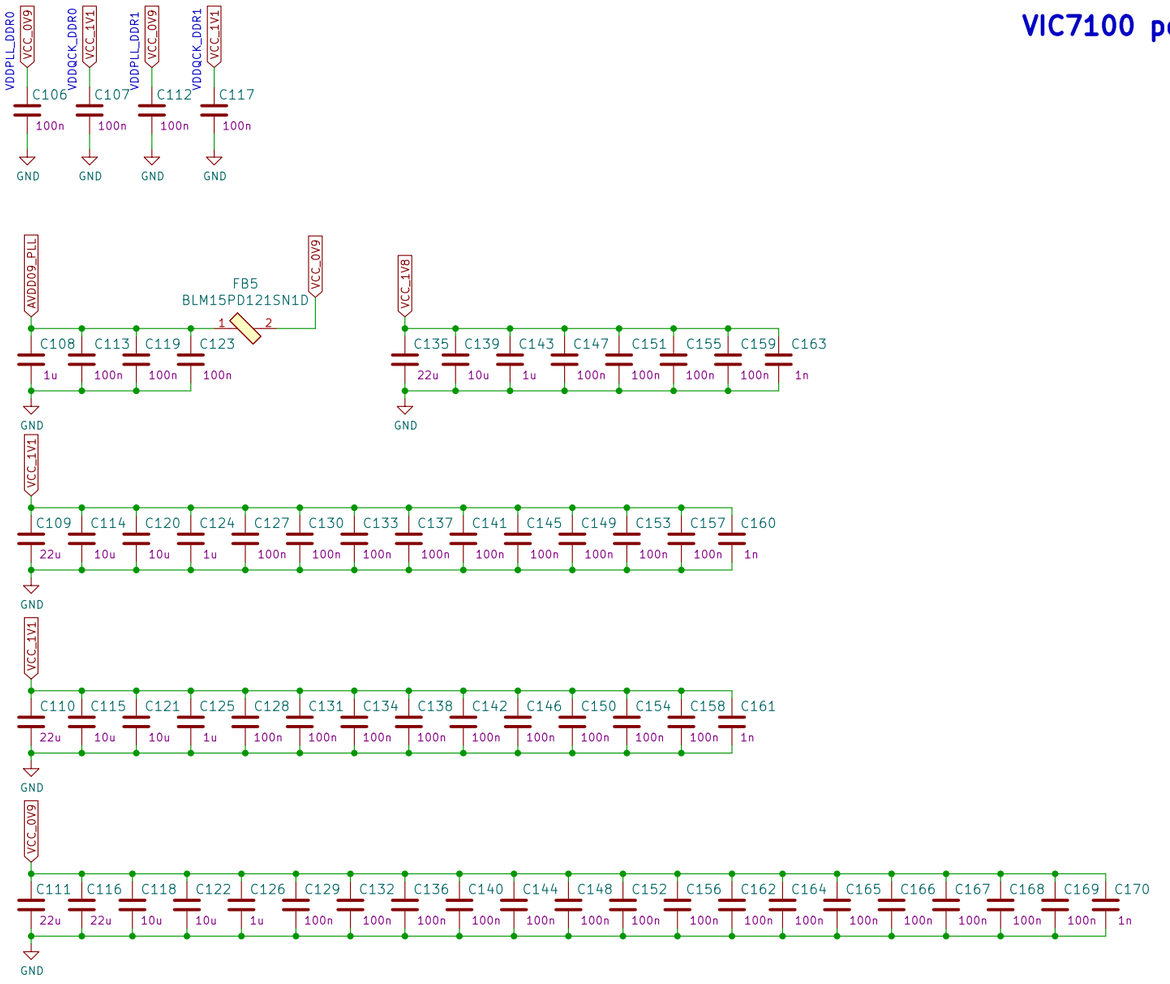
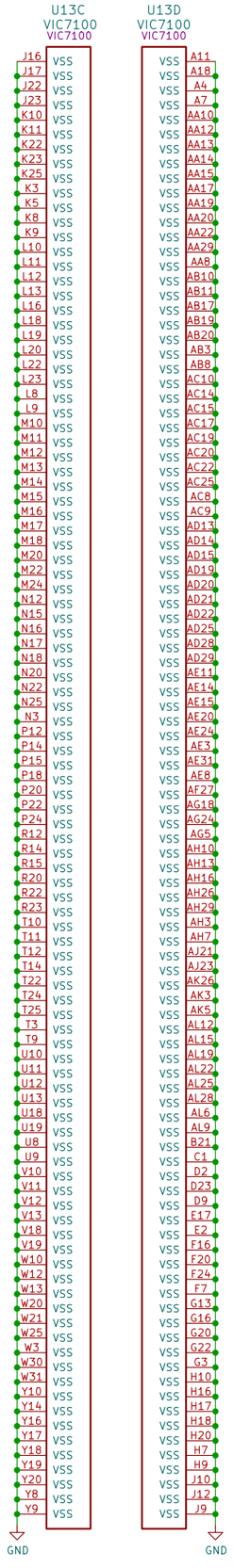
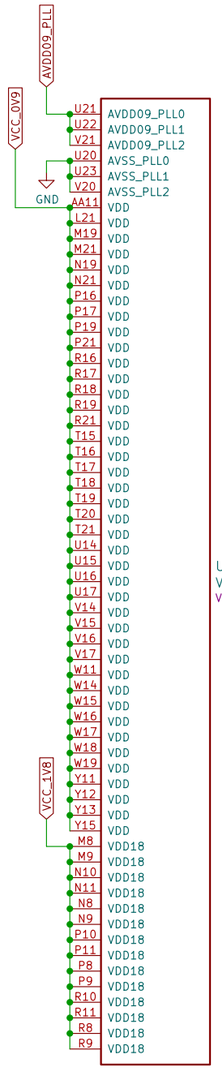
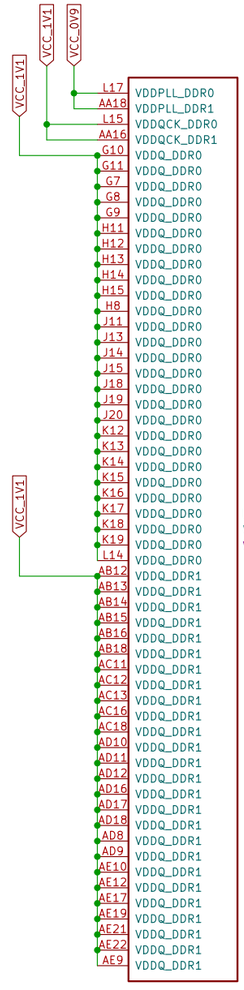
VIC7100 power









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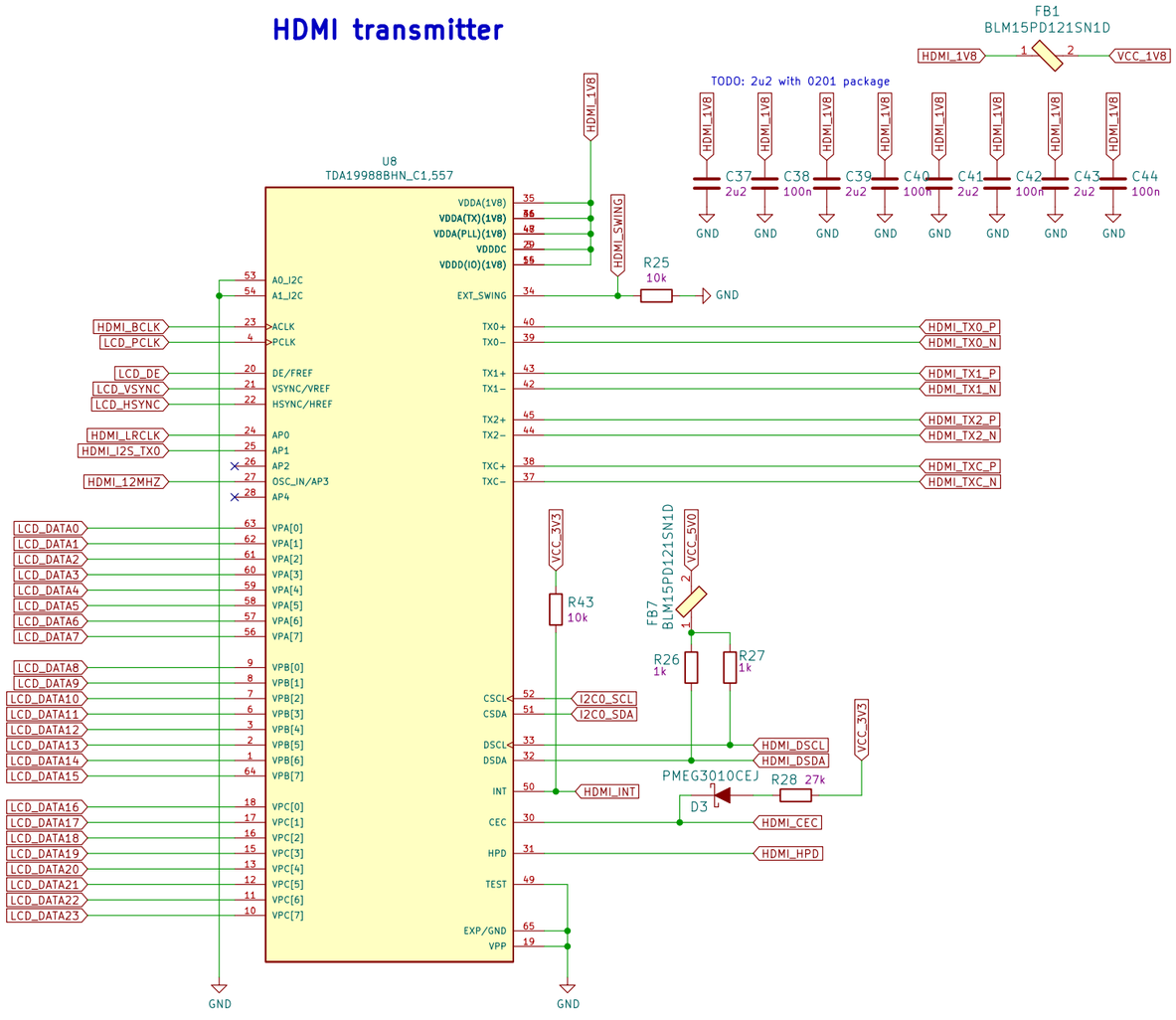
HDMI transmitter



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Size: A3	Date: **Rev: 1.0.0**
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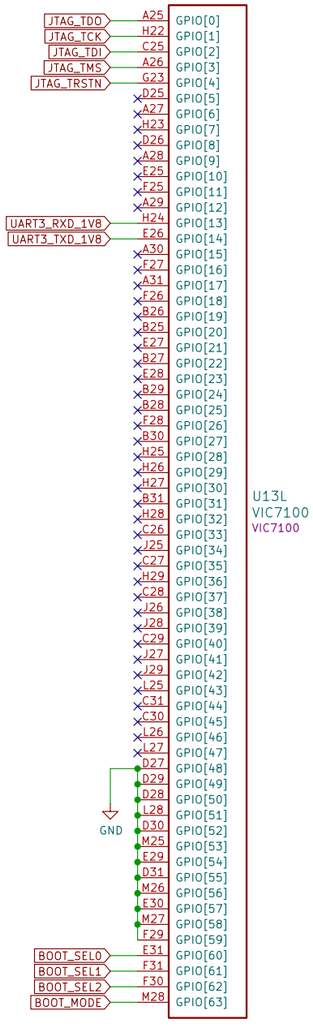
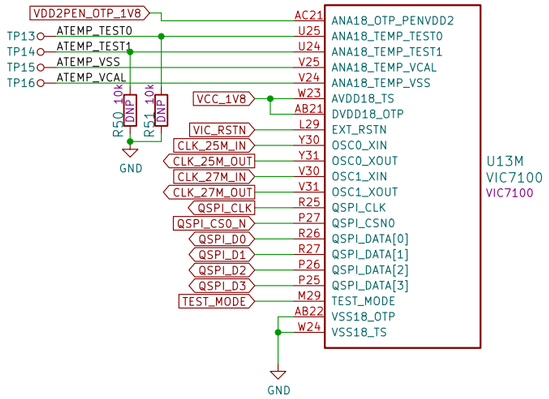
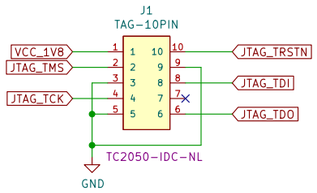
VIC7100 configuration

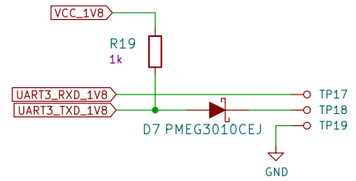




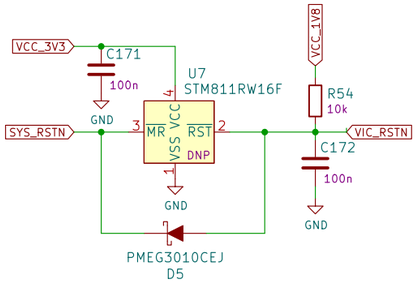
JTAG connector (1V8)



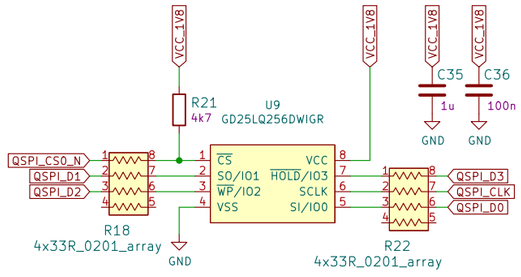
UART3 (1V8)



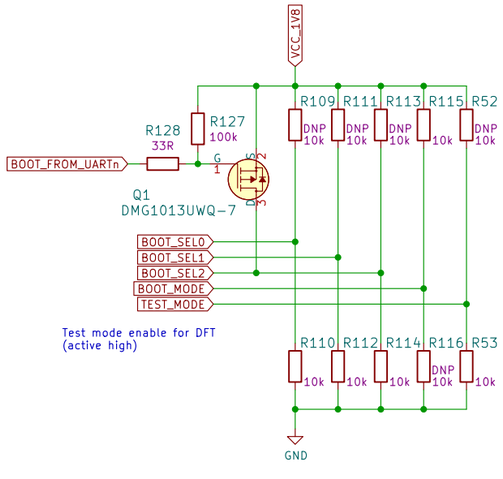
VIC reset



NOR Flash



Boot config



Clock crystals

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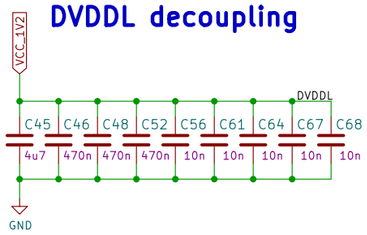
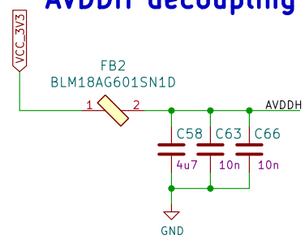
Size: A3	Date:	**Rev: 1.0.0**
KiCad E.D.A.	eeschema 6.0.5+dfsg-1~bpo11+1	Id: 5/10

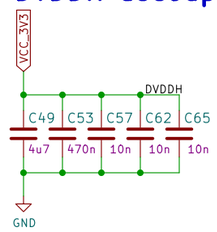
DVDDL decoupling



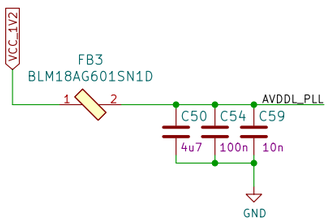
AVDDH decoupling



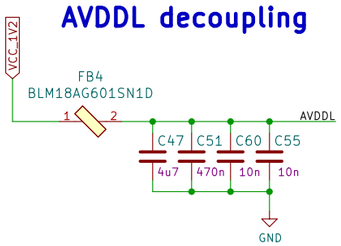
DVDDH decoupling



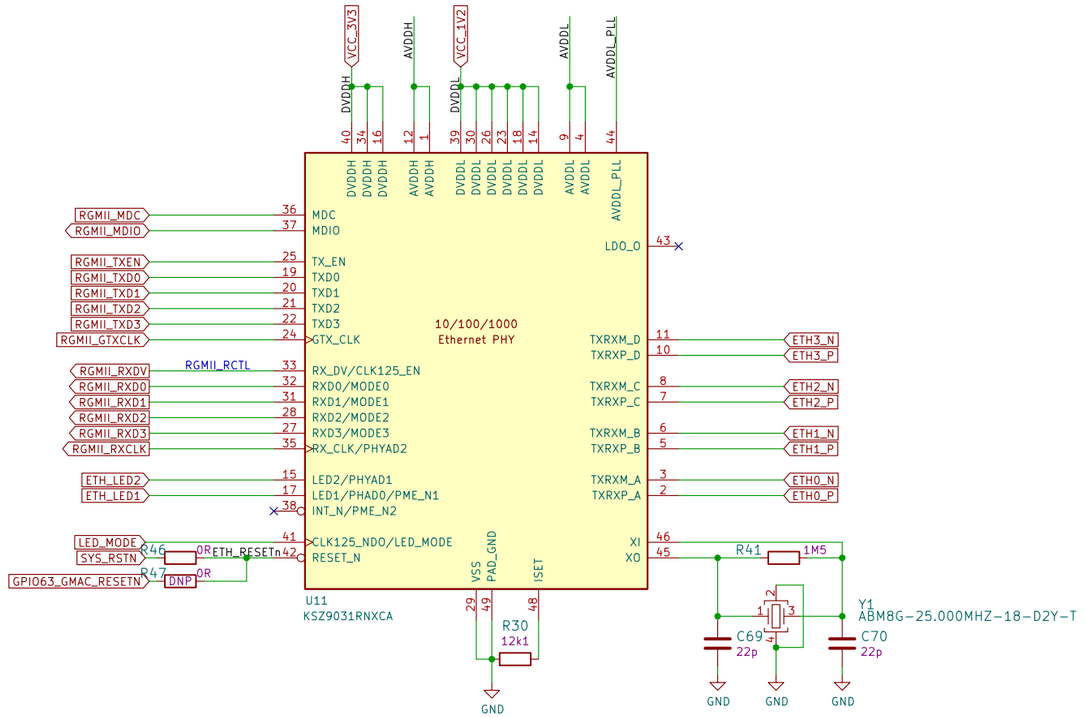
AVDDL_PLL decoupling



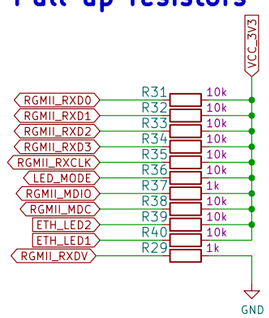
AVDDL decoupling



PHY



Pull up resistors



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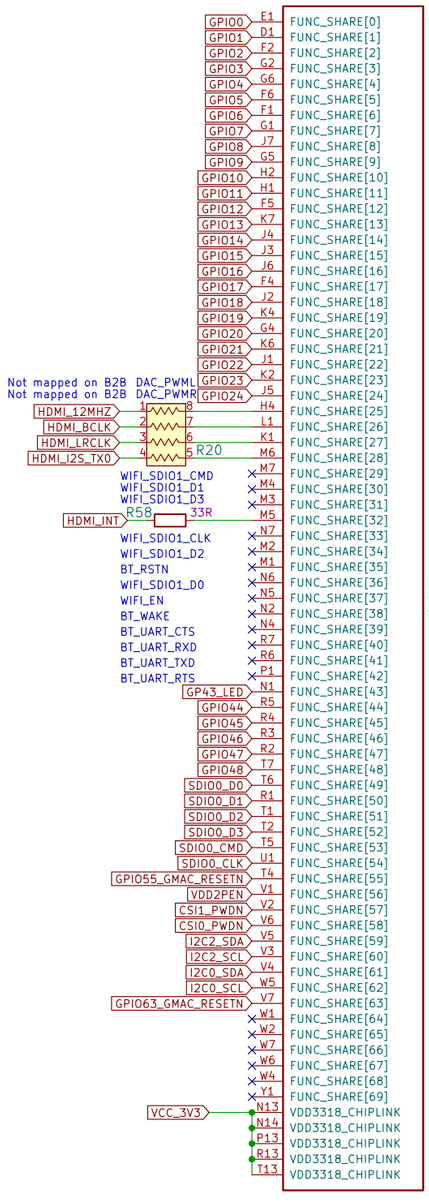
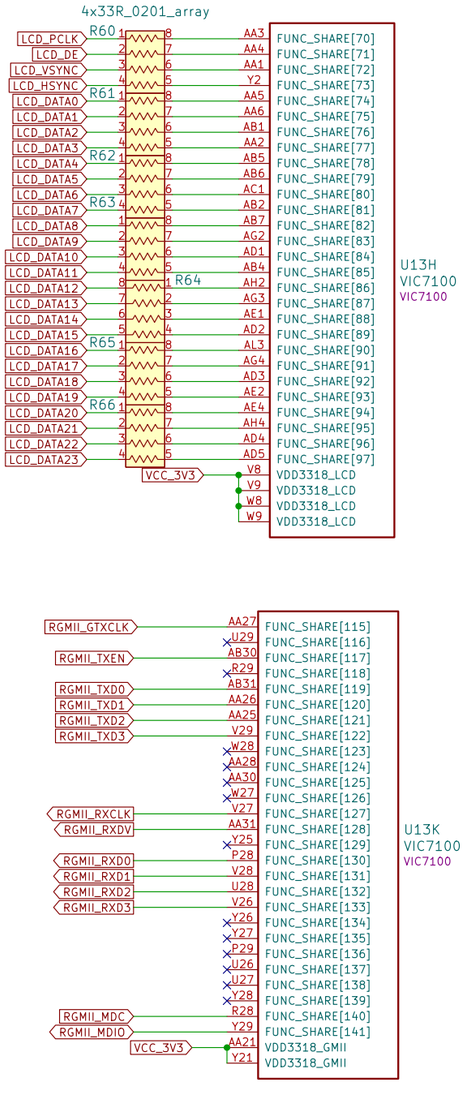
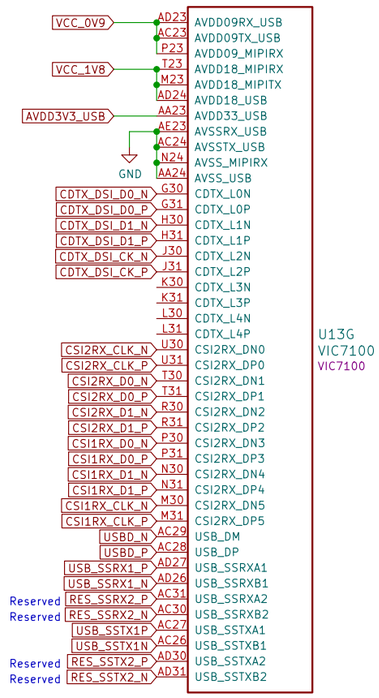
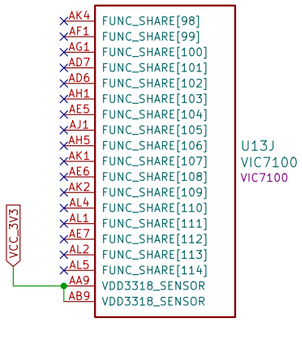
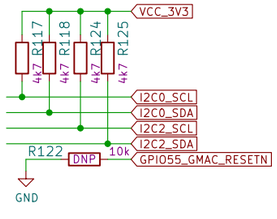
VIC7100 interfaces



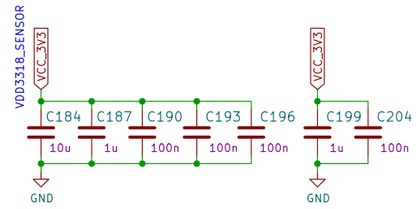




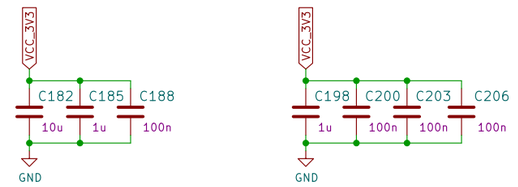




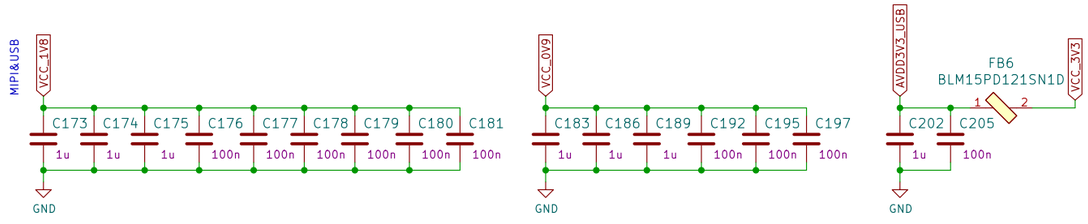
VIC 3V3 GPIO



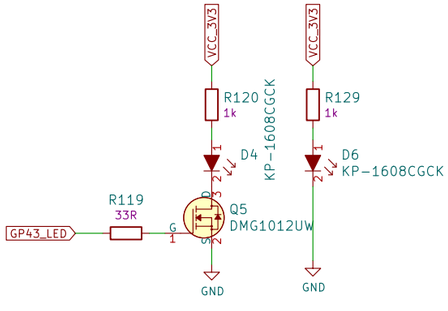
VIC LCD&RGMII



VIC MIPI&USB



ACK LED



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Size: A3	Date:	**Rev: 1.0.0**
KiCad E.D.A. eeschema 6.0.5+dfsg-1~bpo11+1		Id: 7/10

TODO: compare with BeagleV LPDDR4 schematic
TODO: find suitable LPDDR4 (32bit, 2ch, 4die, 1Gb)

LPDDR4

MT53D1024M32D4DT-053 WT:D
U1A

MT53D1024M32D4DT-053 WT:D
U1B

MT53D1024M32D4DT-053 WT:D
U3A

MT53D1024M32D4DT-053 WT:D
U3B

TBD: number of the capacitors varies between BeagleV reference
and our previous LPDDR4 designs — some of them can be probably skipped

TBD: number of the capacitors varies between BeagleV reference
and our previous LPDDR4 designs — some of them can be probably skipped

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Size: A3	Date:
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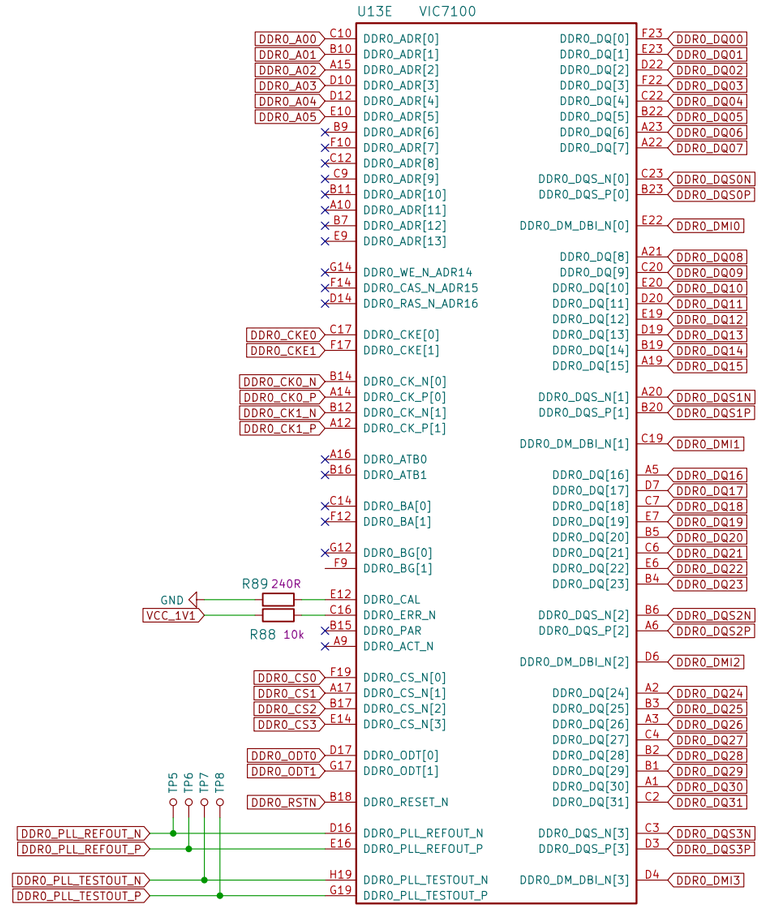
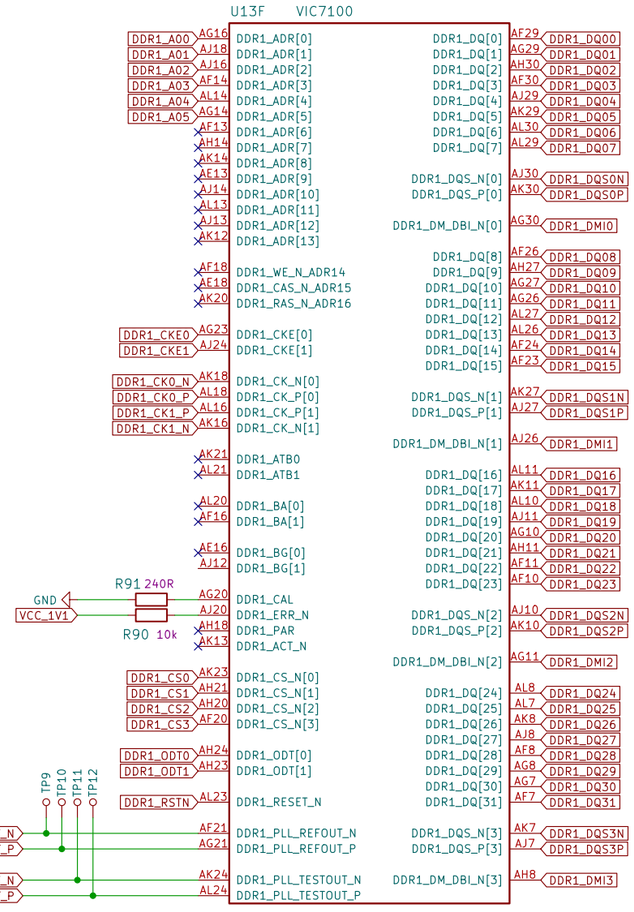
	Id: 8/10

VIC7100 DDR





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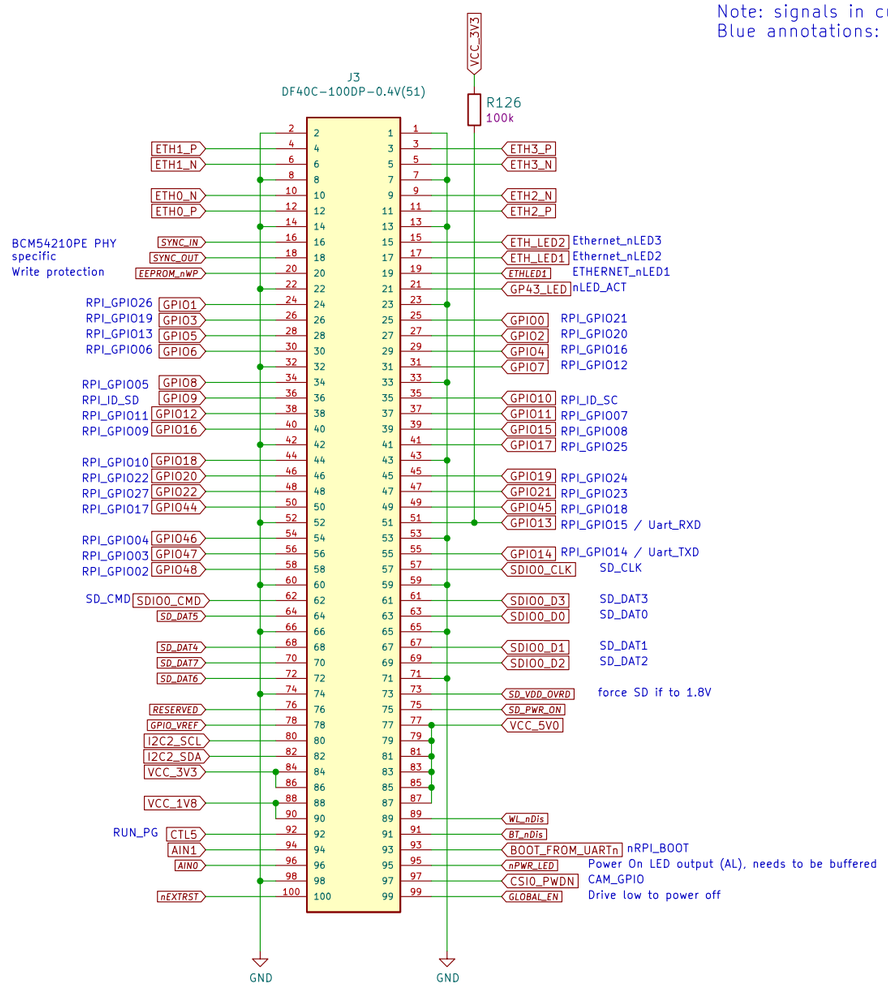
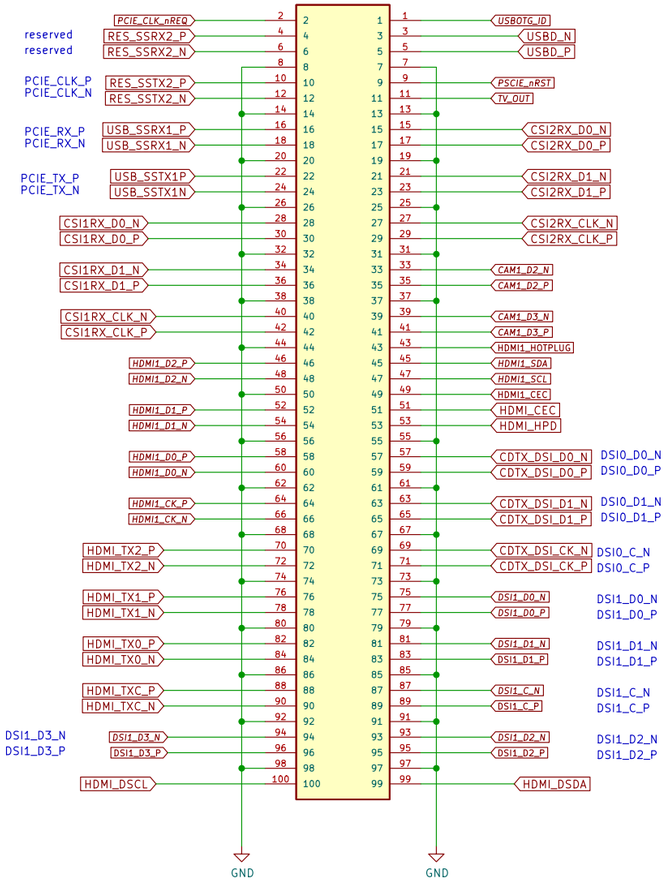
Size: A3	Date:	**Rev: 1.0.0**
KiCad E.D.A. eeschema 6.0.5+dfsg-1~bpo11+1		Id: 9/10

B2B connectors

Note: signals in cursive are NOT mapped on the SoM
Blue annotations: CM pin reference





MP1 PCB_Mount_Hole_2.5
MP2 PCB_Mount_Hole_2.5
MP3 PCB_Mount_Hole_2.5
MP4 PCB_Mount_Hole_2.5

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Sheet: /B2B connectors/
File: B2B-connectors.kicad_sch
Title: SoM board-to-board connectors

Size: A3	Date:	**Rev: 1.0.0**
KiCad E.D.A. eeschema 6.0.5+dfsg-1~bpo11+1		Id: 10/10