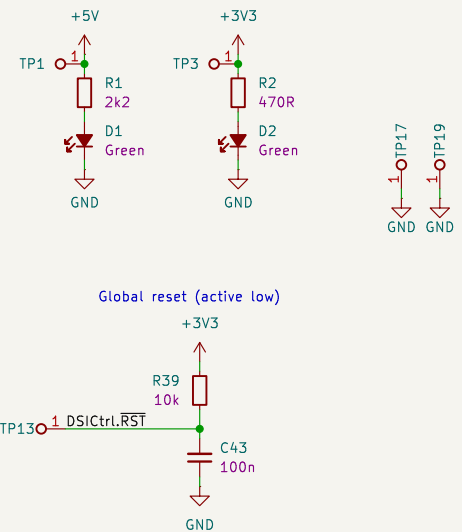
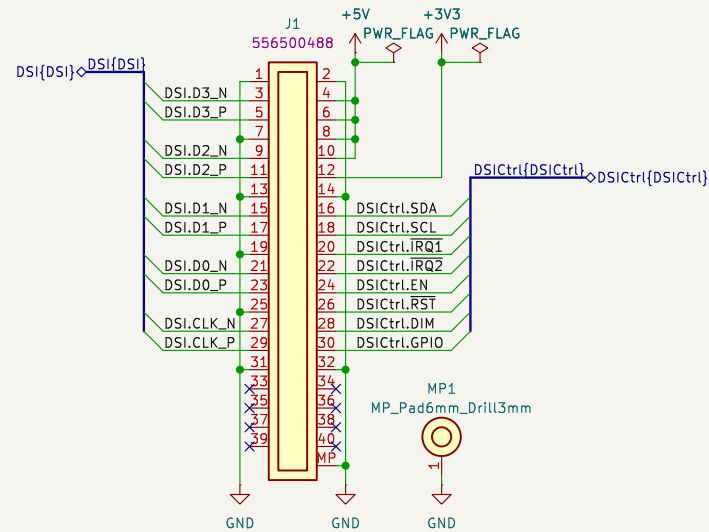


# Antmicro CM4 Baseboard DSI Connector



www.antmicro.com

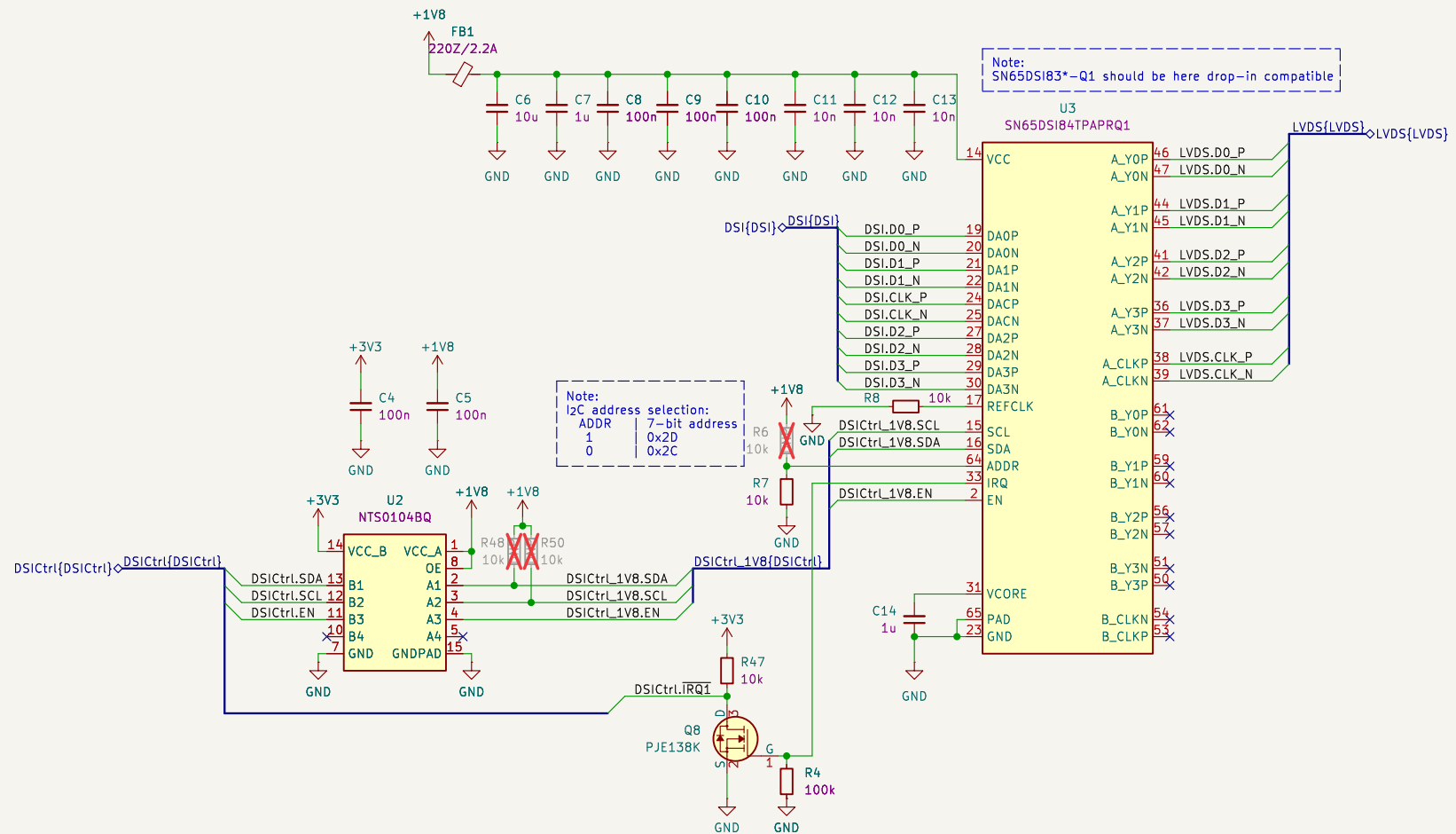
Antmicro

Sheet: /Antmicro CM4 Baseboard DSI Connector /  
File: dsi-connector.kicad\_sch

**Title: CM4 Baseboard LVDS Adapter**

Size: A4	Date: 2024-04-26	Rev: 1.1.0
KiCad E.D.A. kicad-cli 7.0.10+1		Id: 3/5

## DSI -> LVDS



[www.antmicro.com](http://www.antmicro.com)

## Antmicro

Sheet: /DSI to LVDS/

File: dsi-to-lvds.kicad\_sch

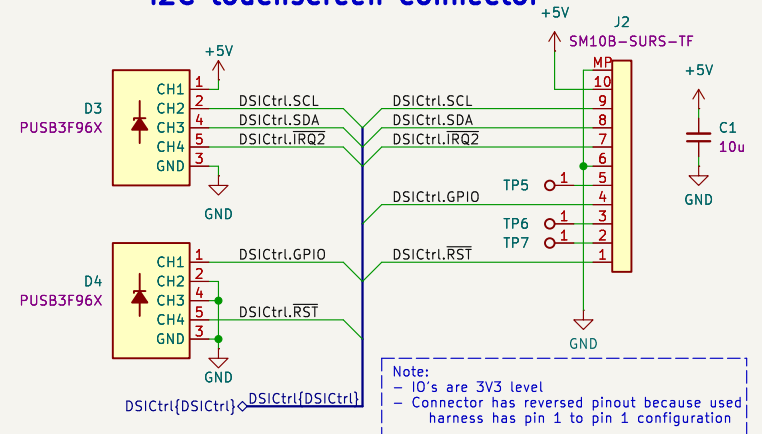
**Title: CM4 Baseboard LVDS Adapter**

Size: A4	Date: 2024-04-26
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Size: 71	Date: 2021-03
KiCad E.D.A.	kicad-cli 7.0.10+1

Rev: 1.1.0

Id: 4/5



Note:

- IO's are 3V3 level
- Connector has reversed pinout because used harness has pin 1 to pin 1 configuration