

# Data Center RDIMM DDR4 Tester



HyperRAM



File: hyperram.kicad\_sch

DDR4



File: DDR4.kicad\_sch

Interfaces



File: interfaces.kicad\_sch

Ethernet



File: ethernet.kicad\_sch

Supply



File: supply.kicad\_sch

FMC HPC



File: fmc\_hpc.kicad\_sch

Config SPI flash



File: config-spi.kicad\_sch

FPGA power



File: fpga-power.kicad\_sch

FPGA banks 12–15



File: fpga-banks-12-15.kicad\_sch

FPGA banks 16–34

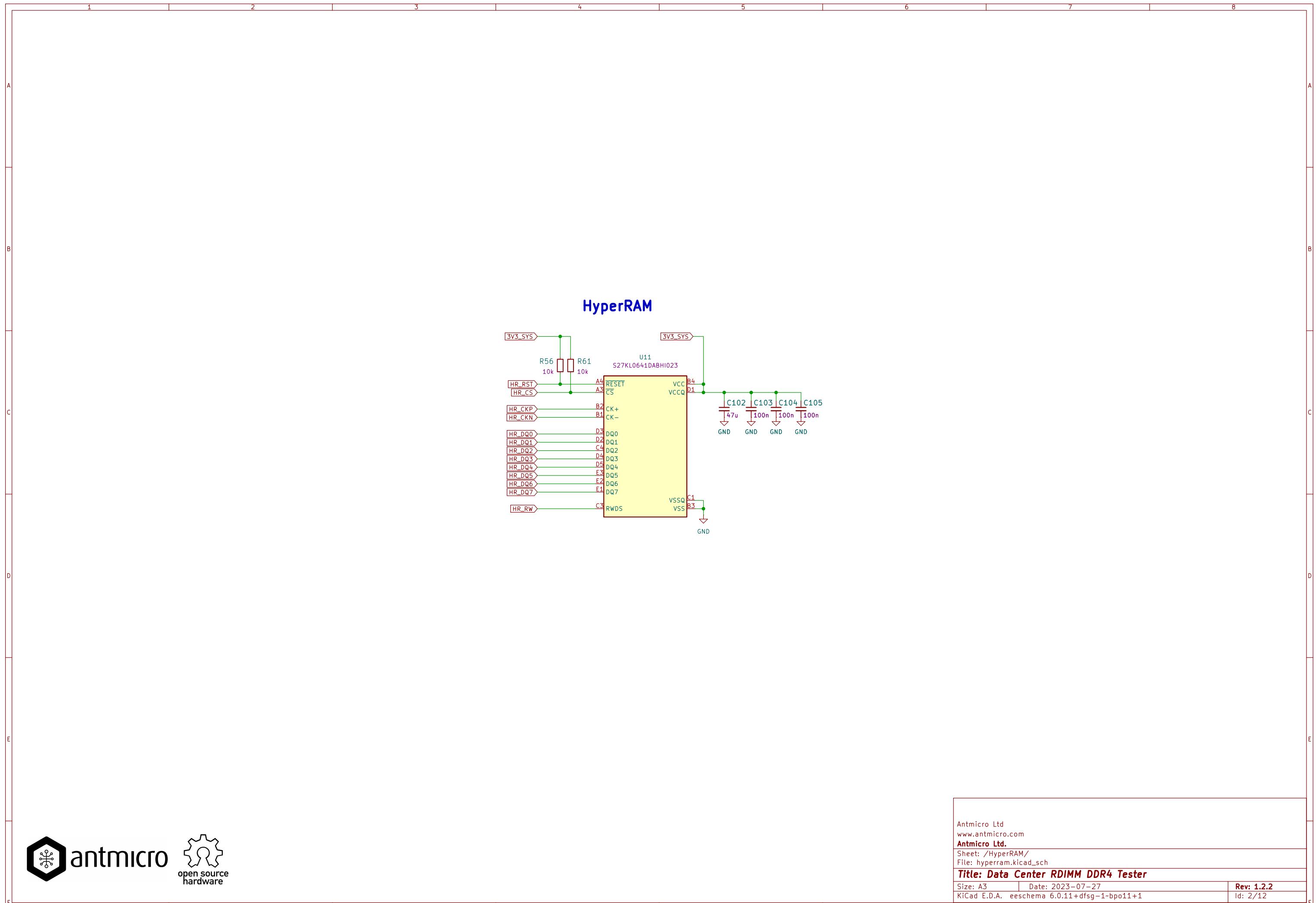


File: fpga-banks-16-34.kicad\_sch

FPGA banks 115–116



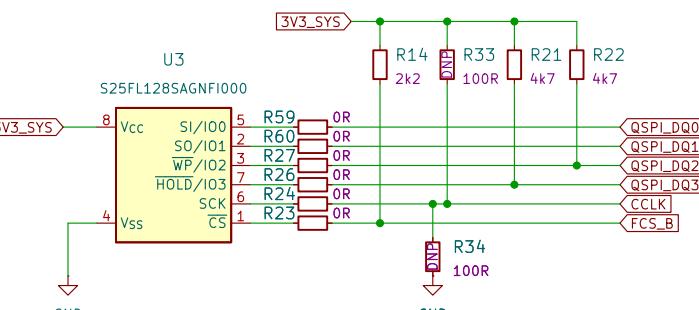
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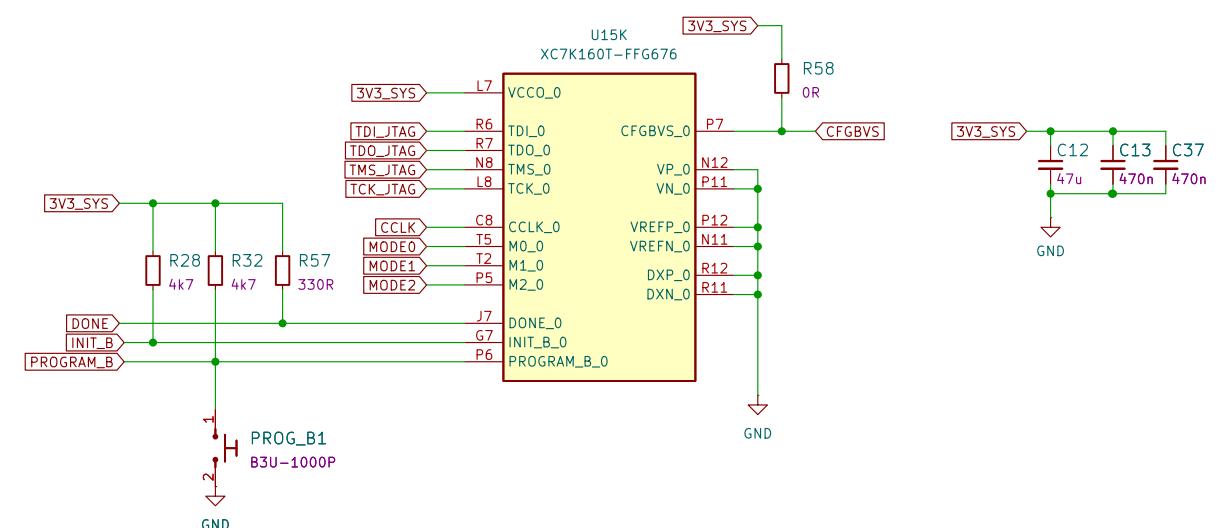
## Master SPI Quad (x4) configuration scheme

Follows Figure 2-14 7 Series FPGAs Configuration User Guide UG470 (v1.13.1)

## (Q)SPI flash

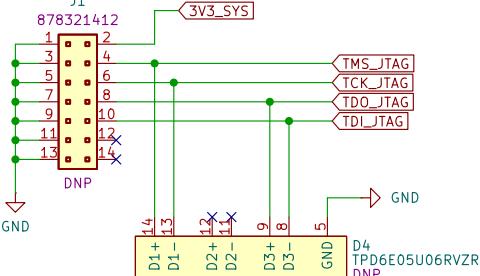


FPGA BANK 0

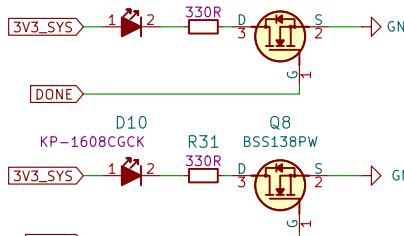


# JTAG Connector

Compatible with Xilinx Platform Cable

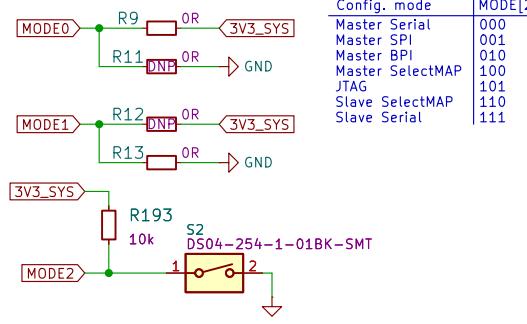


## STATUS LEDs

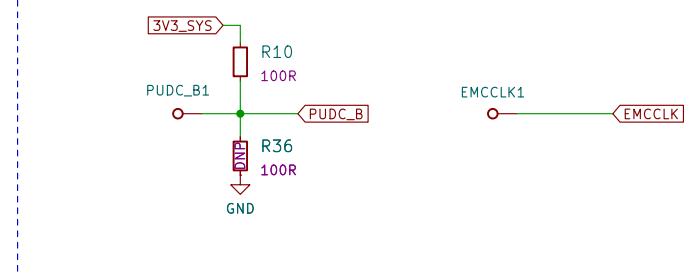


## Configuration Modes

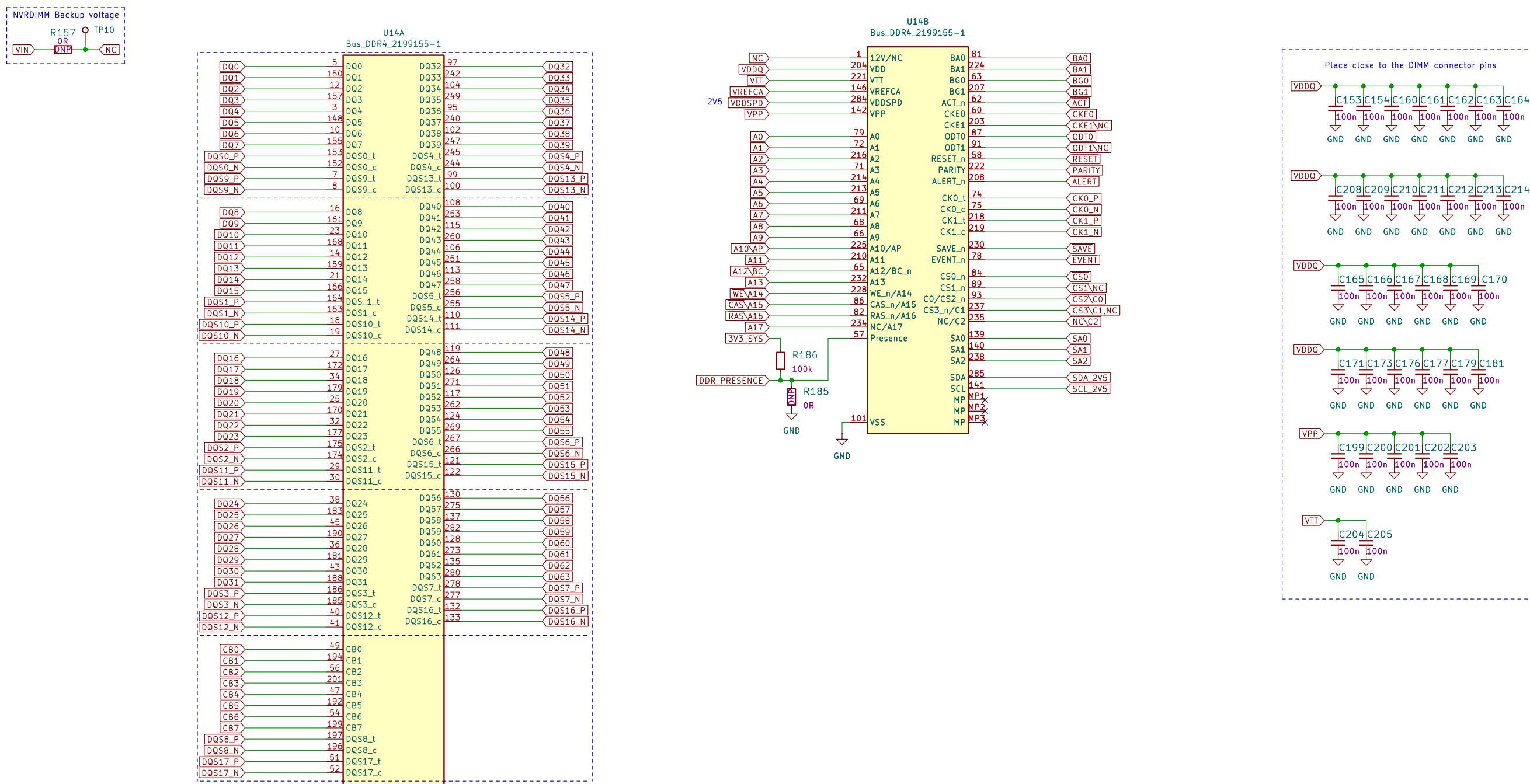
For details, see UG470 p. 21



## Probes



## DDR4 RDIMM connector



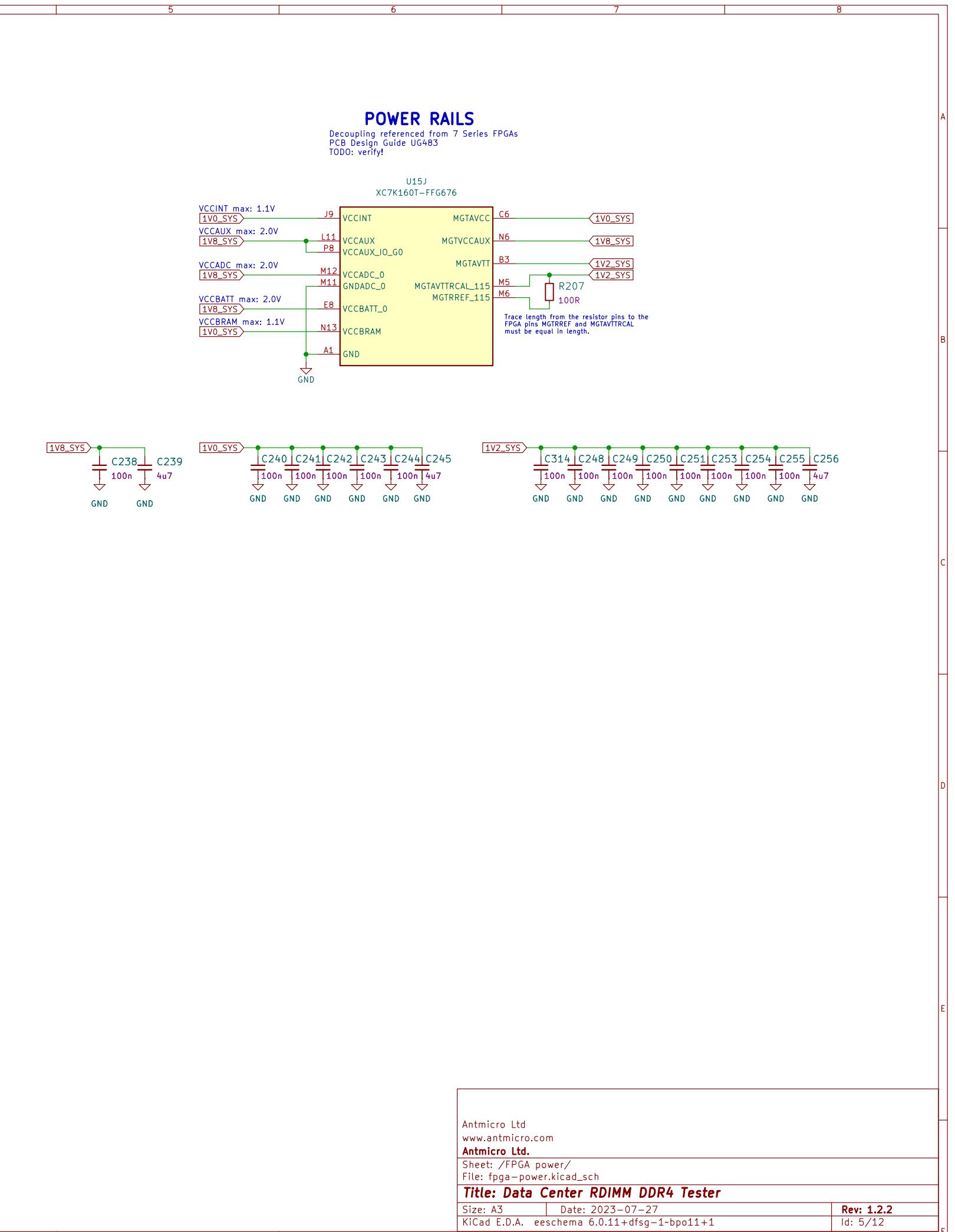
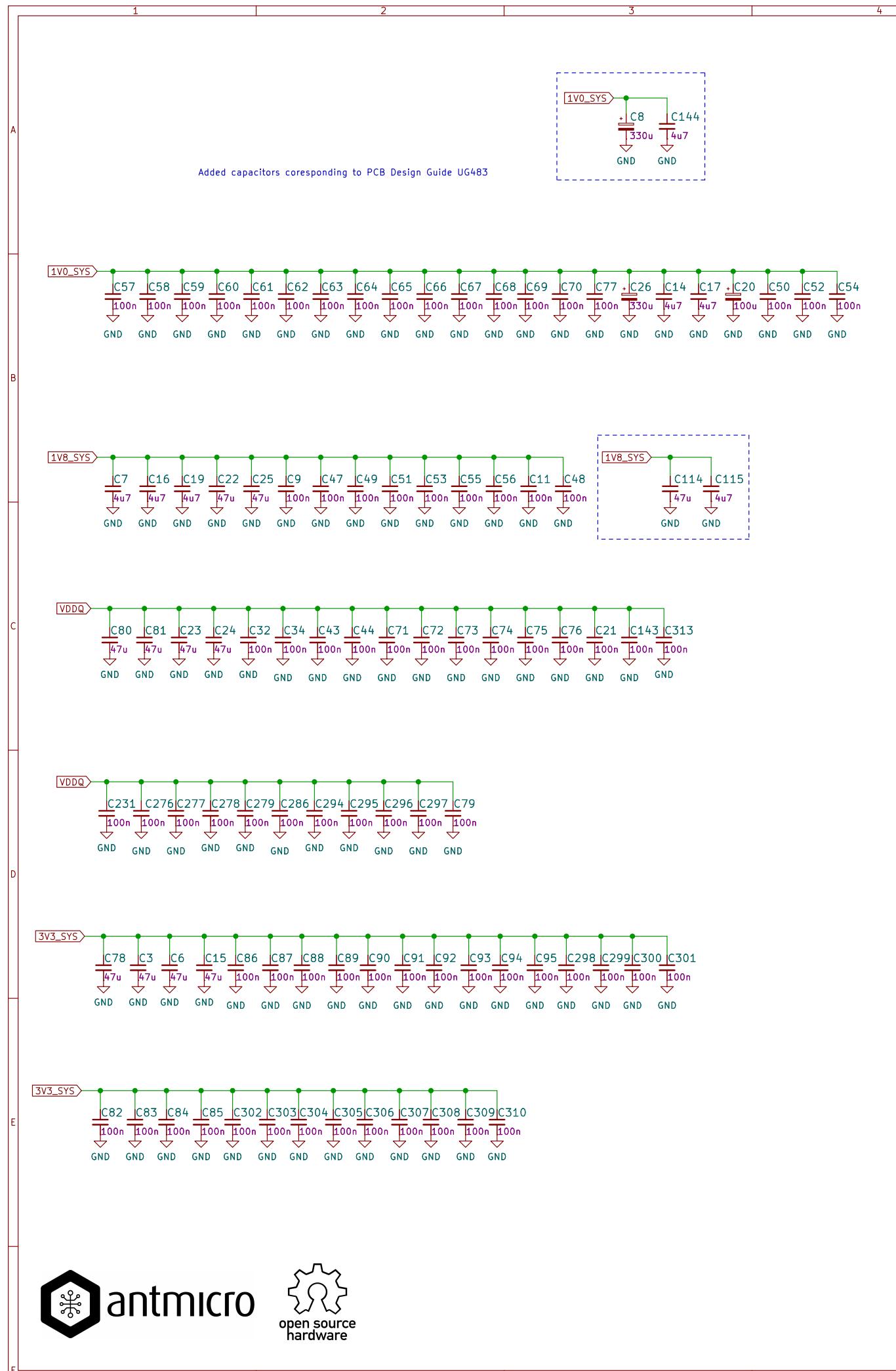
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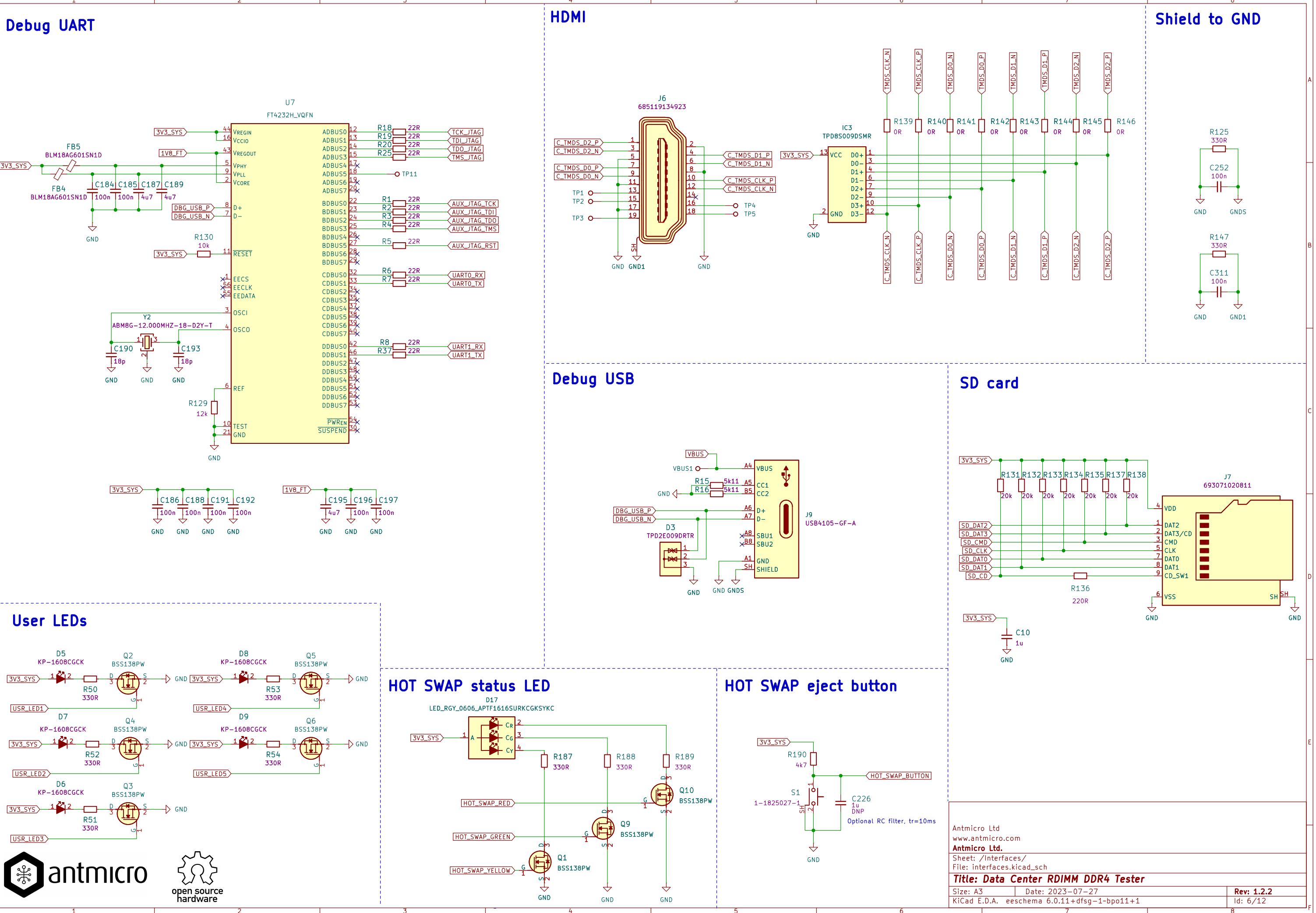
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File: DDR4.kicad\_sch

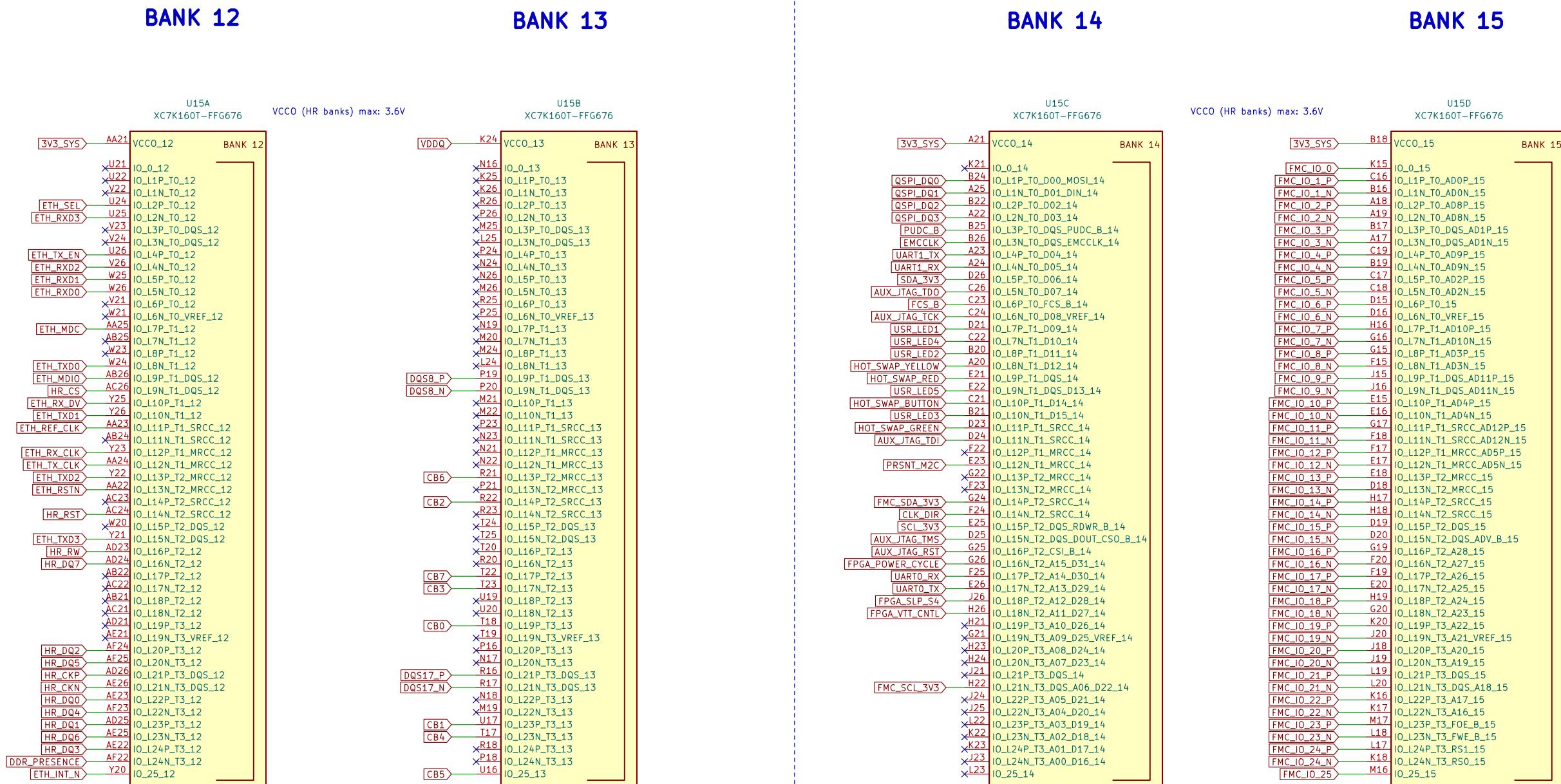
**Title: Data Center RDIMM DDR4 Tester**

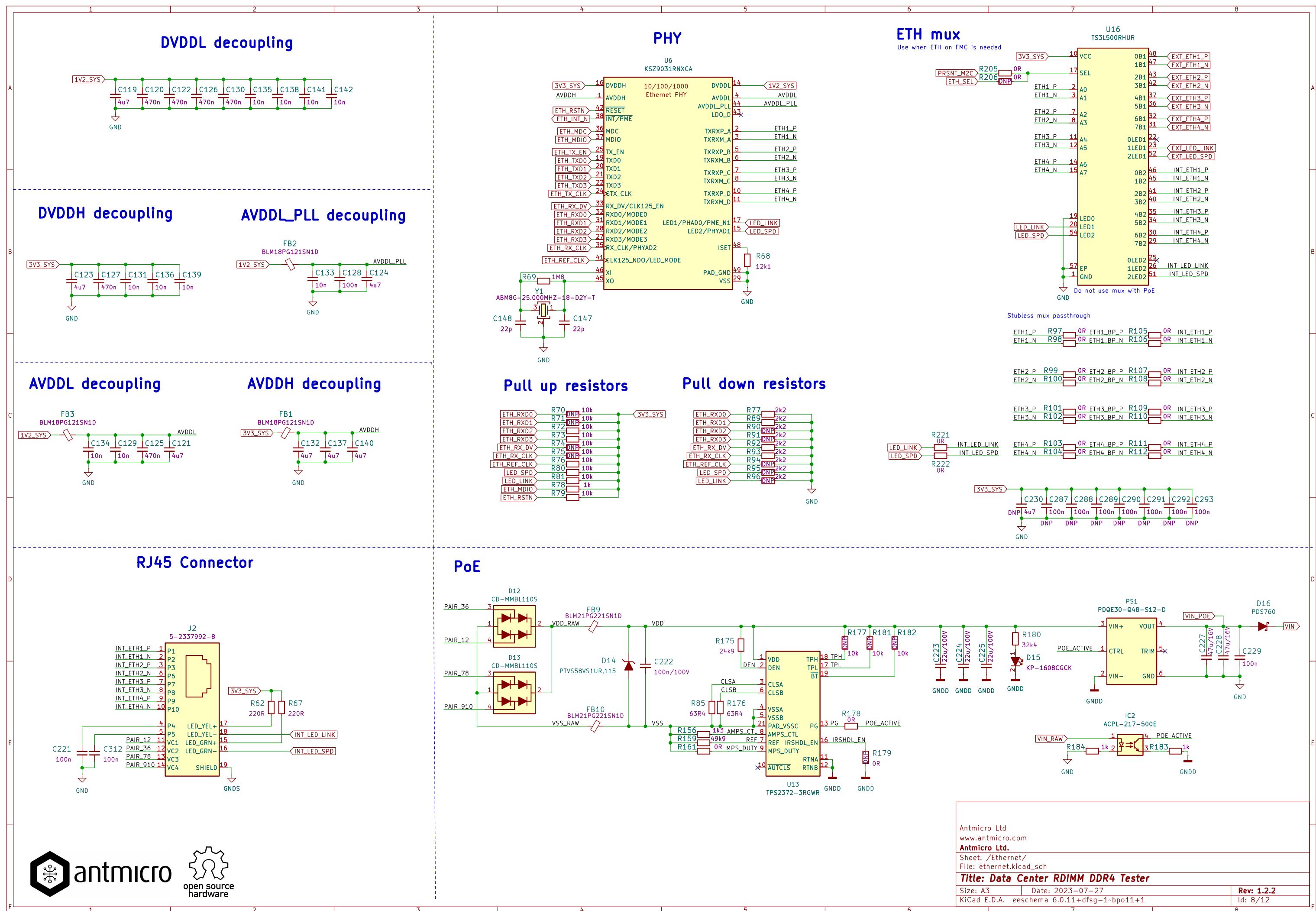
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KiCad E.D.A. eeschema 6.0.11+dfsg1-1+bpo11+1

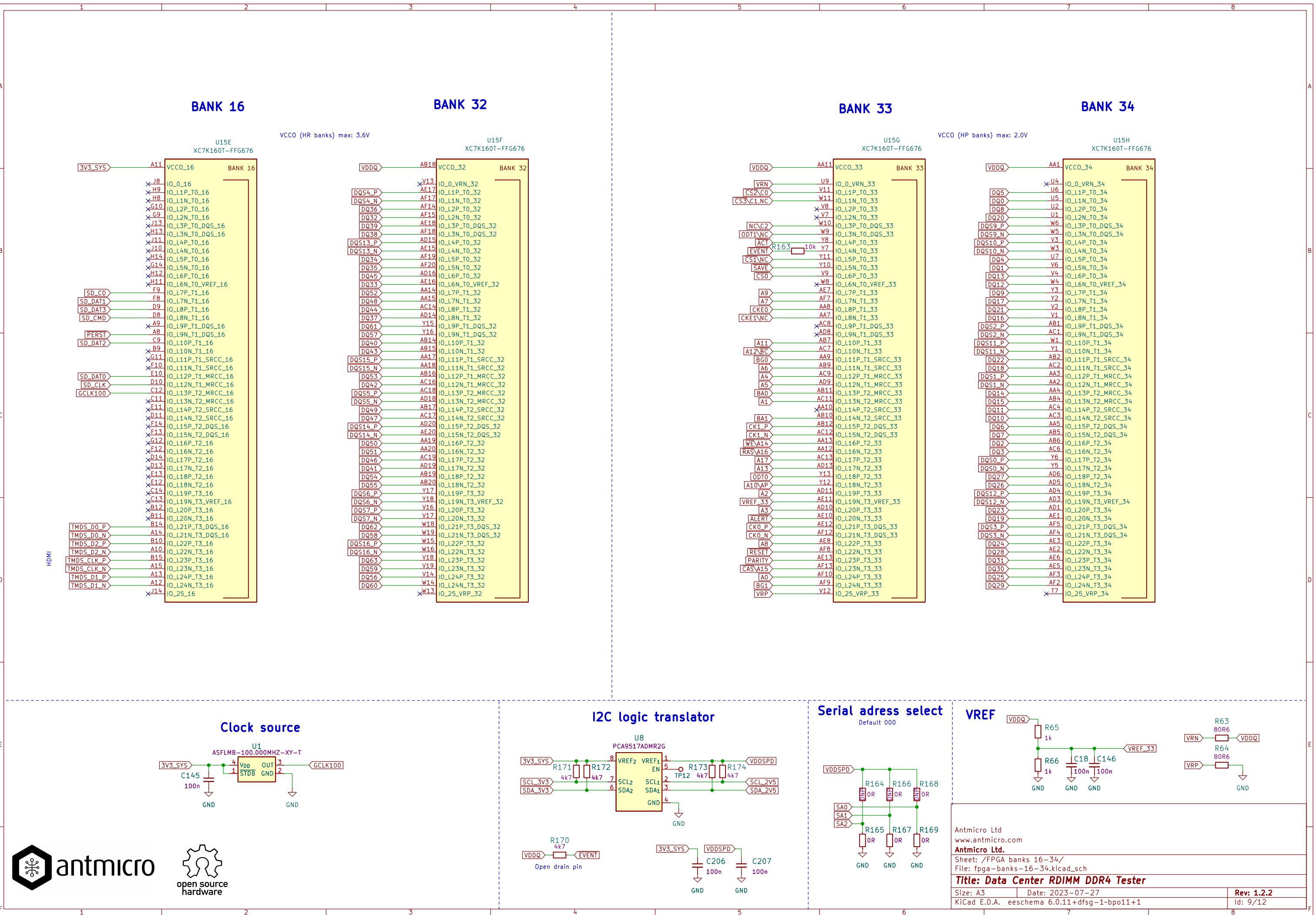
Rev: 1.2.2 | Id: 4/12

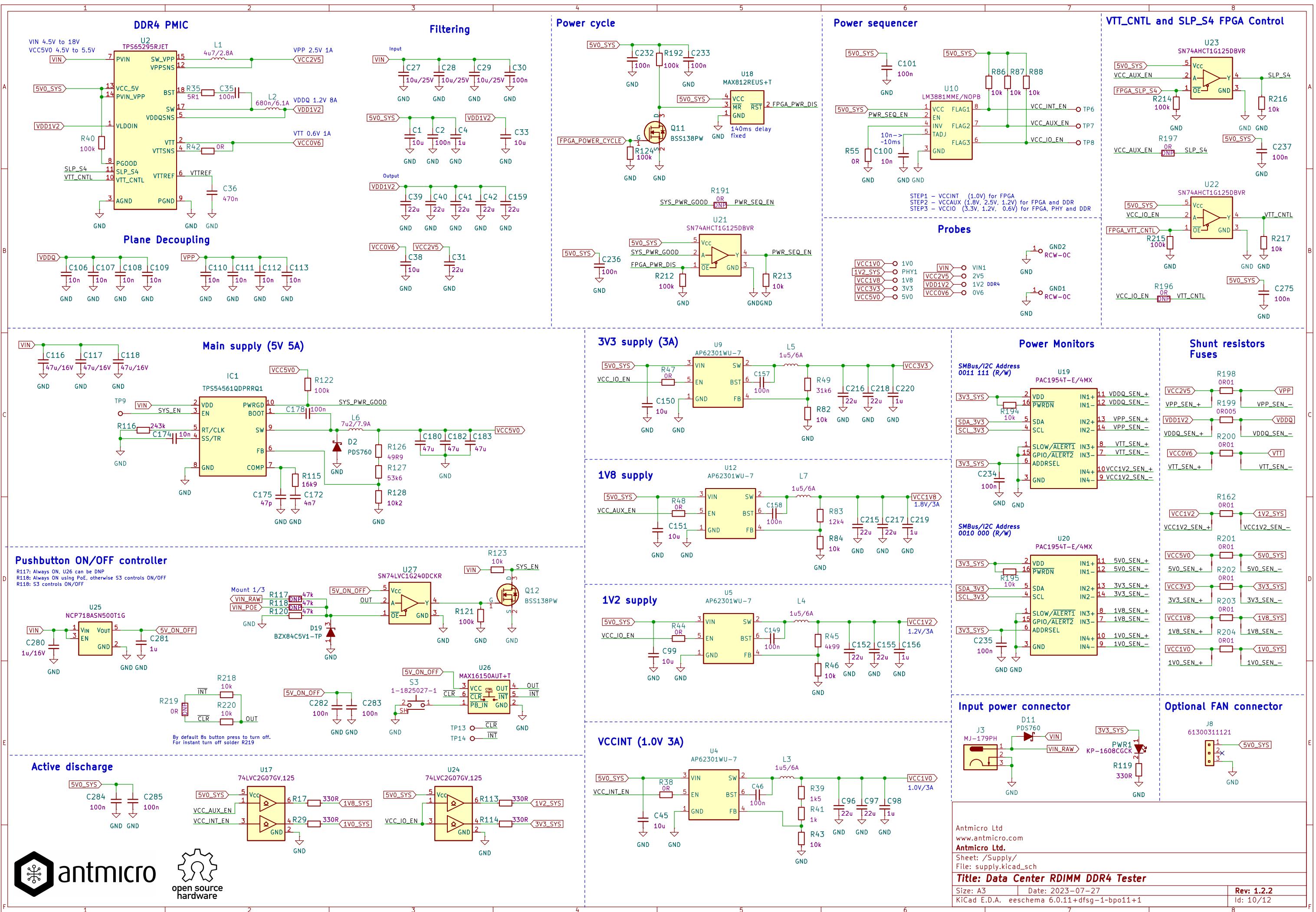












A

A

B

B

C

C

D

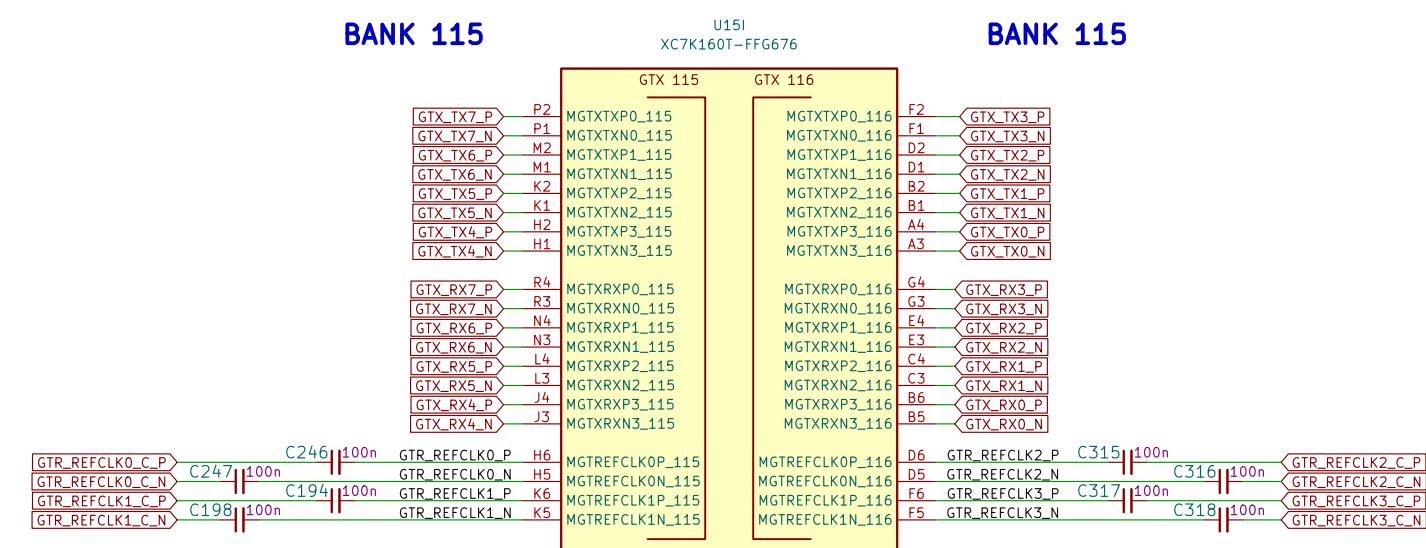
D

E

E

F

F



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Sheet: /FPGA banks 115-116/  
File: pcie.kicad\_sch

**Title: Data Center RDIMM DDR4 Tester**

Size: A3 | Date: 2023-07-27  
KiCad E.D.A. eeschema 6.0.11+dfsg-1-bpo11+1

Rev: 1.2.2 | Id: 11/12

# FMC HPC

