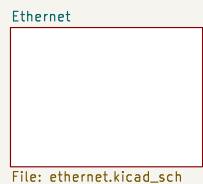
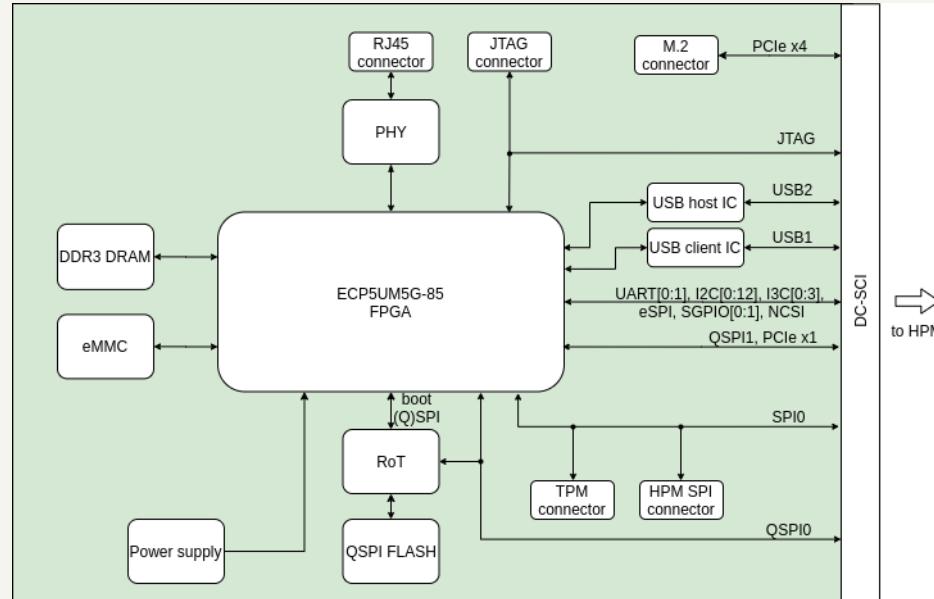


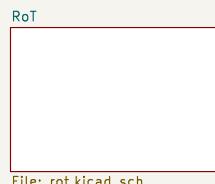
## ECP5 – Datacenter Secure Control Module (DC-SCM)



Ethernet



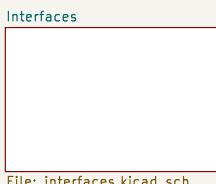
PCIe-connector



RoT



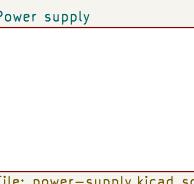
Edge connector



Interfaces



DDR3



Power supply

File: ethernet.kicad\_sch

File: pcie-conn.kicad\_sch

File: rot.kicad\_sch

File: edge-connector.kicad\_sch

File: interfaces.kicad\_sch

File: ddr3.kicad\_sch

File: power-supply.kicad\_sch

FPGA power supply

FPGA banks

Logo N2  
oshw\_logo  
N1  
Logo antmicro\_logo

Sheet: /  
File: ecp5-dc-scm.kicad\_sch

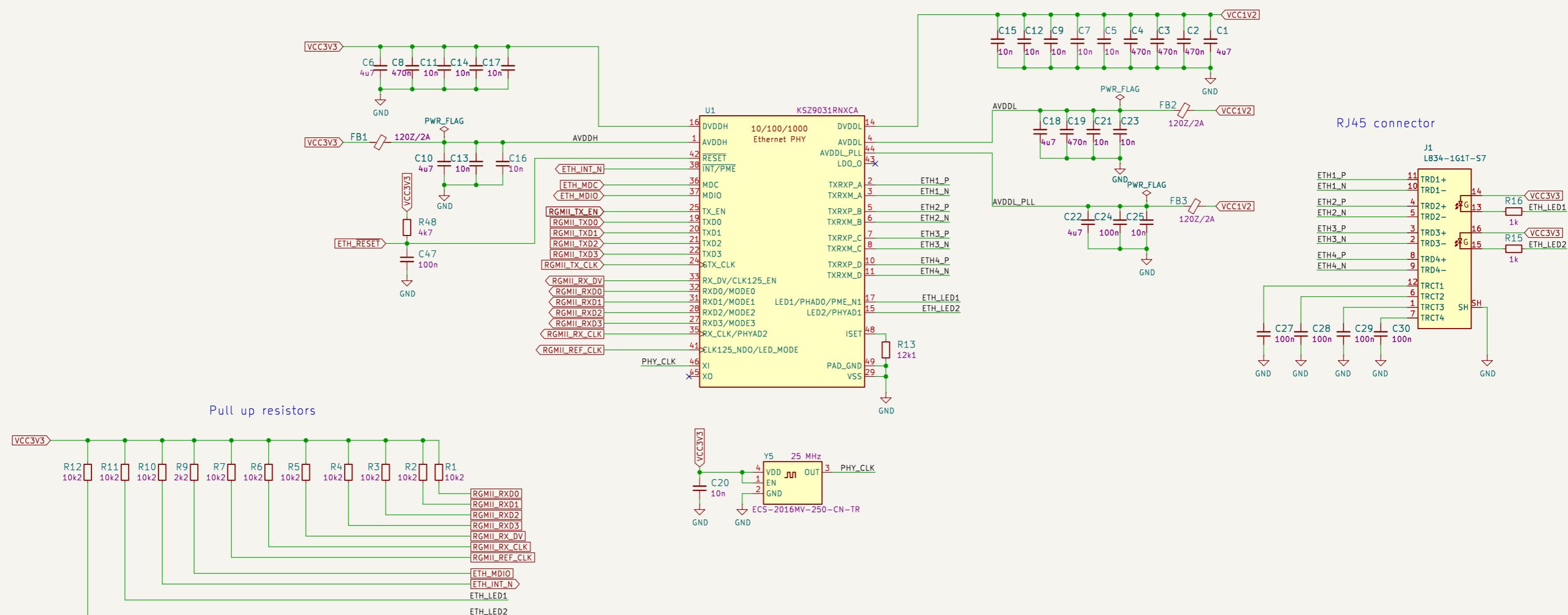
**Title: ECP5 – Datacenter Secure Control Module (DC-SCM)**

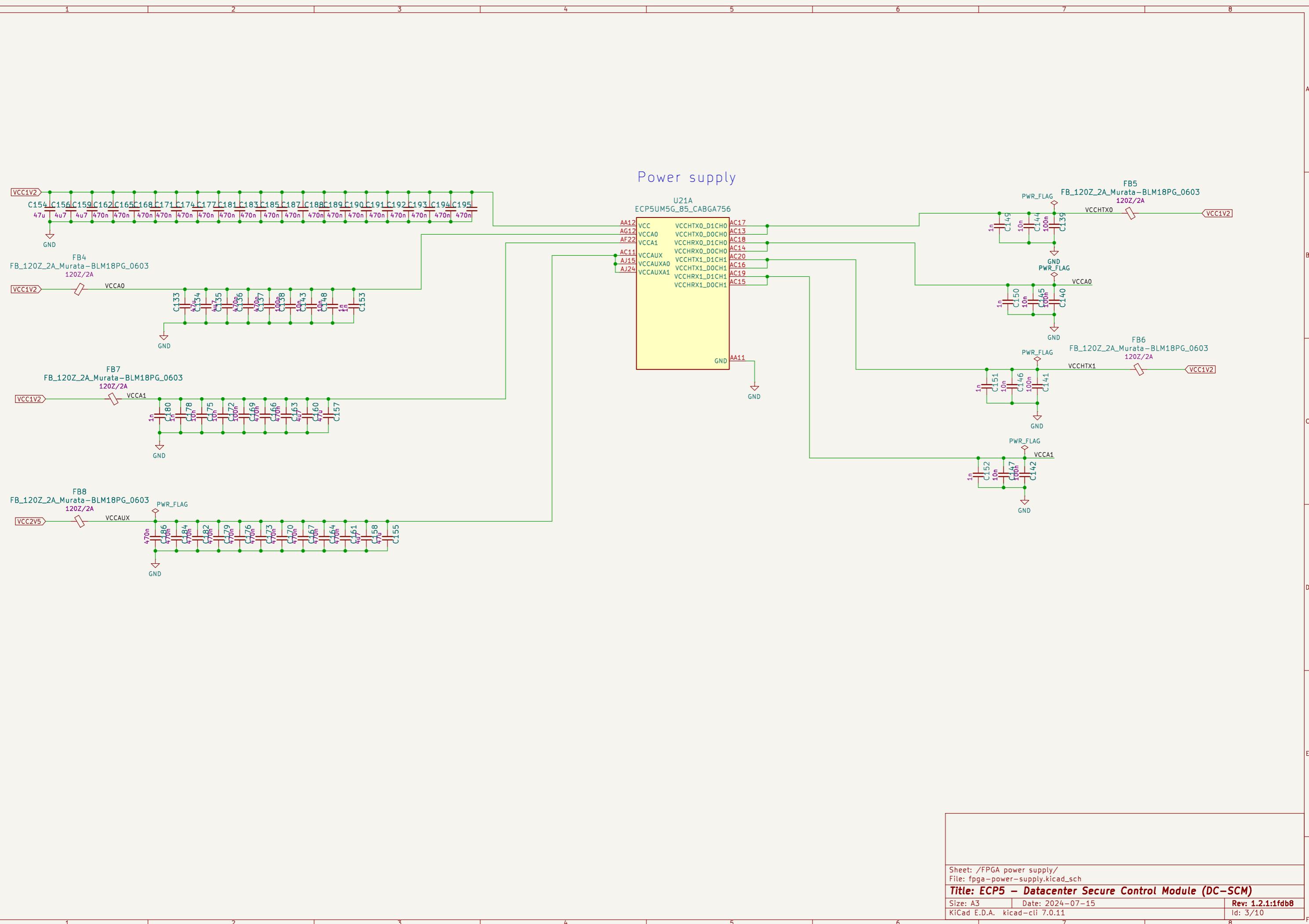
Size: A4 Date: 2024-07-15

KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.2.1:1fdb8

Id: 1/10





A

B

C

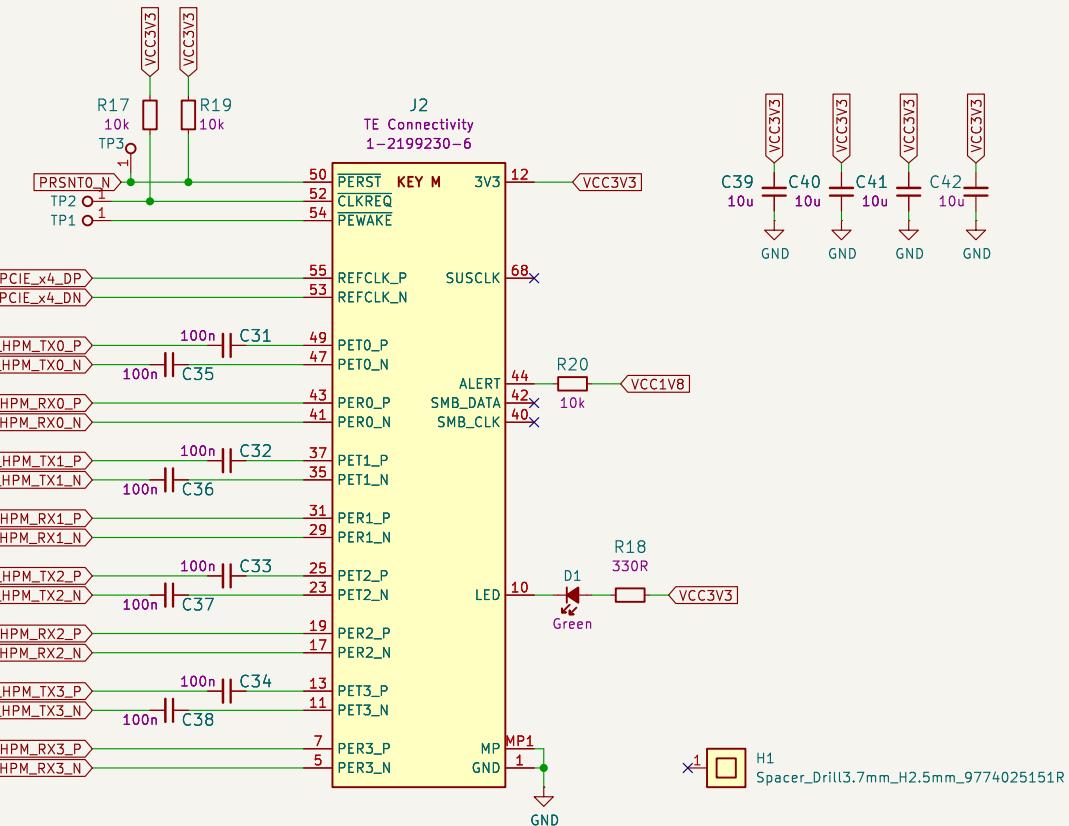
D

A

B

C

D



Sheet: /PCIe-connector/  
File: pcie-conn.kicad\_sch

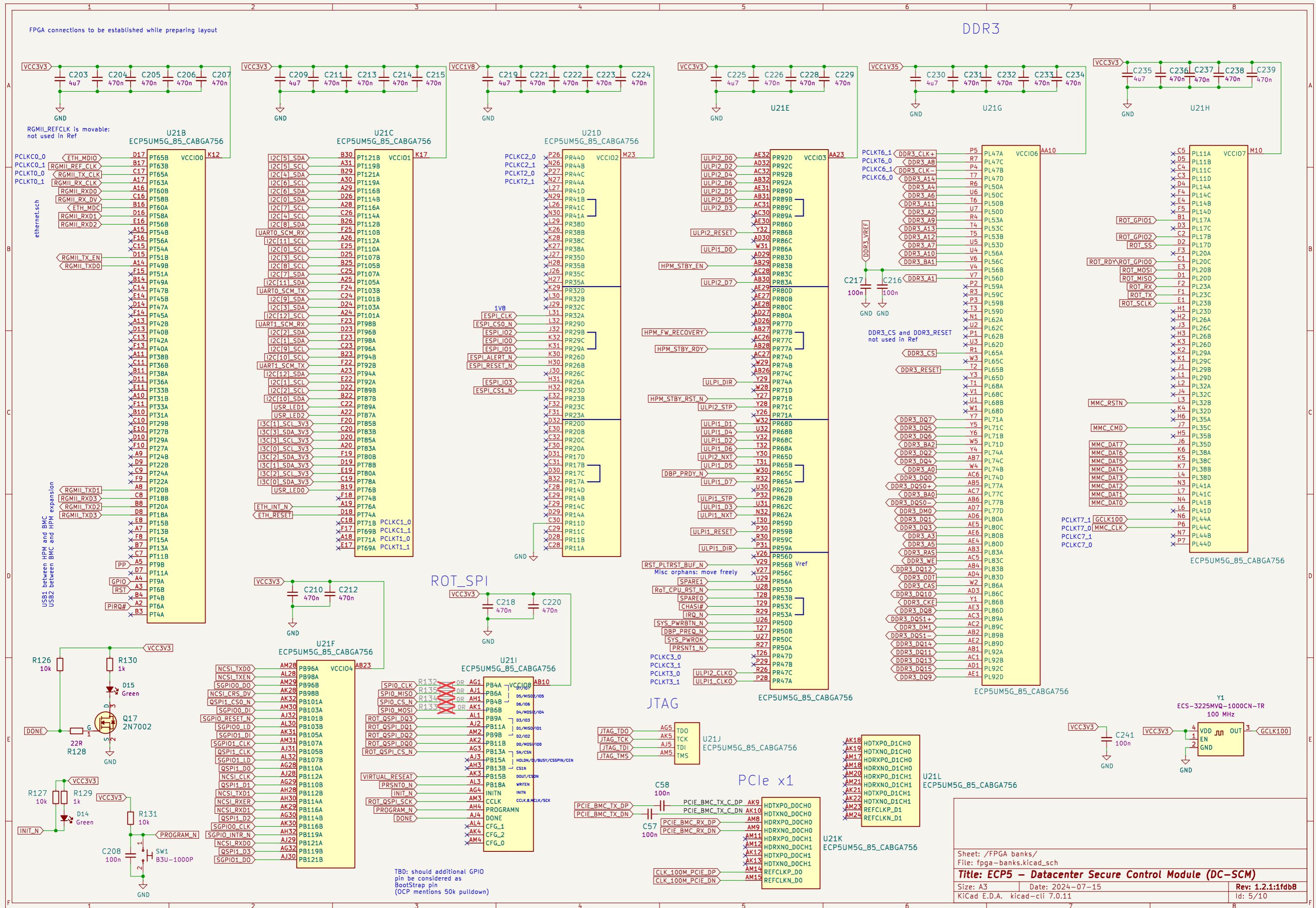
**Title: ECP5 – Datacenter Secure Control Module (DC-SCM)**

Size: A4 Date: 2024-07-15

KiCad E.D.A. kicad-cli 7.0.11

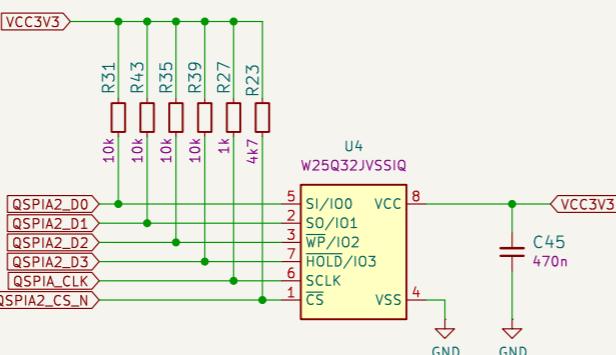
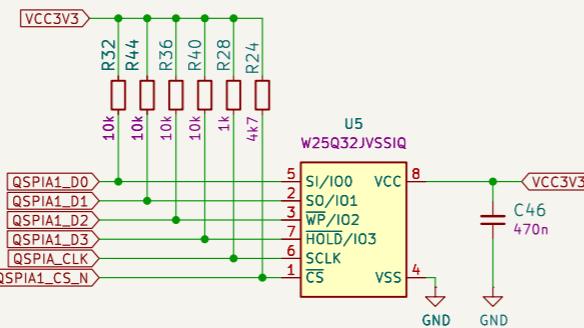
Rev: 1.2.1:1fdb8

Id: 4/10

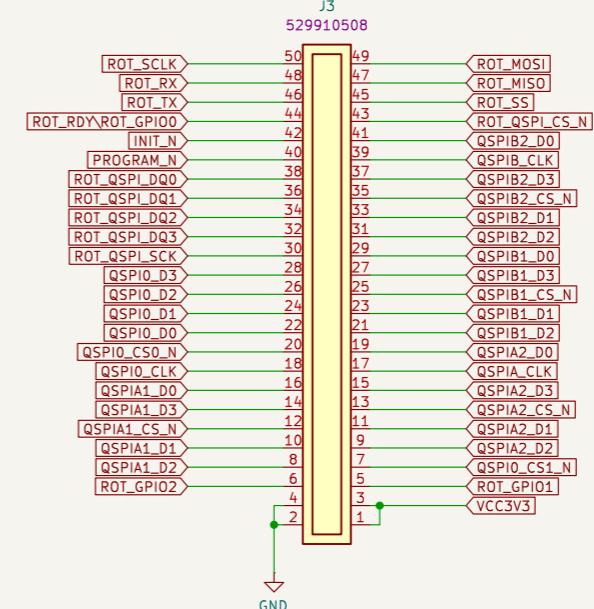


**BIOS flash**

One or typically two flash devices used to contain the BIOS firmware image

**RoT connector**

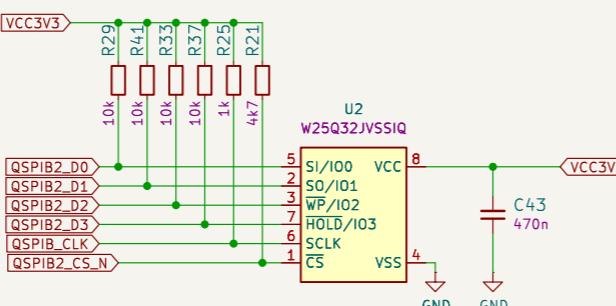
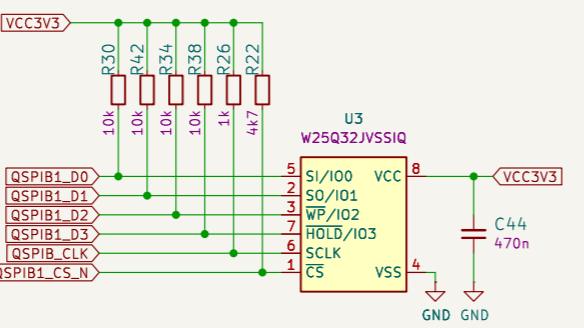
preliminary



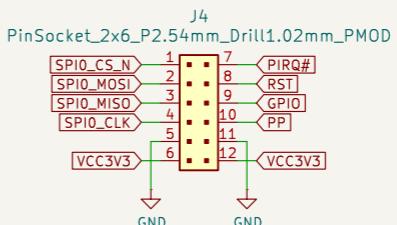
H2  
Spacer\_Drill3.7mm\_H4mm\_9774040151R

**BMC flash**

One or typically two flash devices used to contain the BMC firmware image

**TPM connector**

Pinout for AES-PMOD-TPM20-SLB9670-G



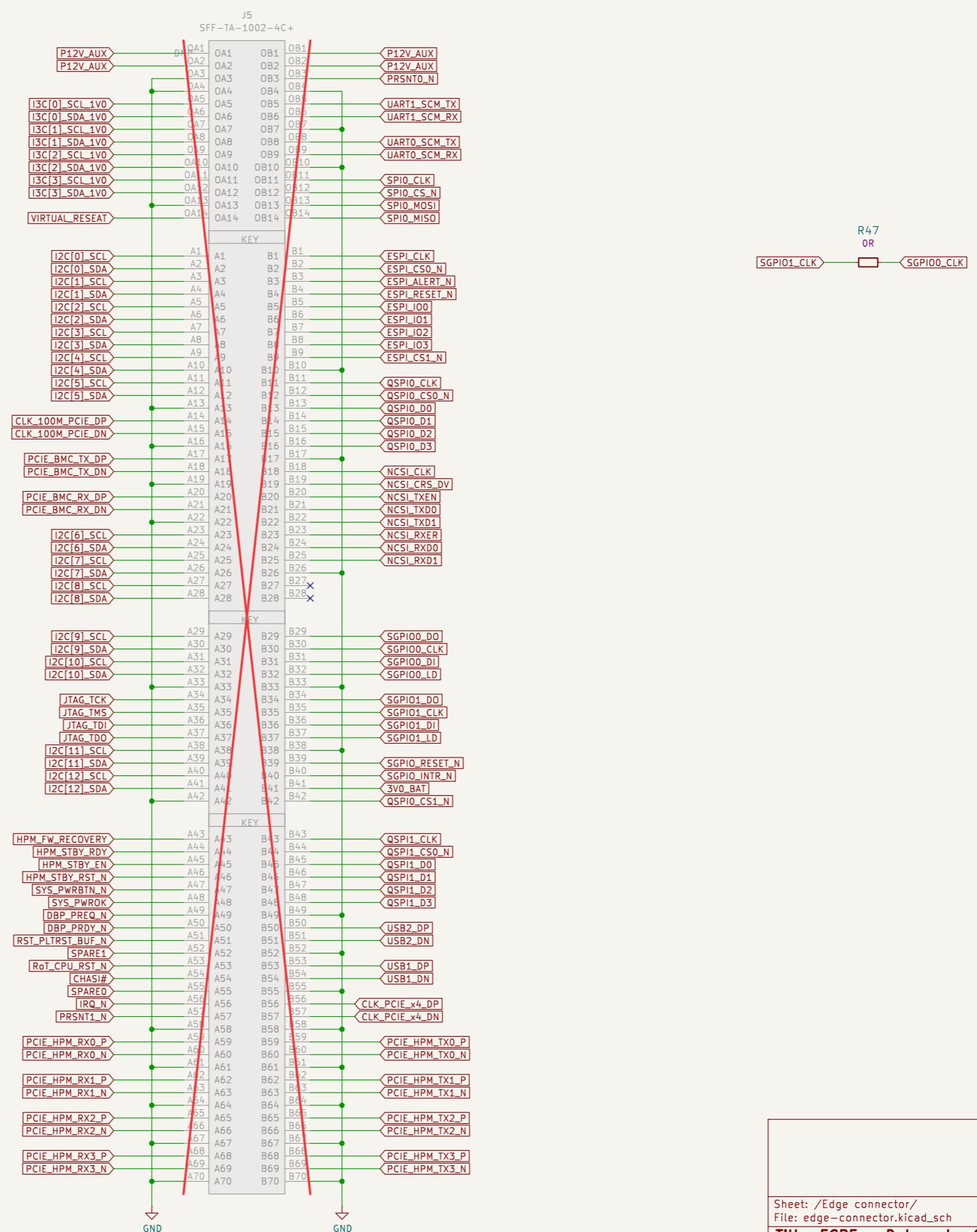
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File: rot.kicad\_sch

**Title: ECP5 – Datacenter Secure Control Module (DC-SCM)**

Size: A3 Date: 2024-07-15  
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.2.1:1fdb8  
Id: 6/10

## Edge connector



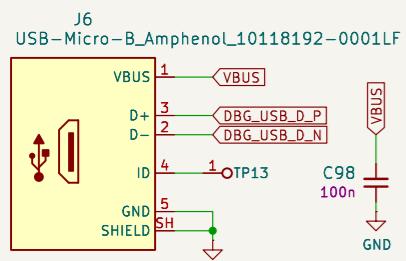
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**Title: ECP5 – Datacenter Secure Control Module (DC-SCM)**

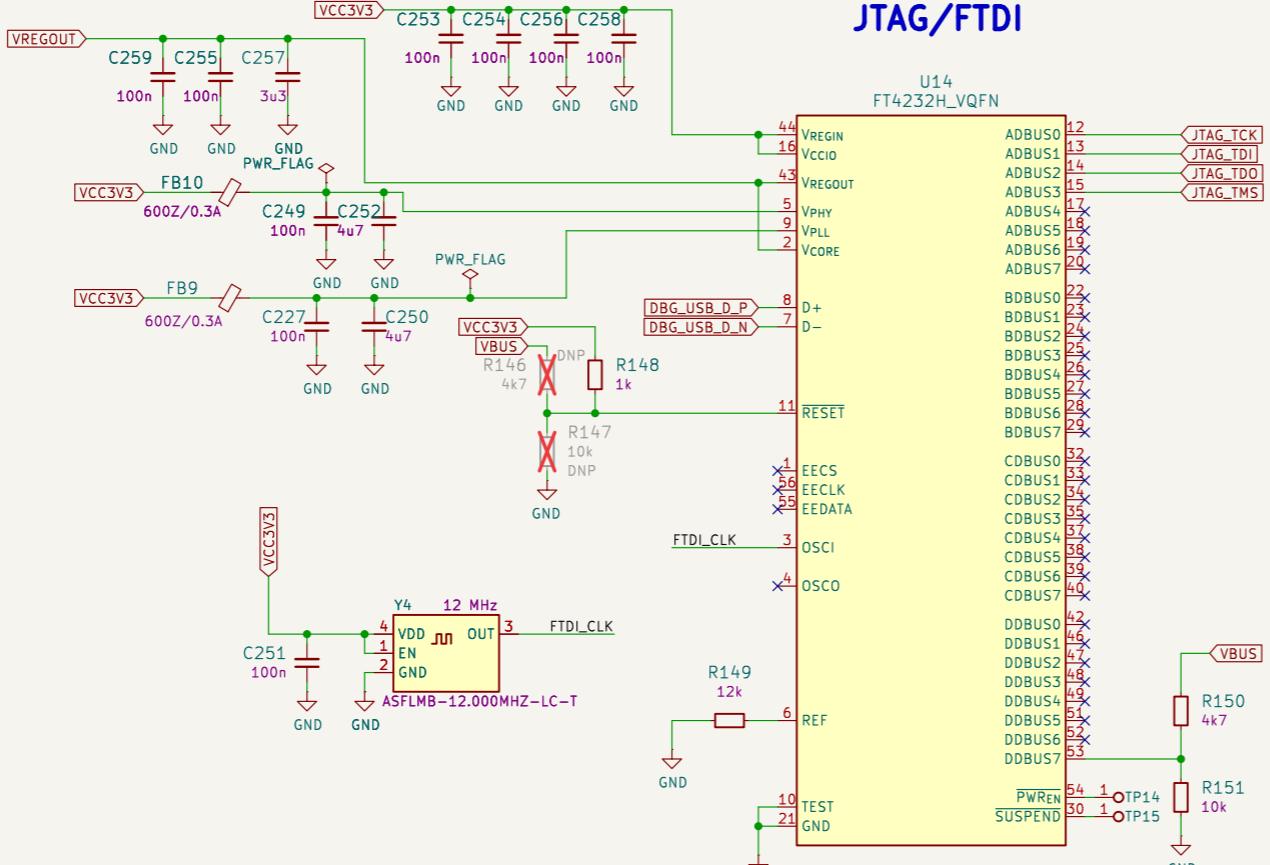
Size: A3 Date: 2024-07-15  
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.2.1:1fdb8  
Id: 7/10

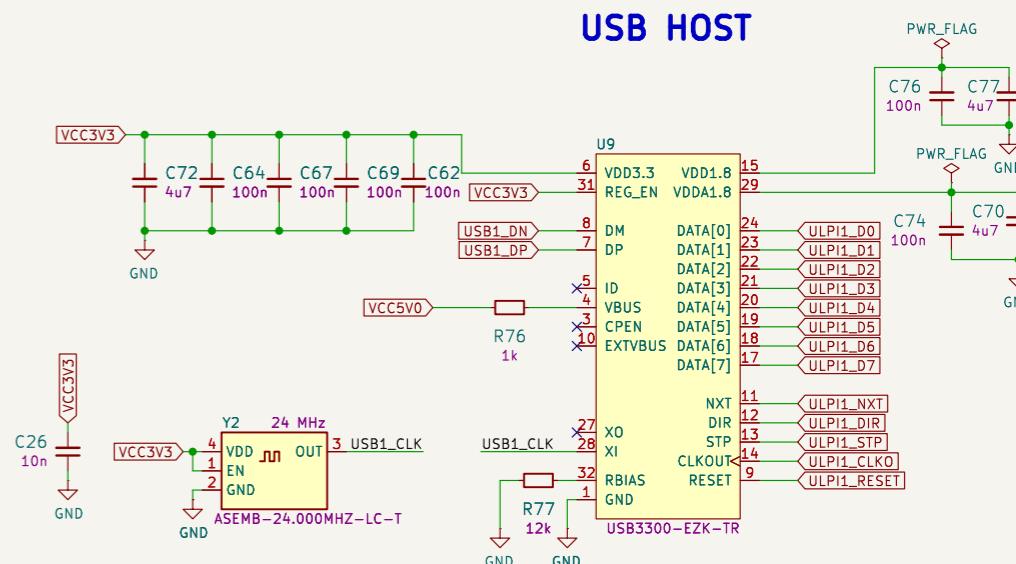
## Debug USB



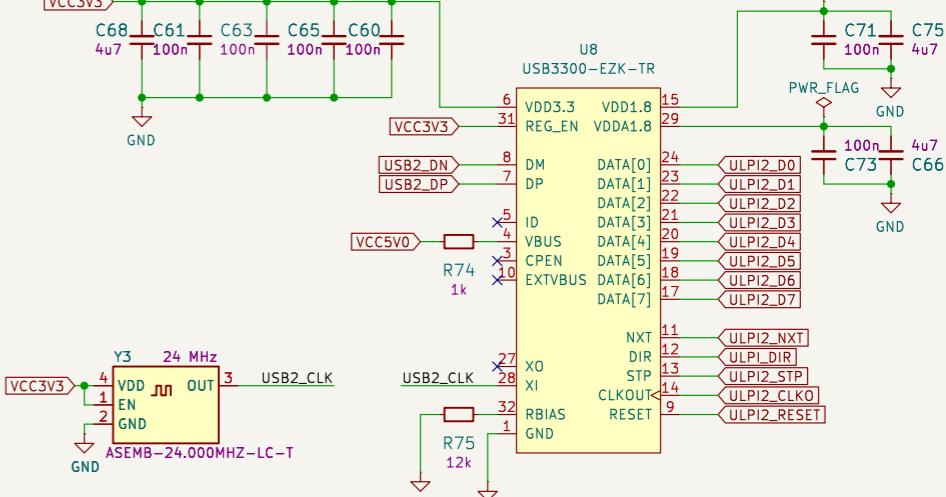
## JTAG/FTDI



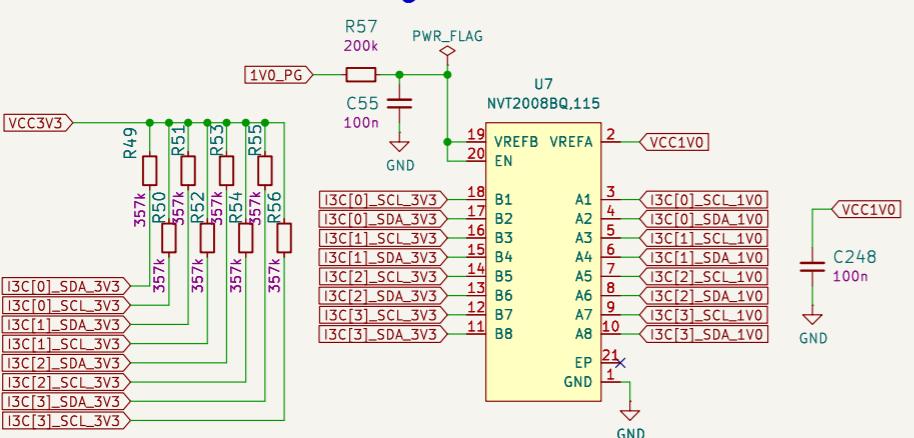
## USB HOST



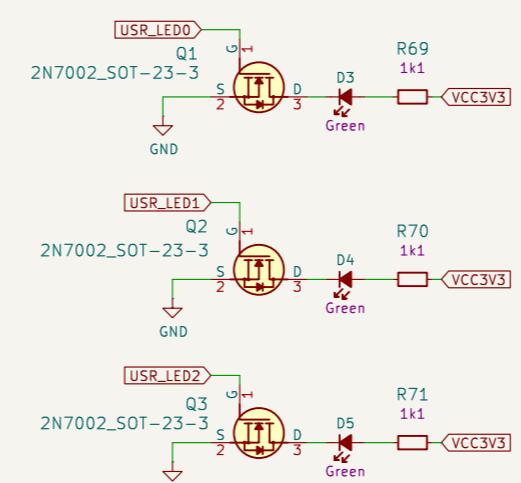
## USB Client



## I2C voltage level translation

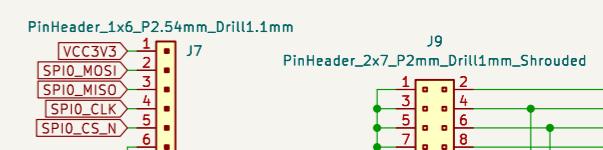


## USER LEDs

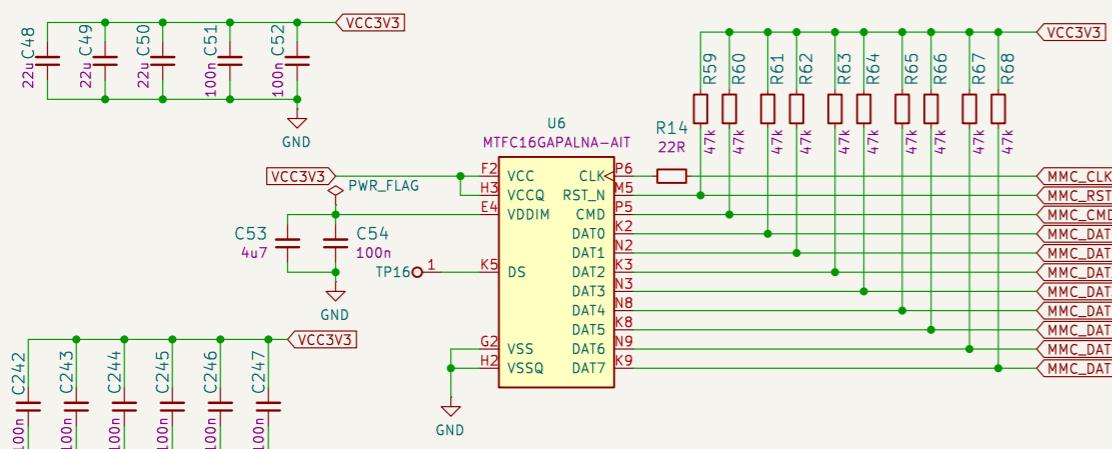
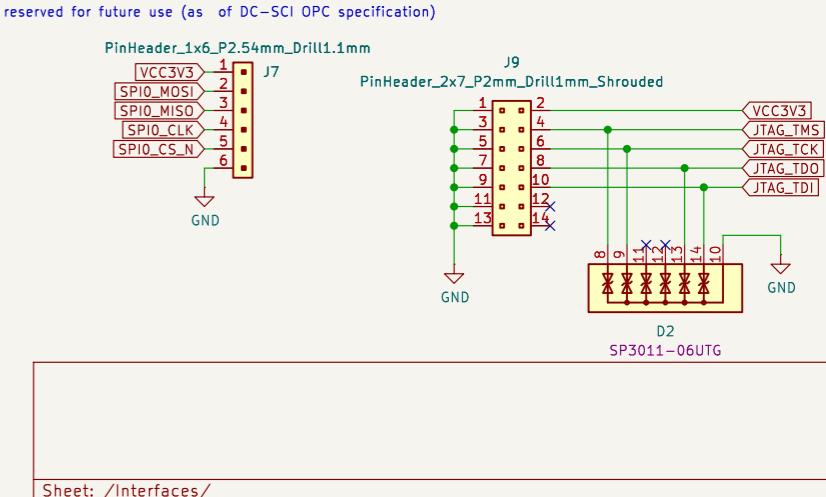


## HPM SPI

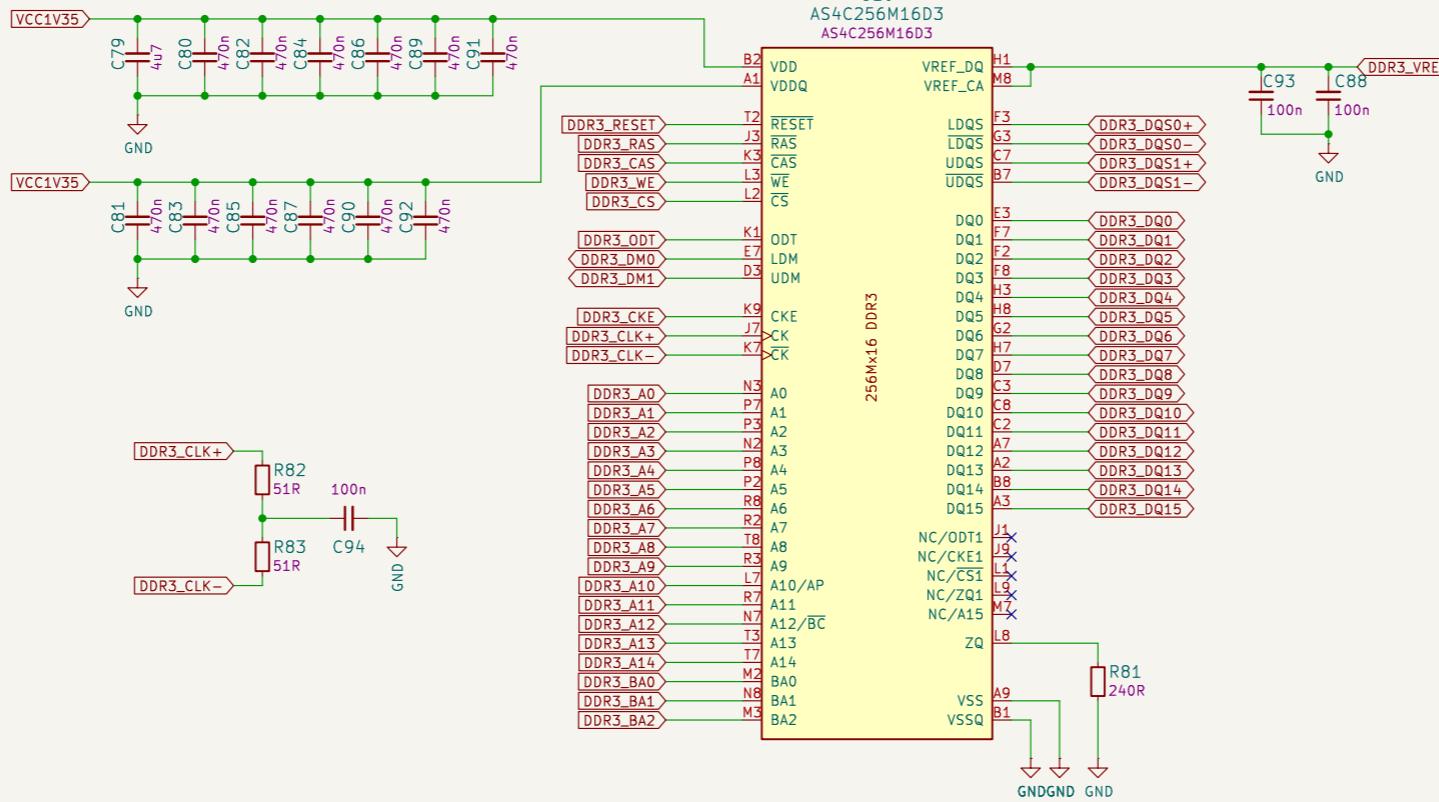
reserved for future use (as of DC-SCI OPC specification)



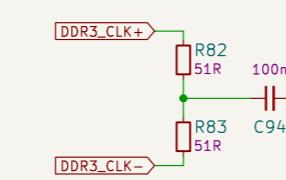
## JTAG connector



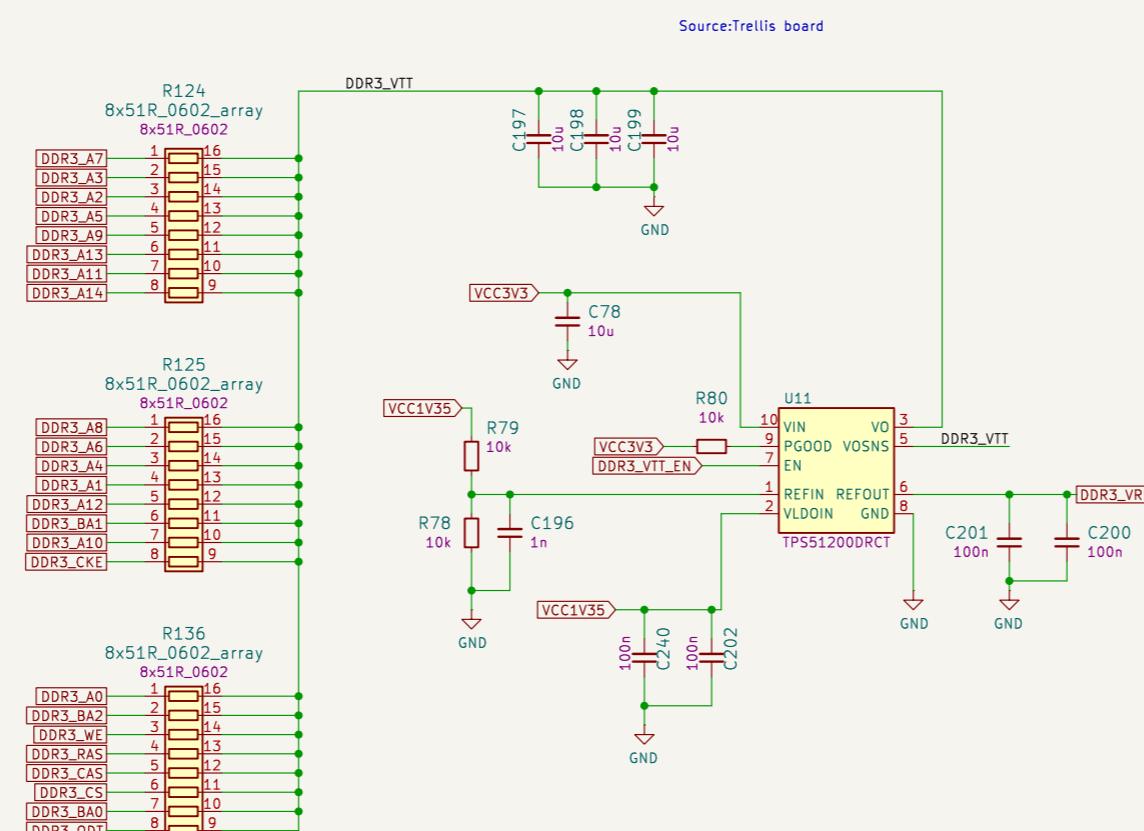
A



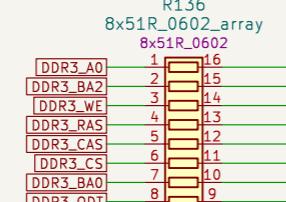
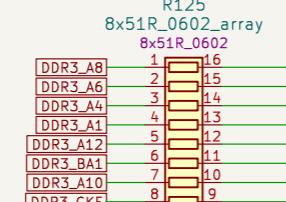
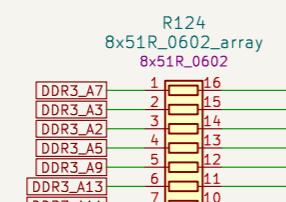
B



C



D



Sheet: /DDR3/  
 File: ddr3.kicad\_sch

**Title: ECP5 – Datacenter Secure Control Module (DC-SCM)**

Size: A3 Date: 2024-07-15  
 KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.2.1:1fdb8  
 Id: 9/10

