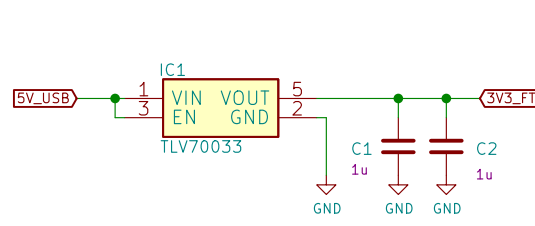
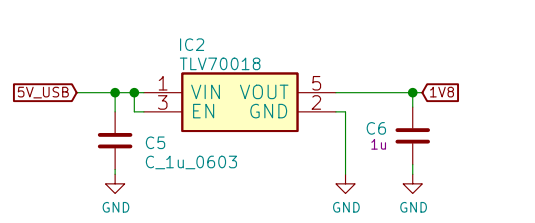


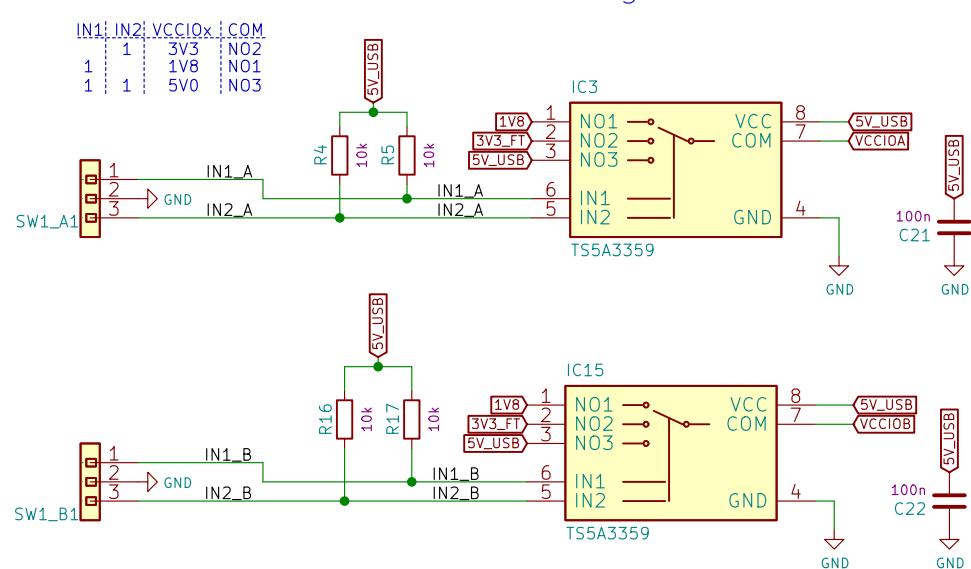
5V → 3.3V LDO 400mA



3V3 → 1.8V LDO 200mA

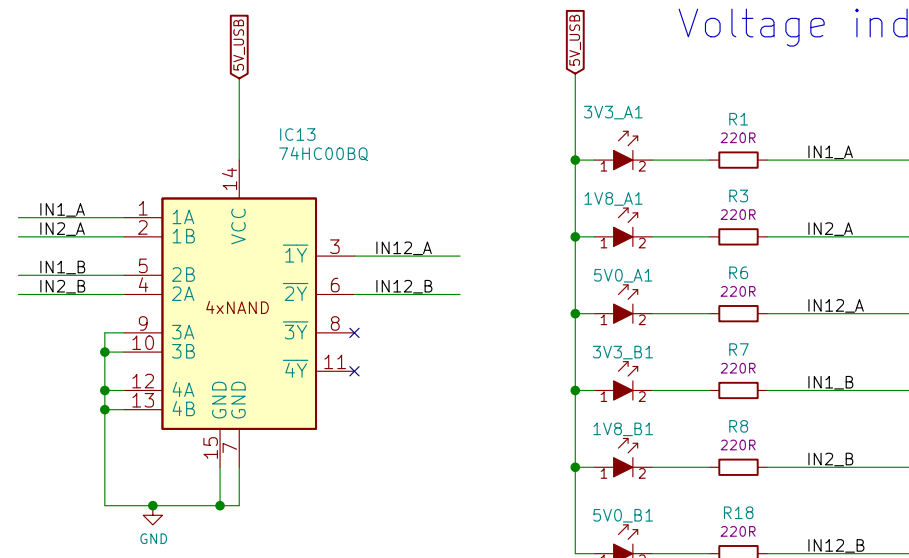


Voltage selection

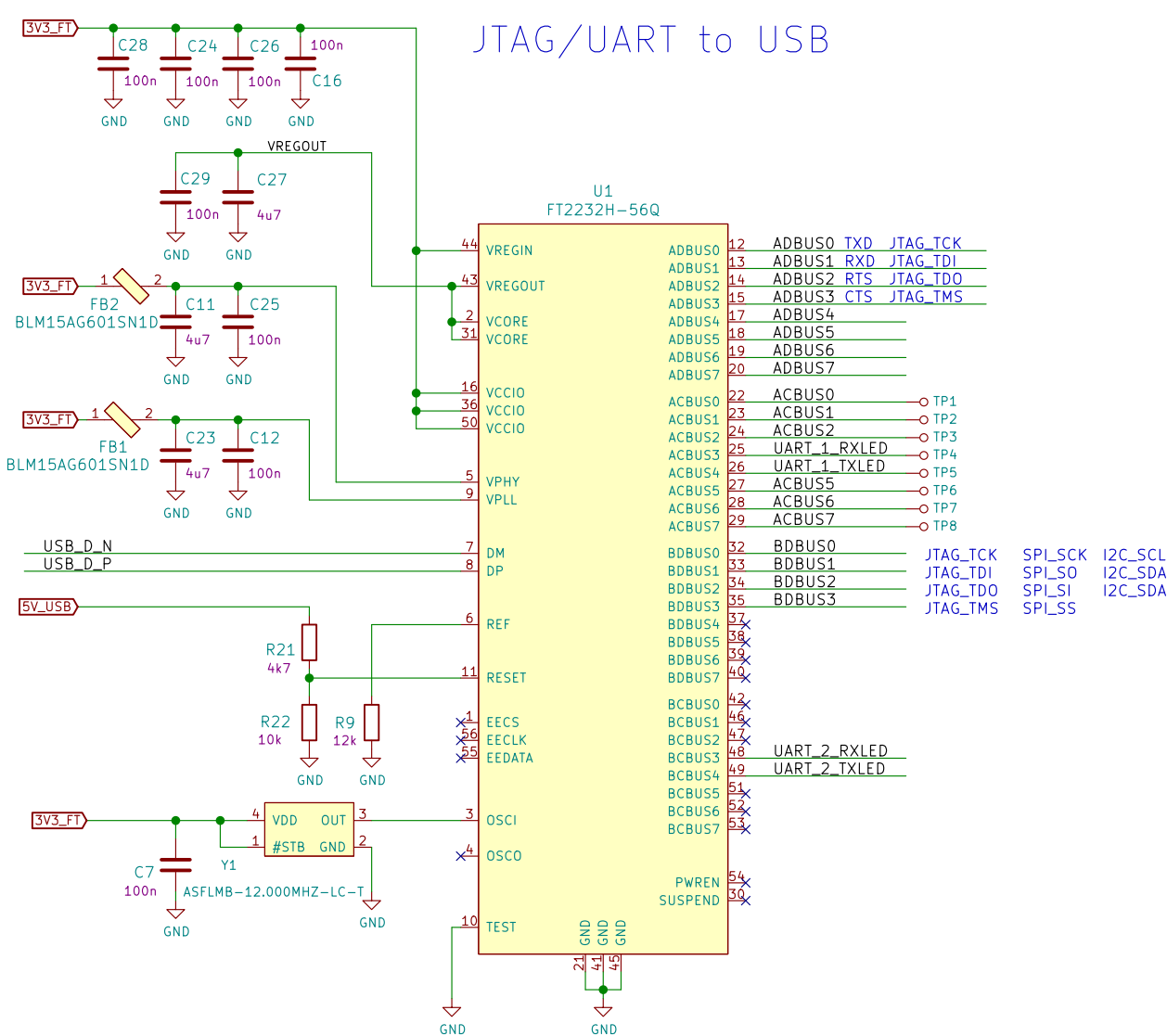


TPD6E05U06RVZR

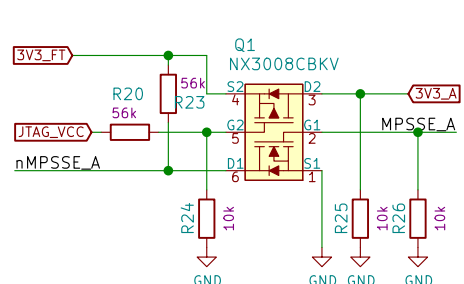
Voltage indicator



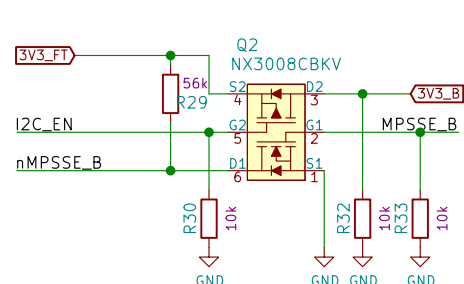
JTAG/UART to USB



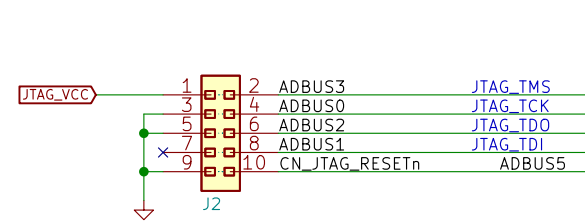
IFA buffer enable and direction



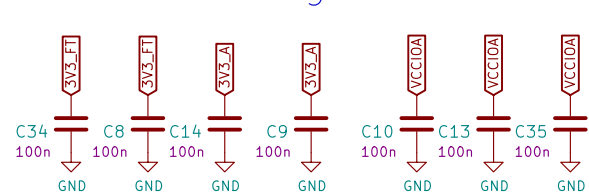
IFB buffer enable and direction



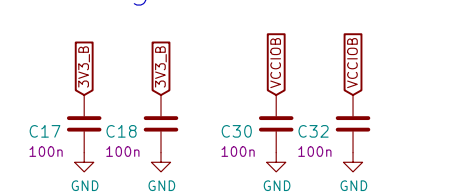
JTAG connector



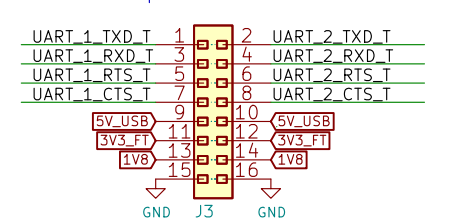
IFA voltage level translation



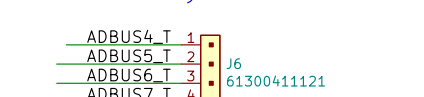
IFB voltage level translation



Goldpin connector



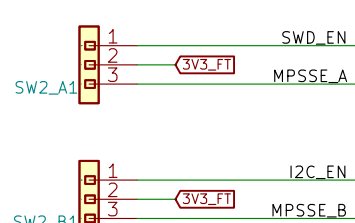
Auxillary connector



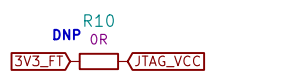
Pin function table

PIN	UART	JTAG	SPI	I2C	SWD
ADBUS0	TXD	JTAG_TCK	SPL_SCK	I2C_SCL	SW_CLK
ADBUS1	RXD	JTAG_TDI	SPL_MOSI	I2C_SDA(o)	SW_IO(o)
ADBUS2	RTS	JTAG_TDO	SPL_MISO	I2C_SDA(i)	SW_IO(i)
ADBUS3	CTS	JTAG_TMS	SPL_CS		
BDBUS0	TXD	JTAG_TCK	SPL_SCK	I2C_SCL	
BDBUS1	RXD	JTAG_TDI	SPL_MOSI	I2C_SDA(o)	
BDBUS2	RTS	JTAG_TDO	SPL_MISO	I2C_SDA(i)	
BDBUS3	CTS	JTAG_TMS	SPL_CS		

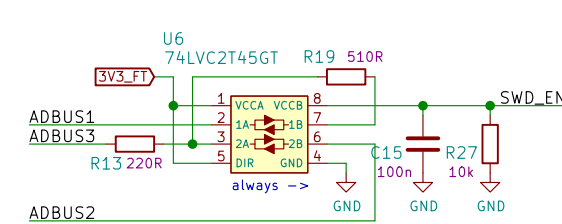
Mode selector



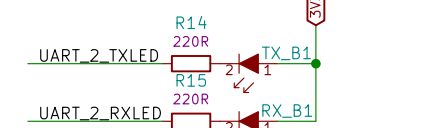
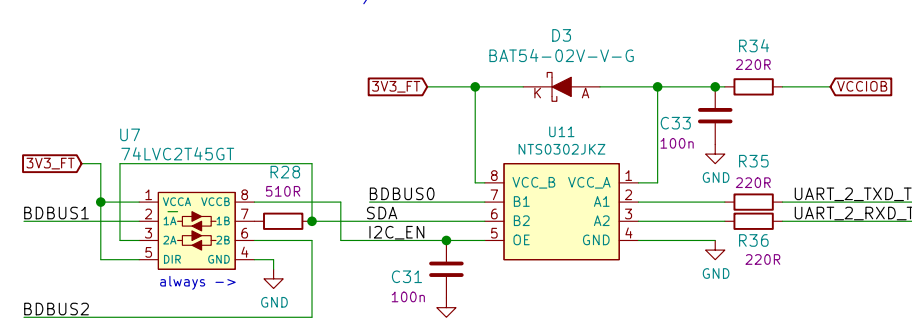
MPSSE mode = JTAG/SPI, connector selected by JTAG_VCC



IFA SWD mode switch



IFB I2C buffer w/ SDO+SDI switch



Logo_{N1} antmicro_logo Logo_{N2} oshw_logo

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File: FTDI-toolkit.sch

Title: FTDI Toolkit

Size: A2 Date: 2022-03-21
KiCad E.D.A. eeschema 5.1.9+dfsg1-1

Rev: 1.2.1

Id: 1/1