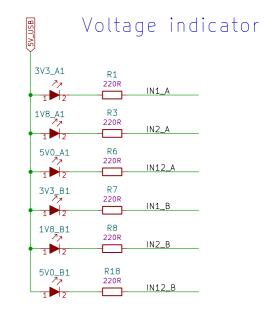
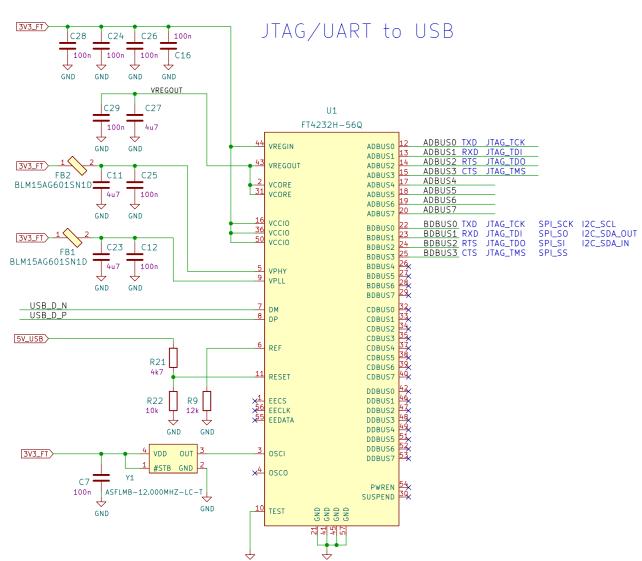


2 MC-HVT1-S02-G GND

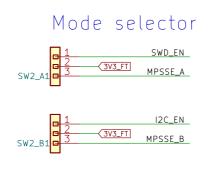


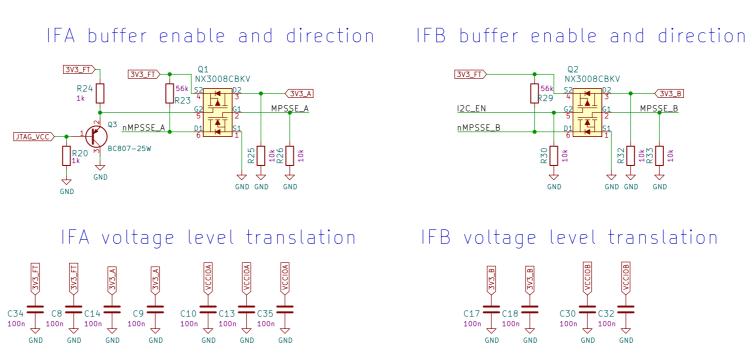


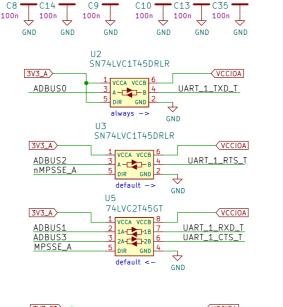


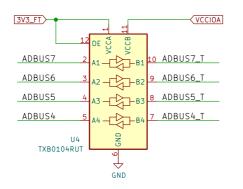
D.W.			CD!		C LID
PIN	UART	JTAG	SPI	I2C	SWD
ADBUS0	TXD	JTAG_TCK	SPI_SCK		SW_CLK
ADBUS1	RXD	JTAG_TDI	SPI_MOSI		SW_IO(o)
ADBUS2	RTS	JTAG_TDO	SPI_MISO		SW_IO(i)
ADBUS3	CTS	JTAG_TMS	SPI_CS		
BDBUS0	TXD	JTAG_TCK	SPI_SCK	I2C_SCL	
BDBUS1	RXD	JTAG_TDI	SPI_MOSI	I2C_SDA(o)	
BDBUS2	RTS	JTAG_TDO	SPI_MISO	I2C_SDA(i)	
BDBUS3	CTS	JTAG_TMS	SPI_CS		

MPSSE mode = JTAG/SPI, connector selected by JTAG_VCC



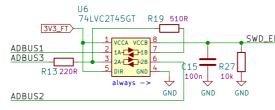


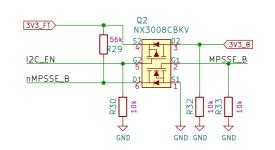




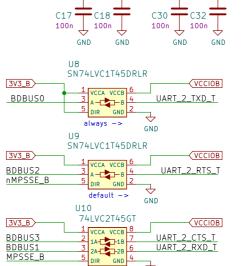
3V3_FT

IFA SWD mode switch

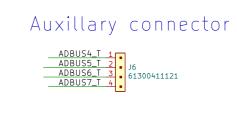




IFB voltage level translation



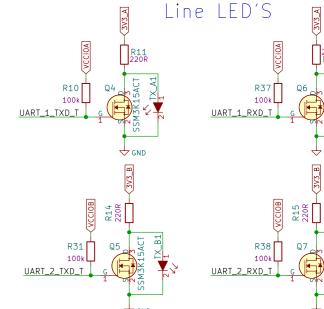






JTAG connector





SWD	buffered	mode	switch

IFB 12C buffer w/ SDO+SDI switch

D3 BAT54-02V-V-G

