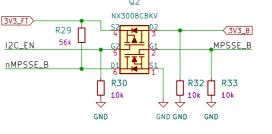
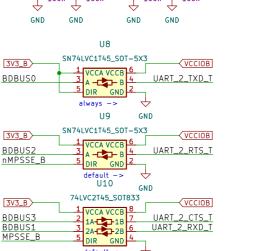


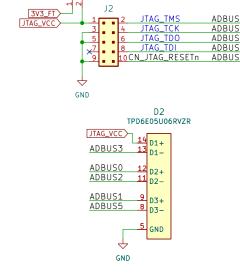


Voltage selection



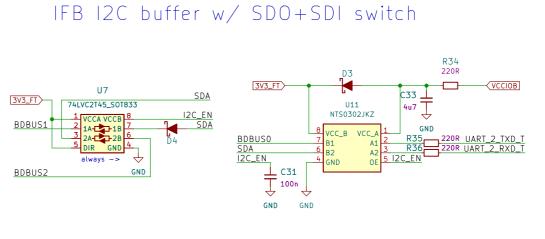
IFB voltage level translation

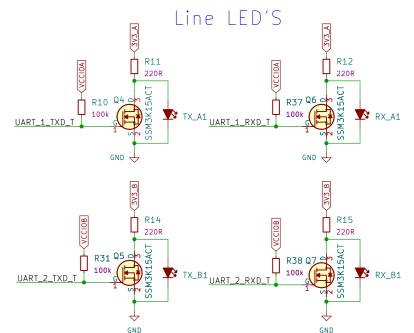


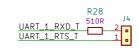


JTAG connector



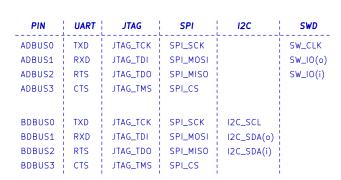


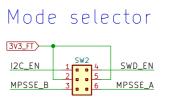




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Pin function table





MPSSE mode = JTAG/SPI, connector selected by JTAG_VCC



