

This is a detailed schematic diagram for the **zGlue Demo Board**, designed for an Antmicro KiCad 5.0.2 environment. The board is based on the FT4232HQ (U6) and includes various functional blocks and connectors.

### Functional Blocks and Components

- Auxiliary power supply entry:** Features a 5V0\_IN input (J2, DF13A-2P-1.25H) and a 5V0 power path using MBR0540T1G diodes (D3, D4, D7) to provide VUSB and 5V0\_DBG\_USB.
- Voltage level translation:** Utilizes NTB0104BQ (U2, U3, U4) level shifters to interface the 3V3\_SYS with the FT4232HQ's VCCIO1, VCCIO2, and VCCIO3 pins.
- Baseboard connectors:** Includes J6 (556500488) for CSI input/output and J7 (556500488) for CSI output. It also features J8 (WE\_686115148922) for CSI input and J9 (WE\_686115148922) for CSI output.
- Flash Programming FTDI:** Uses an LD1117S33CTR (U7) LDO for the FTDI's 3V3\_FTDI supply. The FTDI (U6) is connected to the board's 3V3\_FTDI and 1V8\_FTDI pins.
- Flash Programming and debug USB connector:** Features J10 (0473460001) and J5 (0473460001) connectors for USB debugging and programming.
- FPGA USB:** Includes J5 (0473460001) for USB debugging and programming.
- JTAG interface:** Utilizes a 20021121-00010C4LF (J3) JTAG interface for debugging.
- Other components:** Includes various capacitors (C1-C26), resistors (R1-R25), and a 93LCS6CT-IMC (U5) for the 3V3\_SYS supply.

### Connectors and Pin Headers

- RPi connector (J4):** A 2x20 Header\_Female connector for Raspberry Pi compatibility.
- CSI input (J8):** WE\_686115148922 connector for CSI input.
- CSI output (J9):** WE\_686115148922 connector for CSI output.

### Notes and TODOs

- TODO: add LEDs
- TODO: determine what to do with I2C
- TODO: check pin order, check i2c connection
- TODO: check pin order
- TODO: no Done signal
- TODO: check pin order

### Metadata

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Size: A3 Date: Rev: 1.0  
KiCad E.D.A. kicad 5.0.2+dfsg1-1bp09+1 Id: 1/1

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- Flash Programming FTDI:** Uses an LD1117S33CTR (U7) LDO to provide 3V3\_FTDI from the 5V0\_DBG\_USB input. It also includes a 5V0\_DBG\_USB input (J10, 0473460001) and a 5V0\_USB input (J5, 0473460001).
- Flash Programming and debug USB connector:** Features a TPD2EUSB30DRTR (D5) and a TPD2EUSB30DRTR (D2) for USB-to-UART conversion.
- FPGA USB:** Includes a TPD2EUSB30DRTR (D2) for USB-to-UART conversion.
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- Other components:** Includes various resistors (R1-R30), capacitors (C1-C26), and a 93LCS6CT-IMC (U5) for signal conditioning.

### Connectors and Pin Headers

- RPi connector (J4):** A 2x20 Header\_Female connector for Raspberry Pi compatibility.
- CSI input (J8):** A 15-pin connector for CSI input signals.
- CSI output (J9):** A 15-pin connector for CSI output signals.

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### Bill of Materials (BOM)

Ref	Part	Value	Notes
J2	DF13A-2P-1.25H		5V0_IN
D3, D4, D7	MBR0540T1G		Diodes
U2, U3, U4	NTB0104BQ		Level shifters
U6	FT4232HQ		FTDI chip
U7	LD1117S33CTR		LDO
U5	93LCS6CT-IMC		Signal conditioner
J6	556500488		CSI input
J7	556500488		CSI output
J8	WE_686115148922		CSI input
J9	WE_686115148922		CSI output
J10	0473460001		5V0_DBG_USB
J5	0473460001		5V0_USB
D5, D2	TPD2EUSB30DRTR		USB-to-UART
J3	20021121-00010C4LF		JTAG interface

**Auxiliary power supply entry**

5V0\_IN (1) → J2 (DF13A-2P-1.25H) → GND (2)

**5V0 power path**

5V0\_IN → D3 (MBR0540T1G) → D4 (MBR0540T1G) → VUSB

5V0\_USB → D7 (MBR0540T1G) → 5V0\_DBG\_USB

**RPi connector**

J4 (2x20\_Header\_Female) → R1 (0R) → 3V3\_SYS

**Voltage level translation**

VCCIO1 → U2 (NTB0104BQ) → 3V3\_SYS

VCCIO2 → U3 (NTB0104BQ) → 3V3\_SYS

VCCIO3 → U4 (NTB0104BQ) → 3V3\_SYS

**Baseboard connectors**

J6 (556500488) → CSI\_CLK\_IN\_P, CSI\_CLK\_IN\_N, CSI\_D1\_IN\_P, CSI\_D1\_IN\_N, CSI\_D2\_IN\_P, CSI\_D2\_IN\_N, CSI\_D3\_IN\_P, CSI\_D3\_IN\_N, CSI\_D4\_IN\_P, CSI\_D4\_IN\_N, USB\_D\_P, USB\_D\_N, VUSB, VUSB

J7 (556500488) → E\_PROC\_RST, E\_PROC\_SI, E\_PROC\_SO, E\_PROC\_SCK, E\_PROC\_SS, CTRL\_RST\_FLASH\_SOC\_CS, E\_CTRL\_SI, E\_CTRL\_SO, E\_CTRL\_SCK, E\_CTRL\_SS, DONE, FLASH\_CLK, FLASH\_CS, FLASH\_IO0, FLASH\_IO1, FLASH\_IO2, FLASH\_IO3

**Flash Programming FTDI**

U6 (FT4232HQ) → VREGIN, VREGOUT, VCORE, VPHY, VPLL, DM, DP, REF, RESET, EECS, EECLK, EEDATA, OSCI, OSCO, TEST, PWREN, SUSPEND

**Flash Programming and debug USB connector**

J10 (0473460001) → 5V0\_DBG\_USB, PROG\_USB\_D\_N, PROG\_USB\_D\_P

J5 (0473460001) → 5V0\_USB, USB\_D\_N, USB\_D\_P

**JTAG interface**

J3 (20021121-00010C4LF) → TMS, TCK, TDO, TDI

**CSI input**

J8 (WE\_686115148922) → CSI\_D1\_IN\_N, CSI\_D1\_IN\_P, CSI\_D2\_IN\_N, CSI\_D2\_IN\_P, CSI\_CLK\_IN\_N, CSI\_CLK\_IN\_P, CAM\_GPIO, CAM\_CLK, CAM\_SCL, CAM\_SDA, 3V3\_SYS

**CSI output**

J9 (WE\_686115148922) → CSI\_D1\_OUT\_N, CSI\_D1\_OUT\_P, CSI\_D2\_OUT\_N, CSI\_D2\_OUT\_P, CSI\_CLK\_OUT\_N, CSI\_CLK\_OUT\_P, CAM\_GPIO, CAM\_CLK, CAM\_SCL, CAM\_SDA, 3V3\_SYS

**Footer**

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J5	0473460001		5V0_USB
D5, D2	TPD2EUSB30DRTR		USB-to-UART
J3	20021121-00010C4LF		JTAG interface

This is a detailed schematic diagram for the **zGlue Demo Board**, designed for an Antmicro KiCad 5.0.2+dfsg1-1bp09+1 environment. The board is based on the FT4232HQ (U6) and includes various interfaces and components.

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- RPi connector:** A 2x20 Header\_Female (J4) for Raspberry Pi compatibility, with 3V3\_SYS and 5V0\_IN connections.
- Flash Programming FTDI:** Uses an FT4232HQ (U6) for FTDI programming, with VREGIN, VREGOUT, and VPHY connections.
- LDO for FTDI:** A voltage regulator (U7, LD1117S33CTR) providing 3V3\_FTDI from 5V0\_DBG\_USB.
- Flash Programming and debug USB connector:** Features J10 (0473460001) and J5 (0473460001) for USB connections, including 5V0\_DBG\_USB, PROG\_USB\_D\_N, and PROG\_USB\_D\_P.
- FPGA USB:** Includes J9 (WE\_686115148922) for USB connections, with 5V0\_USB, USB\_D\_N, and USB\_D\_P.
- JTAG interface:** Utilizes J3 (20021121-00010C4LF) for JTAG, with TMS, TCK, TDO, and TDI connections.
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The schematic is organized into a grid with columns 1-8 and rows A-F. It includes a title block at the bottom right with the following information:

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### Auxiliary power supply entry

The auxiliary power supply entry (J2) is a 5V0\_IN (1.25H) connector. It provides power to the 5V0 power path, which includes diodes D3, D4, D7, and MBR0540T1G, and capacitors C1, C3, C15, C18, C19, C23, C20, C24, C22, C26, C16, C17, and C25.

### 5V0 power path

The 5V0 power path is connected to the 5V0\_IN (1.25H) connector. It includes diodes D3, D4, D7, and MBR0540T1G, and capacitors C1, C3, C15, C18, C19, C23, C20, C24, C22, C26, C16, C17, and C25.

### RPi connector

The RPi connector (J4) is a 2x20 Header\_Female connector. It provides power to the 3V3\_SYS and 5V0\_IN lines, and is connected to the 5V0 power path.

### Voltage level translation

The voltage level translation section includes three 1V8 to 3V3 translators (U2, U3, U4) and their associated resistors (R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R2, R3).

### Baseboard connectors

The baseboard connectors include J6 (556500488) and J7 (556500488), which provide connections for various signals and power lines.

### Flash Programming FTDI

The Flash Programming FTDI section includes the FT4232HQ (U6) and its associated resistors (R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R2, R3).

### Flash Programming and debug USB connector

The Flash Programming and debug USB connector section includes the TPD2EUSB30DRTR (D5) and its associated resistors (R4, R6, R7, R10).

### FPGA USB

The FPGA USB section includes the TPD2EUSB30DRTR (D2) and its associated resistors (R4, R6, R7, R10).

### JTAG interface

The JTAG interface section includes the J3 (20021121-00010C4LF) and its associated resistors (R4, R6, R7, R10).

### CSI input

The CSI input section includes the CSI\_D1\_IN\_N, CSI\_D1\_IN\_P, CSI\_D2\_IN\_N, CSI\_D2\_IN\_P, CSI\_CLK\_IN\_N, CSI\_CLK\_IN\_P, CAM\_GPIO, CAM\_CLK, CAM\_SCL, CAM\_SDA, and 3V3\_SYS lines.

### CSI output

The CSI output section includes the CSI\_D1\_OUT\_N, CSI\_D1\_OUT\_P, CSI\_D2\_OUT\_N, CSI\_D2\_OUT\_P, CSI\_CLK\_OUT\_N, CSI\_CLK\_OUT\_P, CAM\_GPIO, CAM\_CLK, CAM\_SCL, CAM\_SDA, and 3V3\_SYS lines.

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