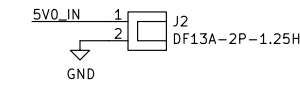
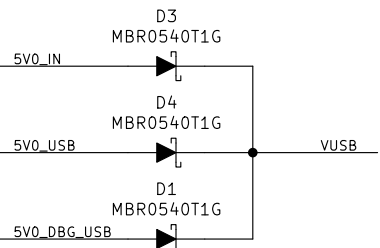


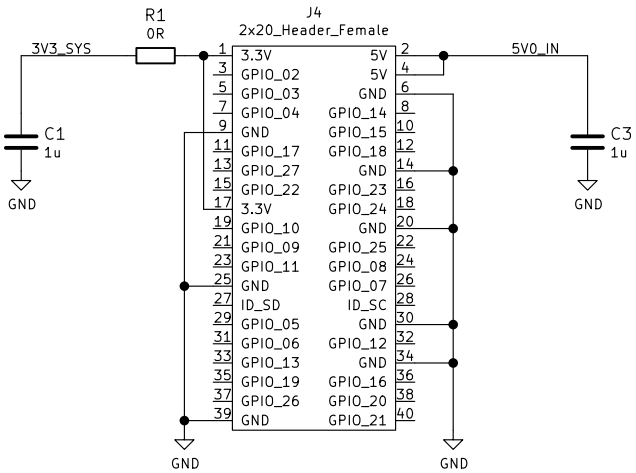
Auxiliary power supply entry



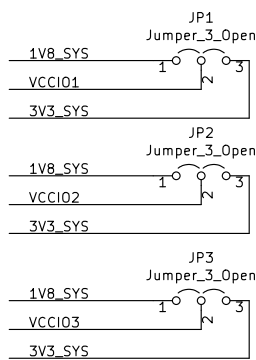
5V0 power path



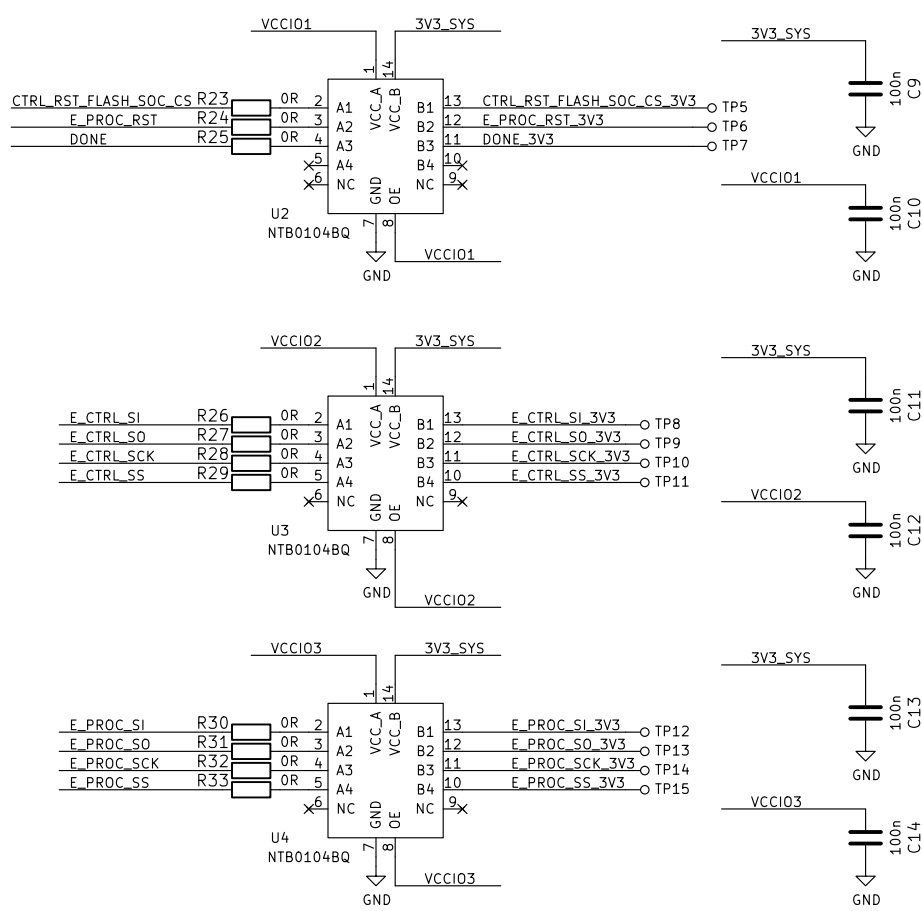
RPi connector



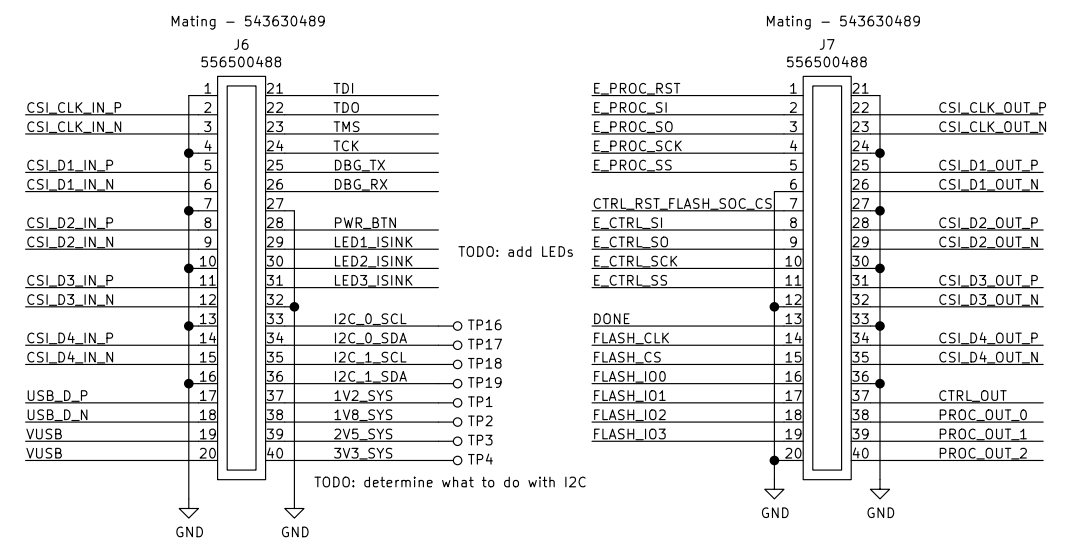
Voltage level selection



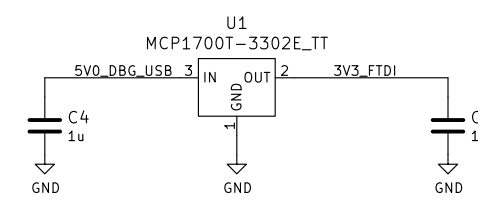
Voltage level translation



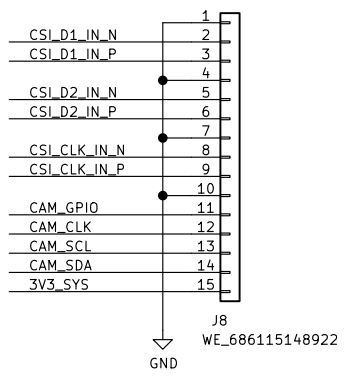
Baseboard connectors



LDO for FTDI

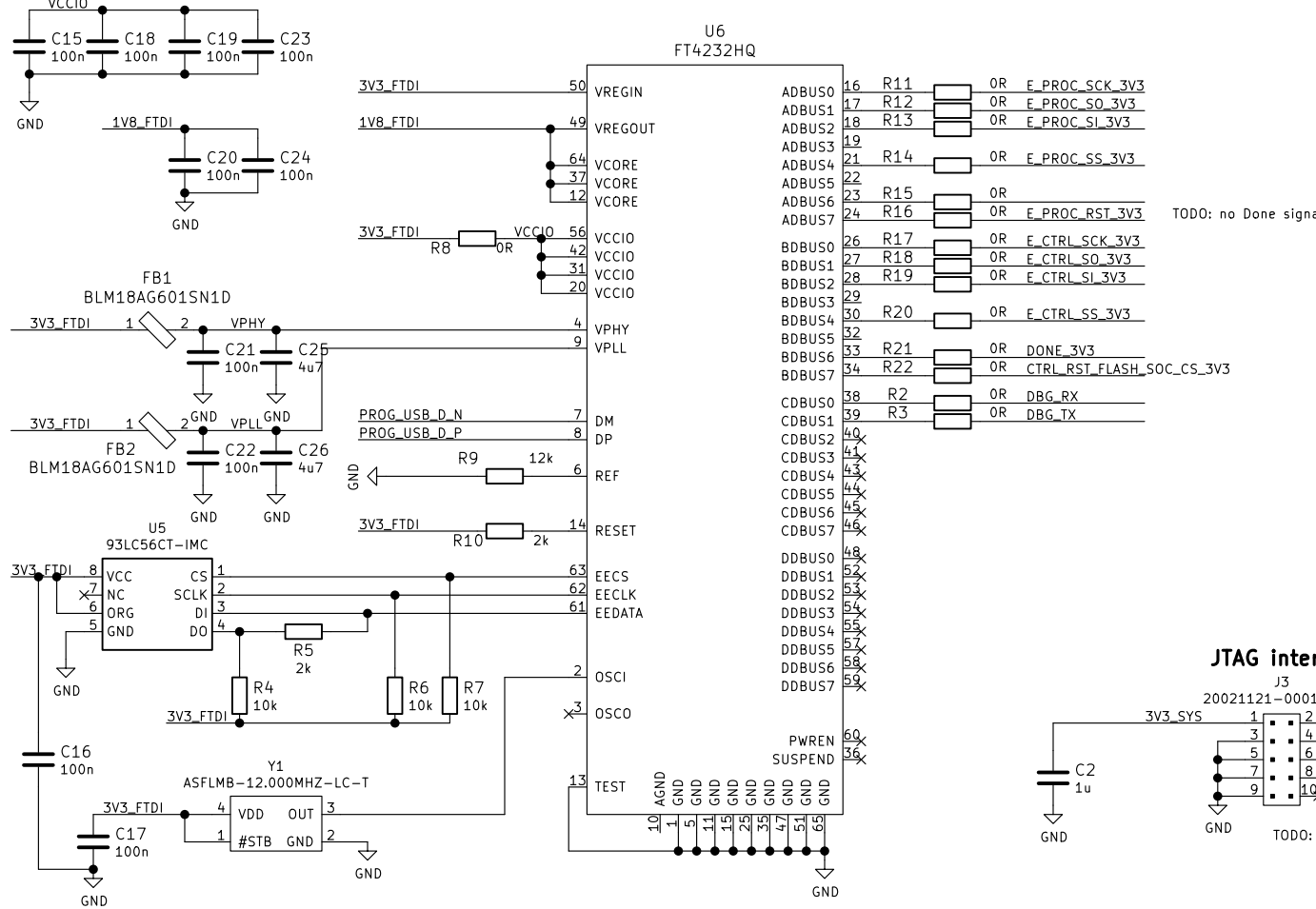


CSI input

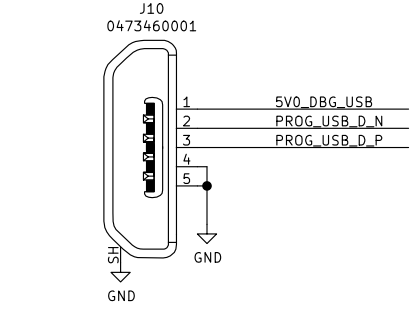


TODO: check i2c connection

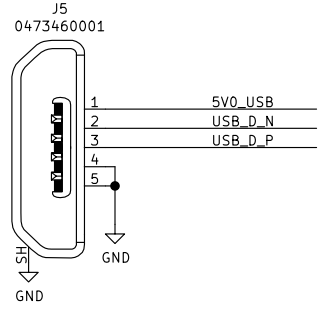
Flash Programming and debug FTDI



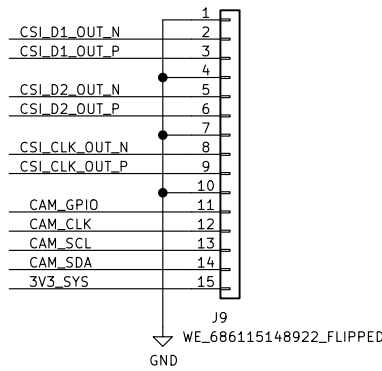
Flash Programming and debug USB connector



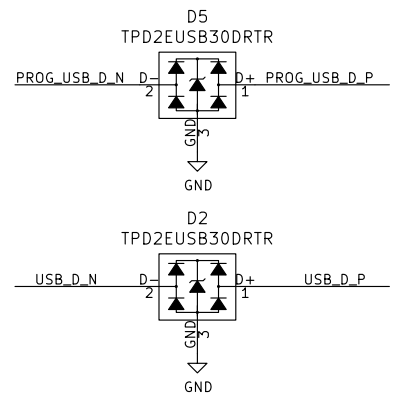
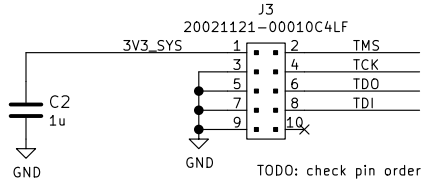
FPGA USB



CSI output



JTAG interface



antmicro

www.antmicro.com

Antmicro

Sheet: /

File: zglue-demo-board.sch

Title: zGlue Demo Board

Size: A3

Date:

Rev: 1.0

KiCad E.D.A. kicad 5.0.2+dfsg1-1bpo9+1

Id: 1/1