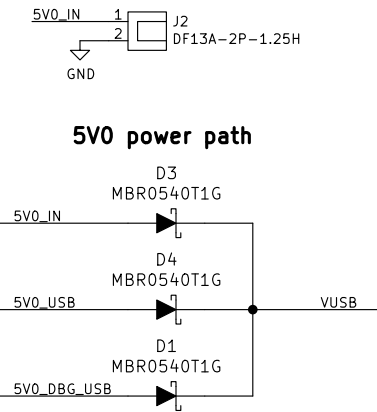
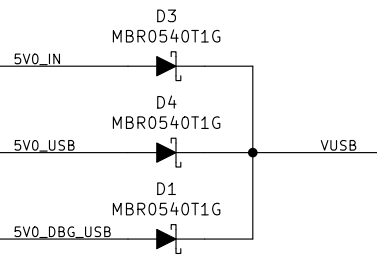


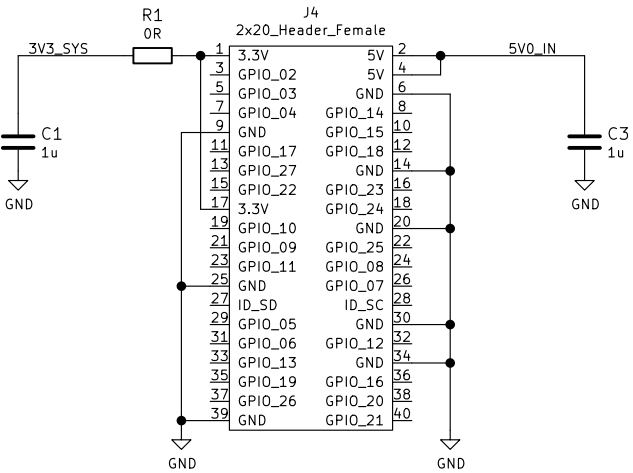
Auxiliary power supply entry



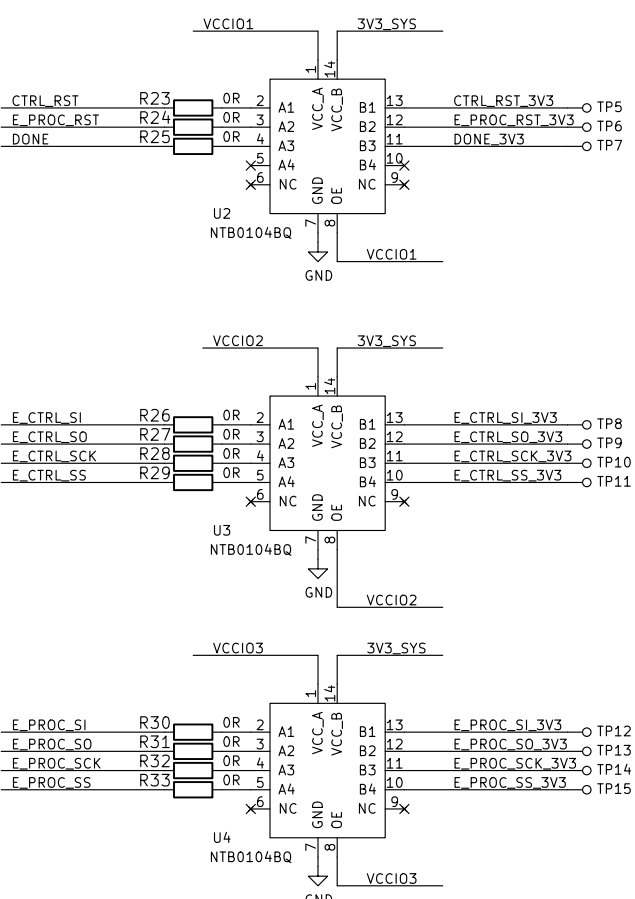
5V0 power path



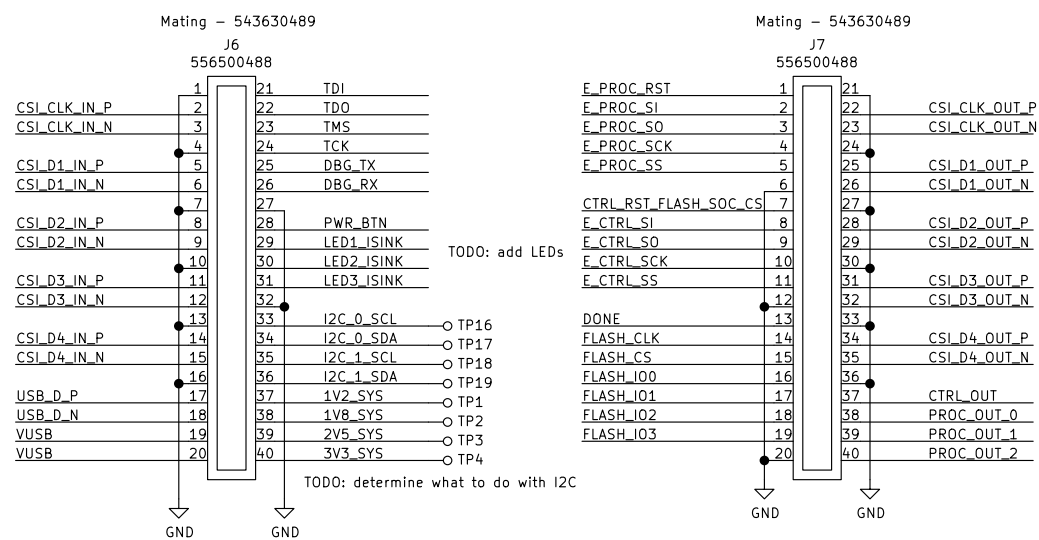
RPi connector



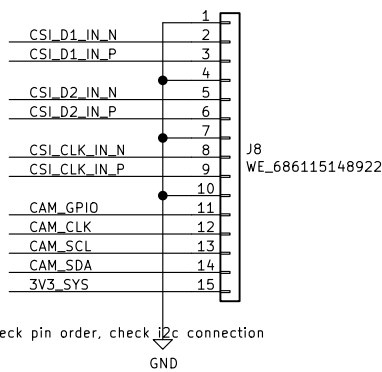
Voltage level translation



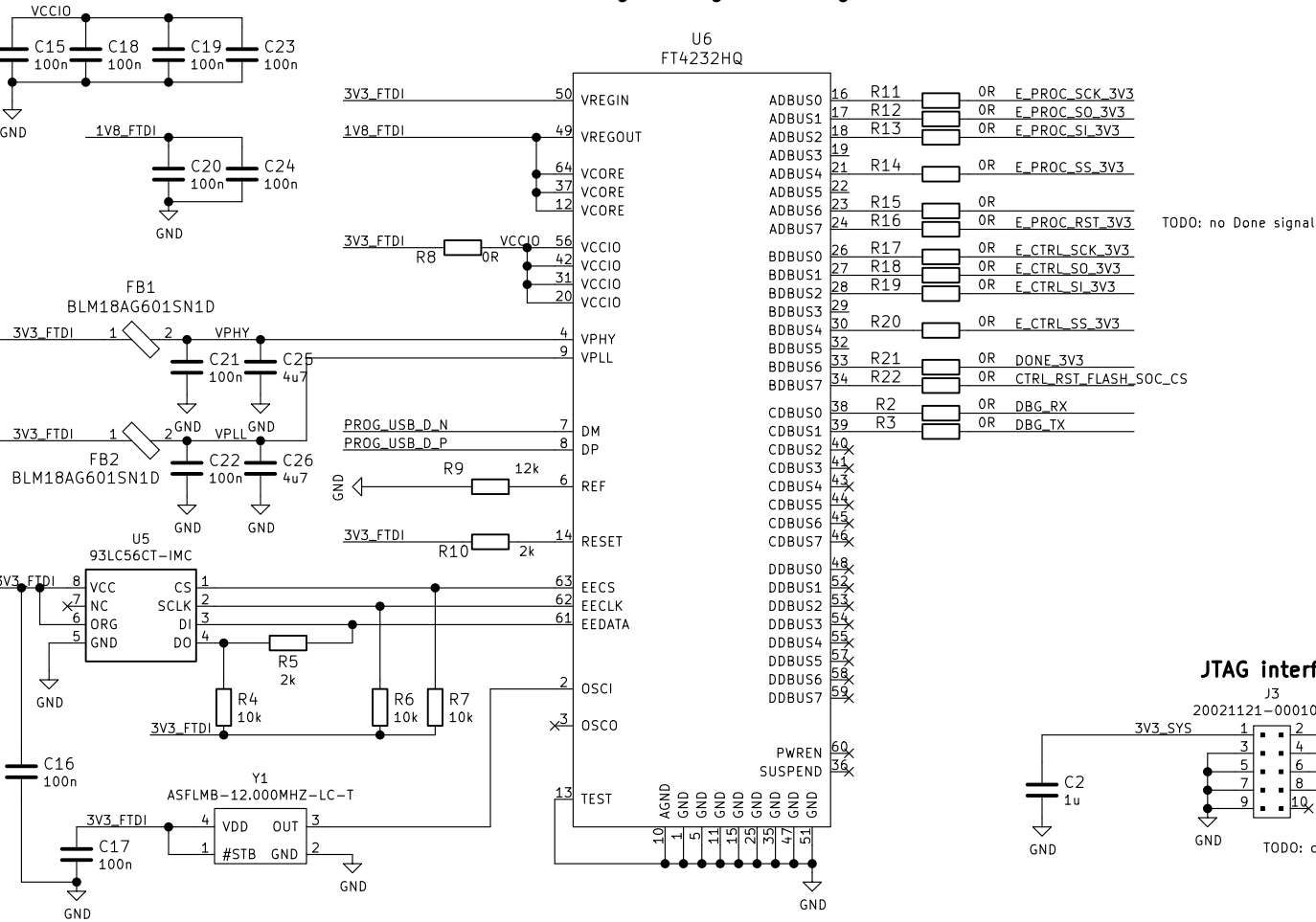
Baseboard connectors



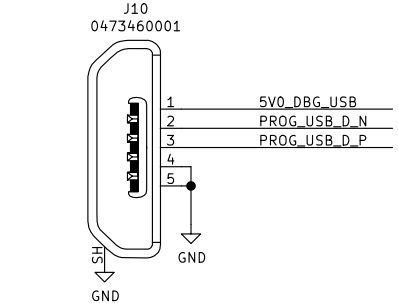
CSI input



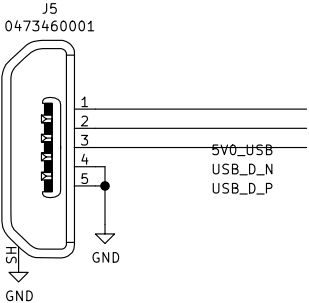
Flash Programming and debug FTDI



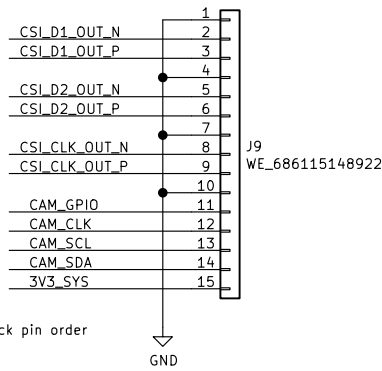
Flash Programming and debug USB connector



FPGA USB



CSI output



JTAG interface

