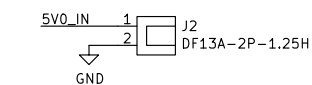
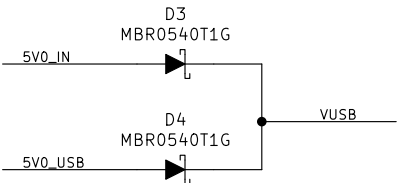


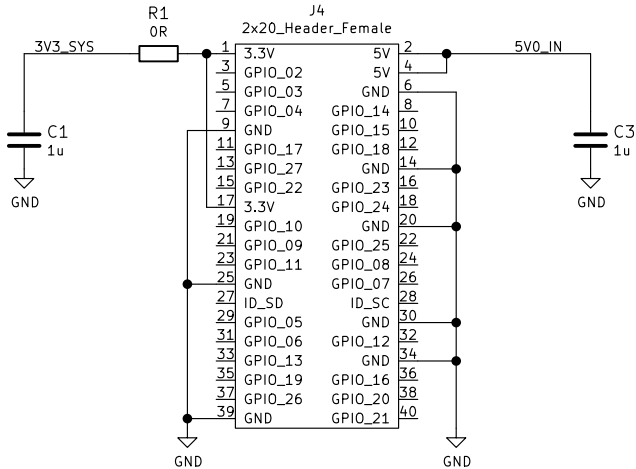
Auxiliary power supply entry



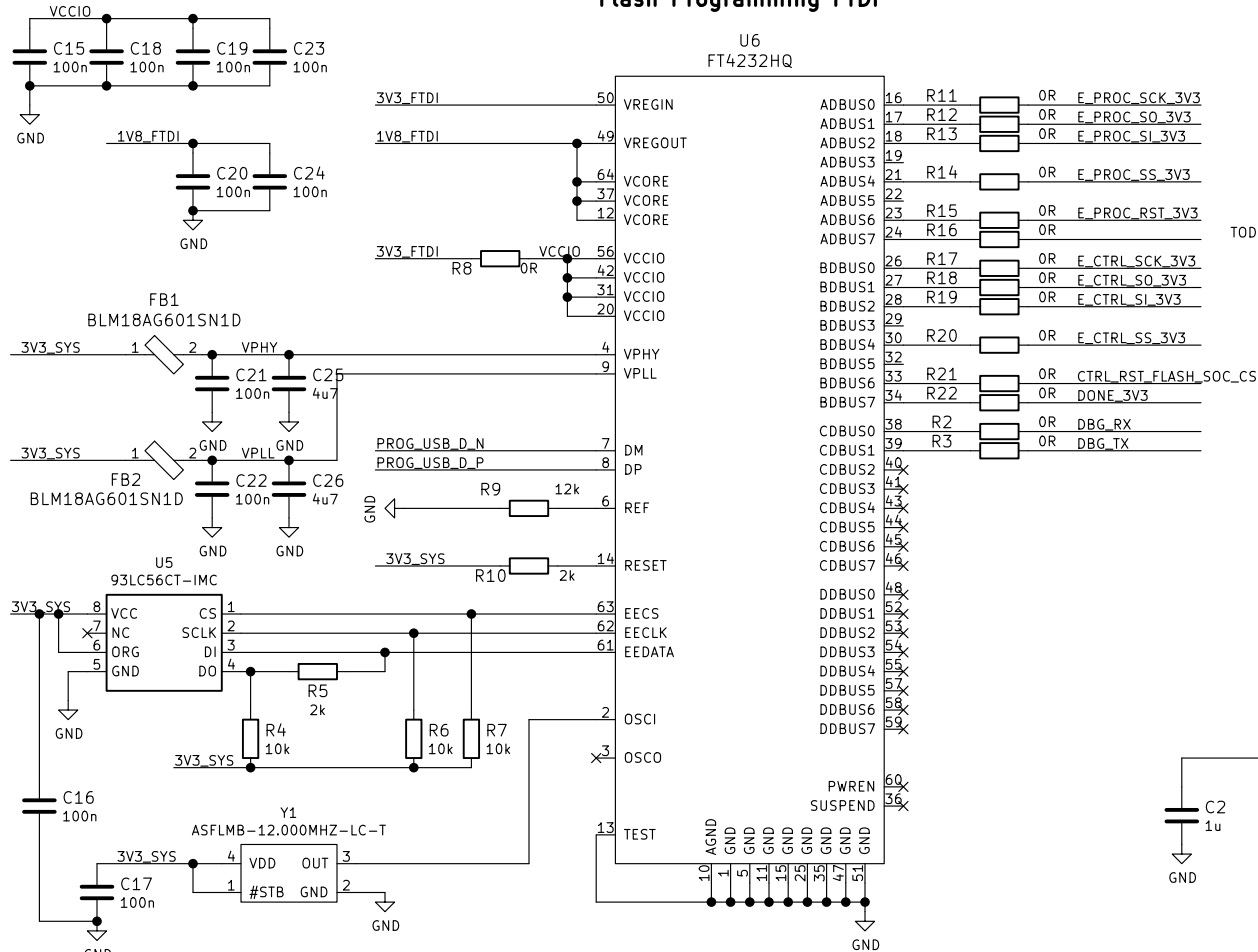
5V0 power path



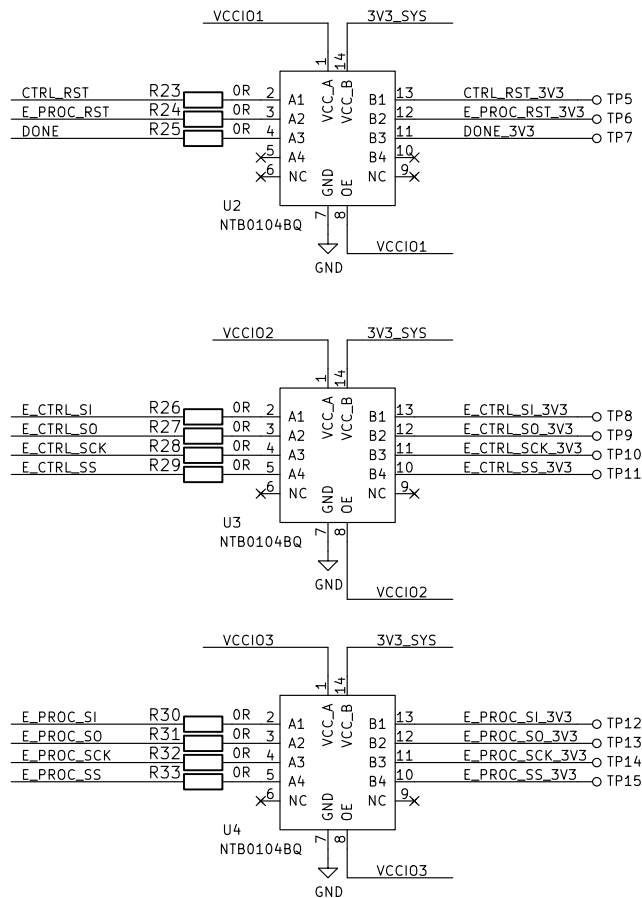
RPi connector



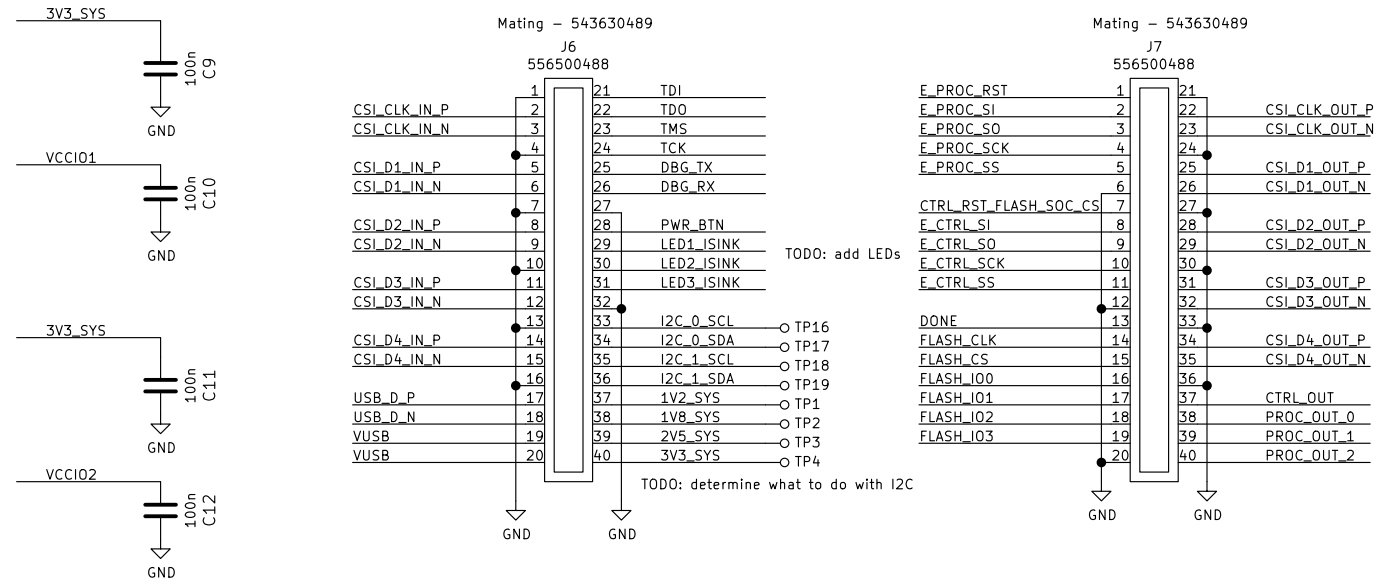
Flash Programming FTDI



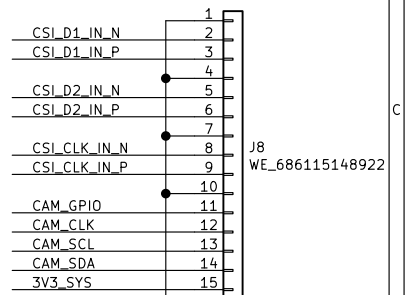
Voltage level translation



Baseboard connectors

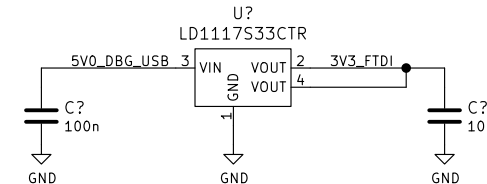


CSI input

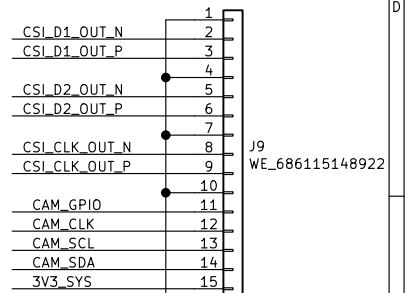


TODO: check pin order, check I2C connection

LDO for FTDI

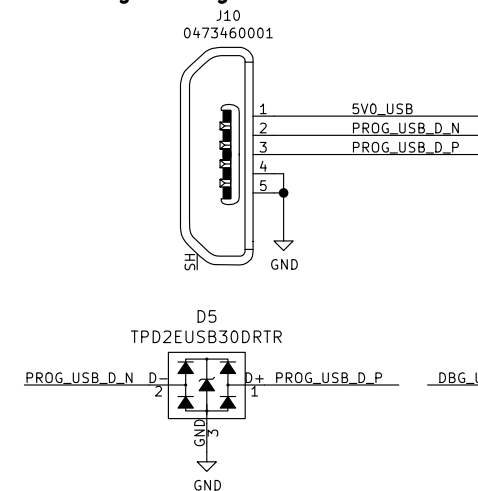


CSI output

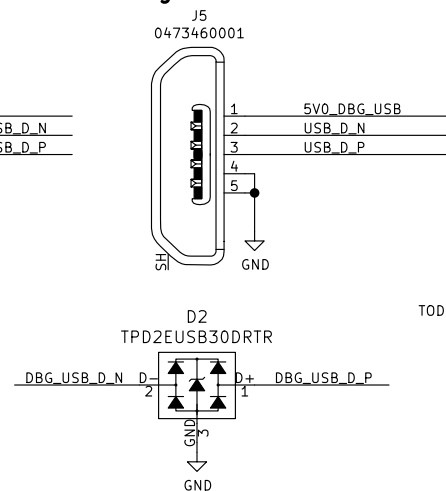


TODO: check pin order

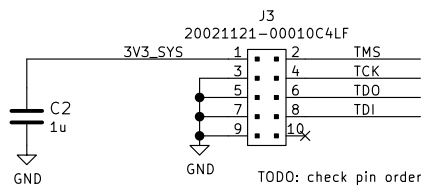
Flash Programming USB connector



Debug USB console



JTAG interface



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Sheet: /

File: zglue-demo-board.sch

Title: zGlue Demo Board

Size: A3

Date:

Rev: 1.0

KiCad E.D.A. kicad 5.0.2+dfsg1-1bpo9+1

Id: 1/1