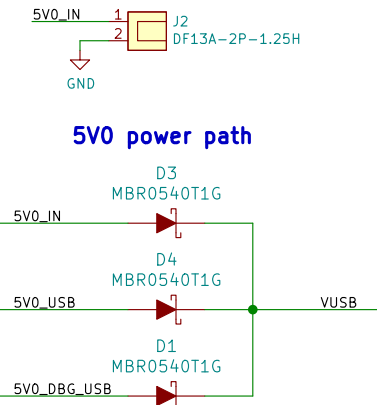
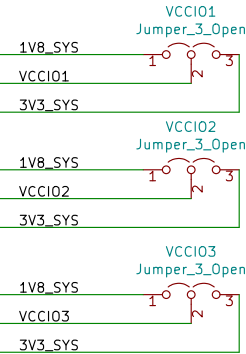


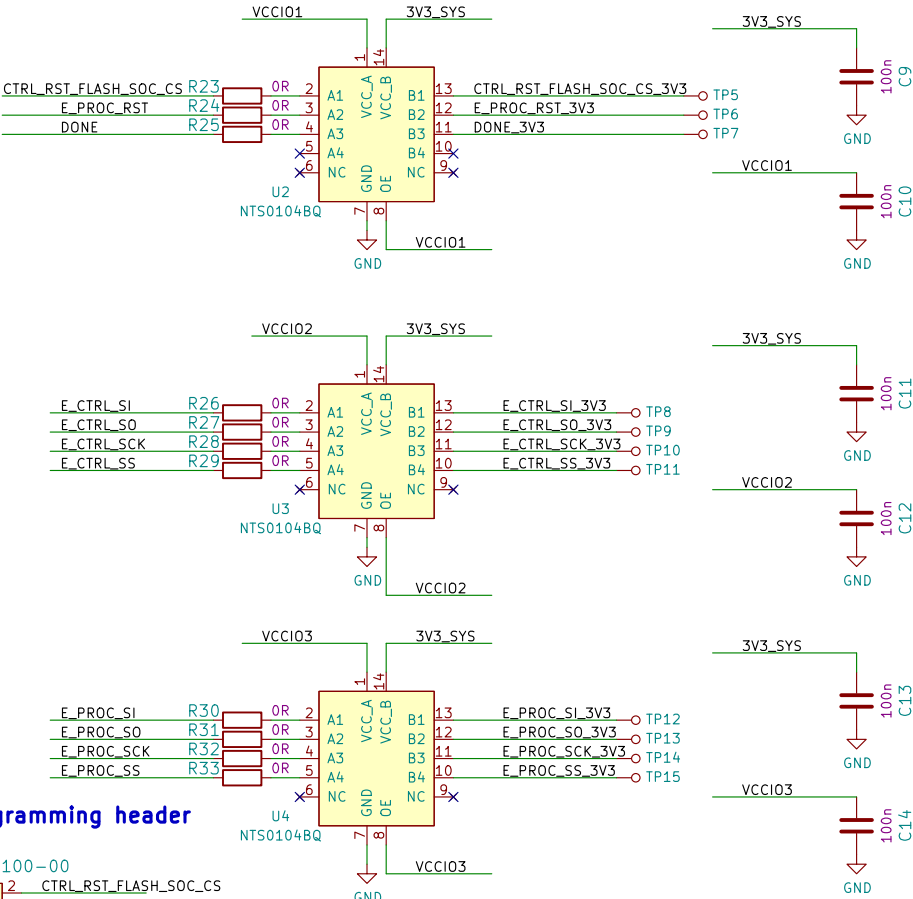
Auxiliary power supply entry



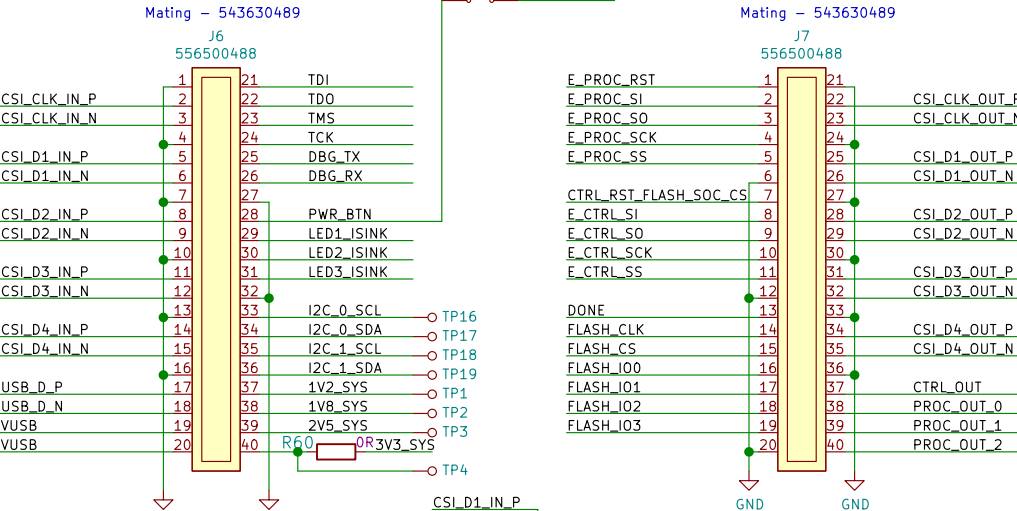
Voltage level selection



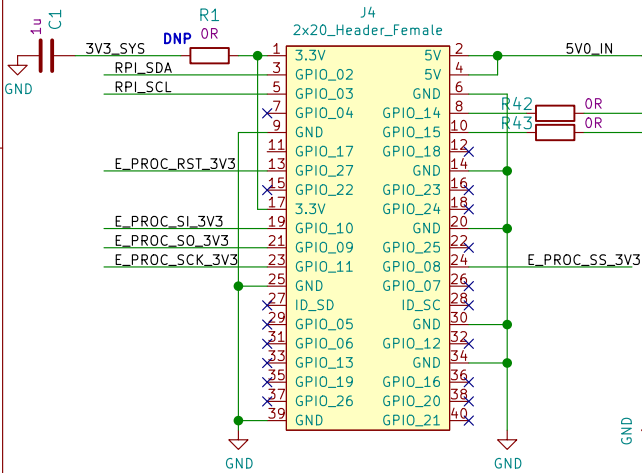
Voltage level translation



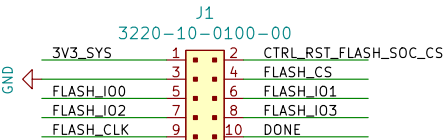
Baseboard connectors



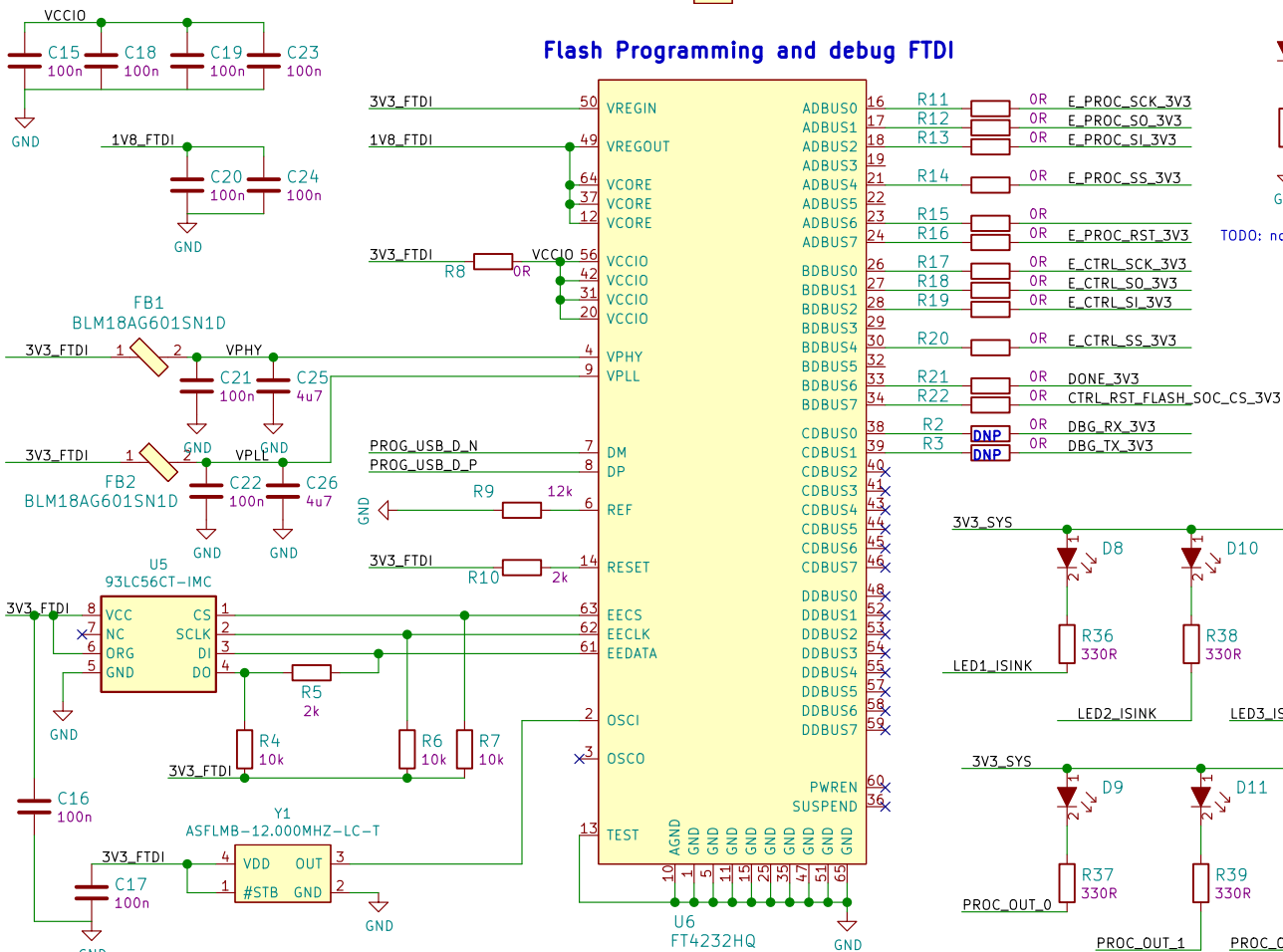
RPi connector



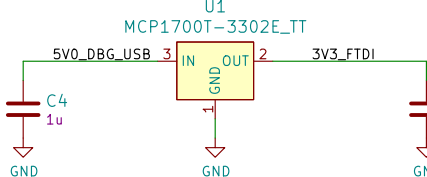
QSPI flash programming header



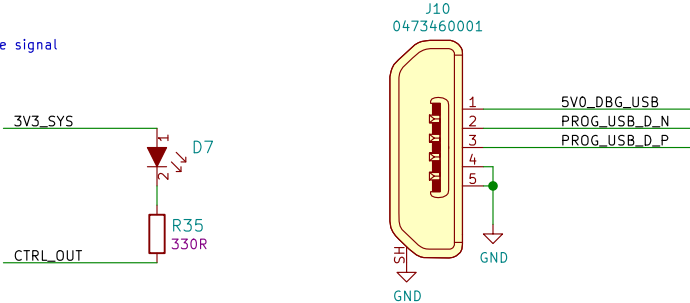
Flash Programming and debug FTDI



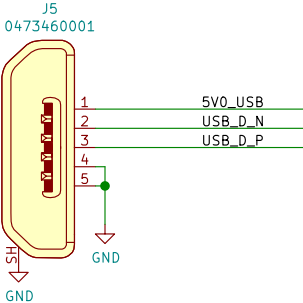
LDO for FTDI



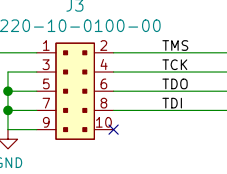
Flash Programming and debug USB connector



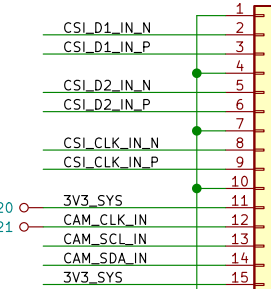
FPGA USB



JTAG interface



CSI input



CSI output

