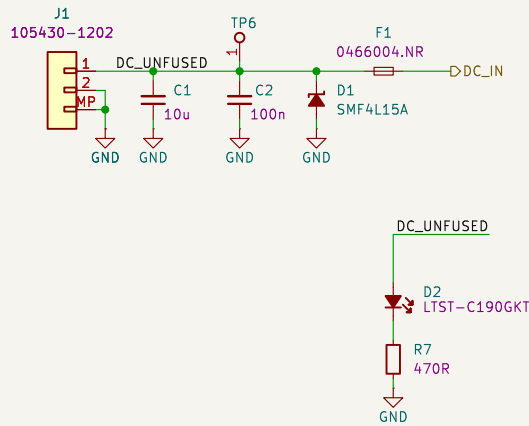
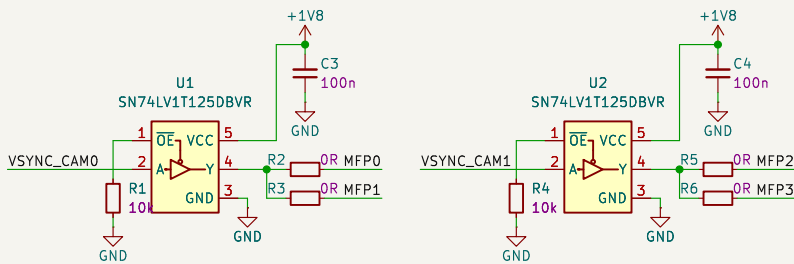


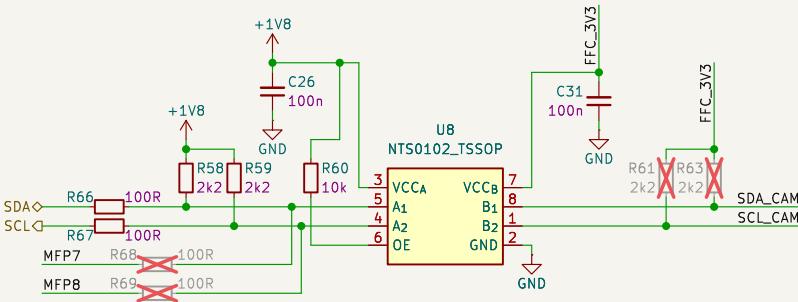
Power connector



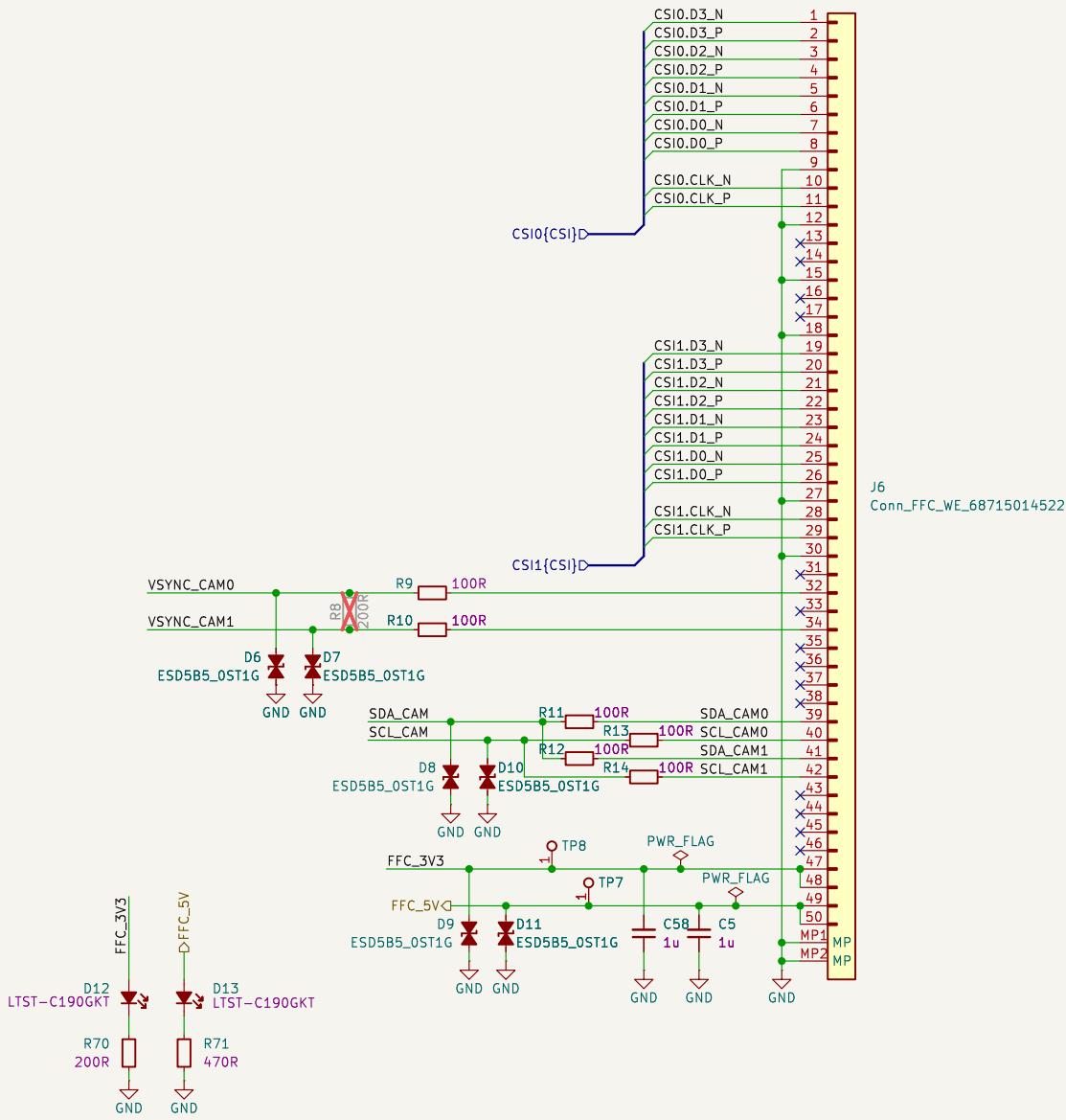
Buffer gates



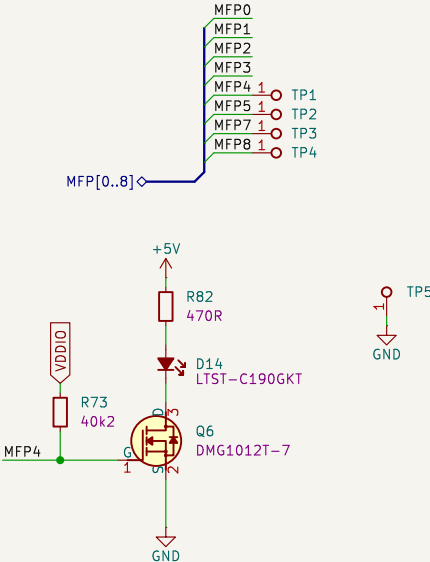
Level shifter



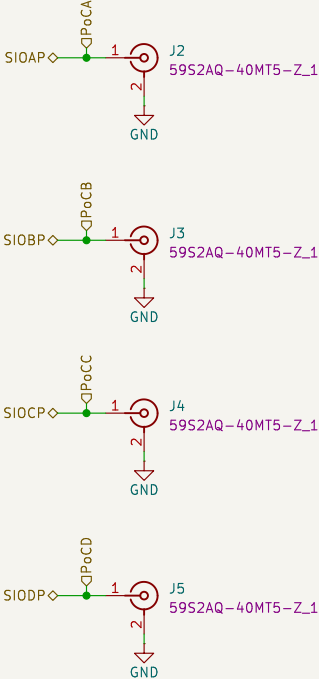
CSI connector



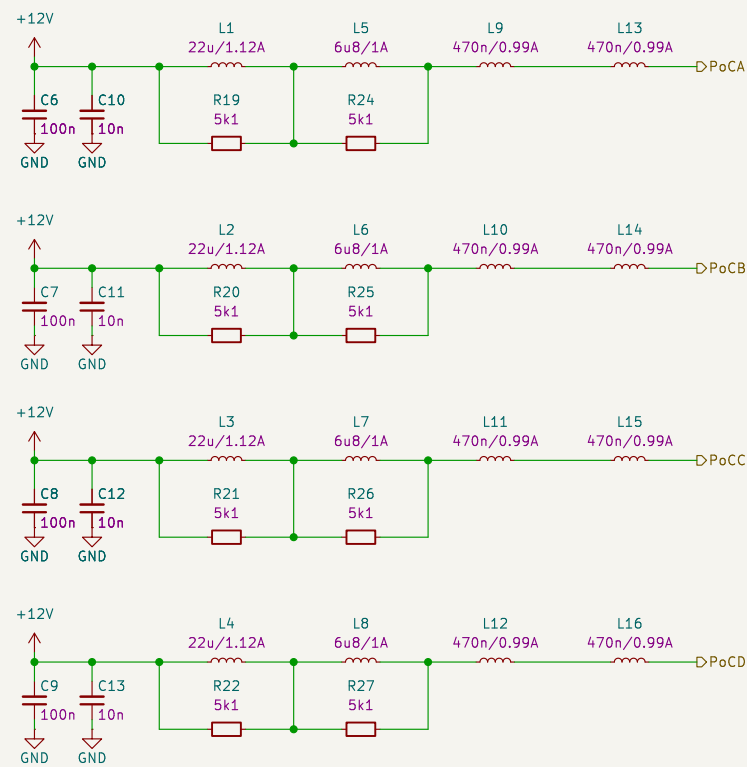
Multi-function pins



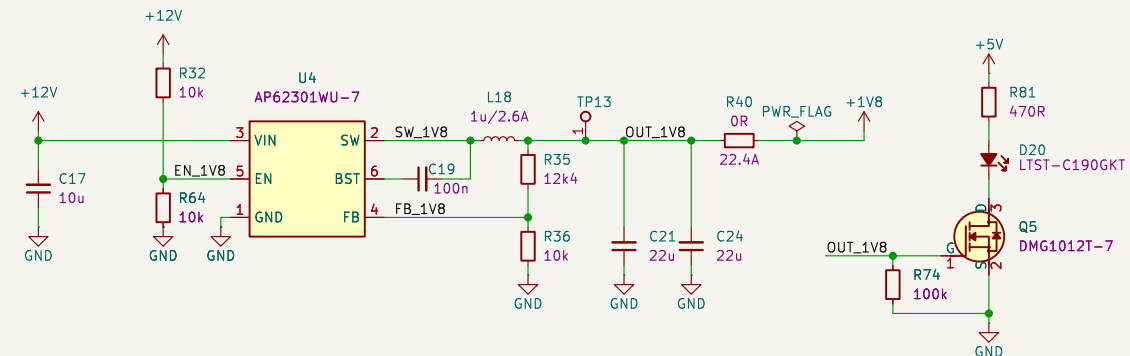
Fakra connectors



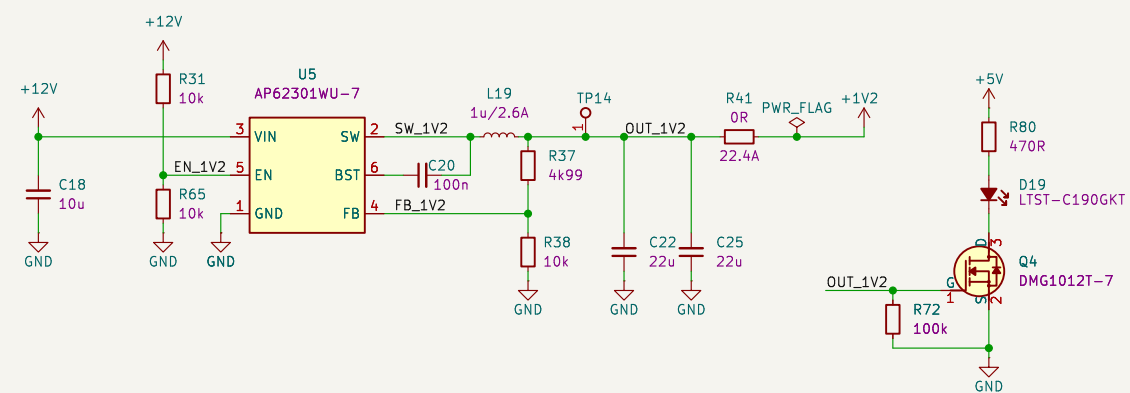
PoC circuit



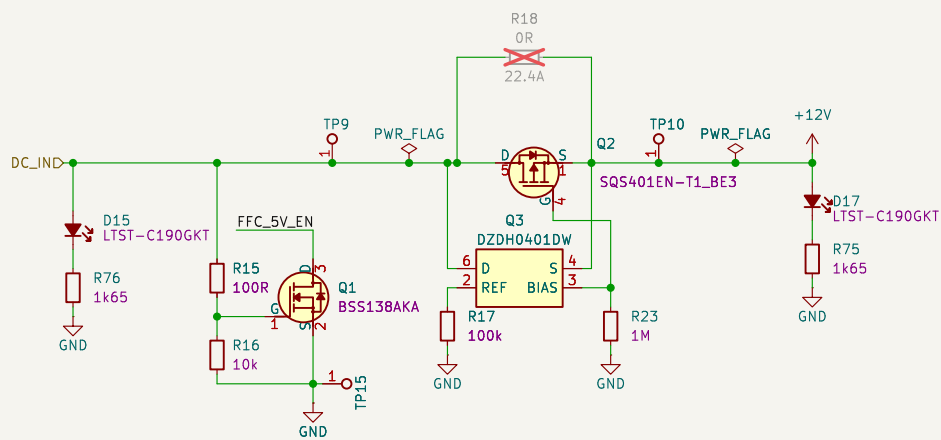
Buck converter  
12V to 1.8V



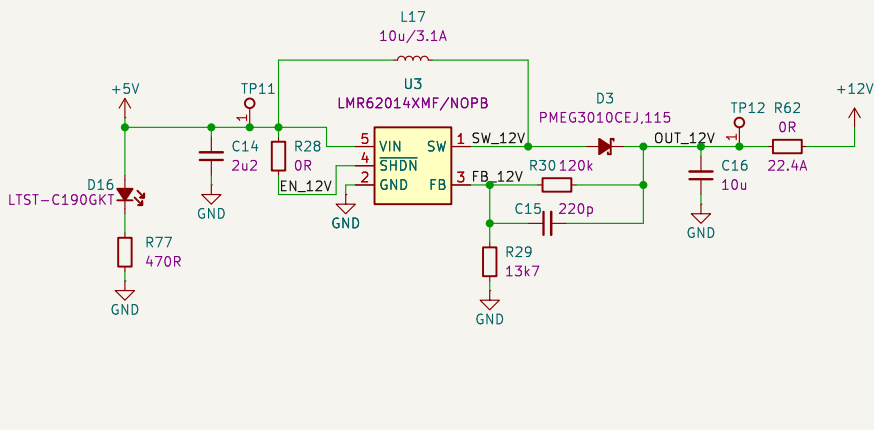
Buck converter  
12V to 1.2V



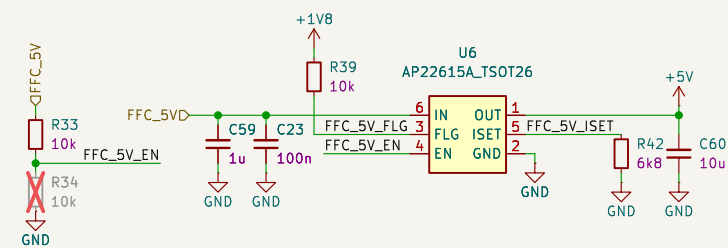
Power arbiter



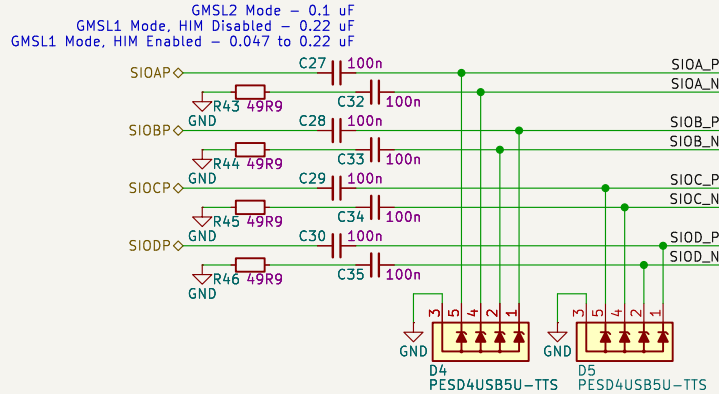
Boost converter  
5V to 12V



FFC 5V current limit



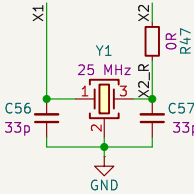
# Link-Isolation capacitors and ESD diodes



# Crystal

$C_{inx1} = 3 \text{ pF}$  (from MAX96724 data sheet)  
 $C_{trace} = 2 \text{ pF}$  (estimated)  
 $C_{inx2} = 1 \text{ pF}$  (from MAX96724 data sheet)  
 $CL = 18 \text{ pF}$  (from crystal data sheet)  
 $C_{x1total} = C_{inx1} + CL + C_{trace}$   
 $C_{x2total} = C_{inx2} + CL + C_{trace}$   
 $CL = (C_{x1total} * C_{x2total}) / (C_{x1total} + C_{x2total})$

$CL1 = CL2 = 33 \text{ pF}$   
 $C_{x1total} = 38 \text{ pF}$ ,  $C_{x2total} = 36 \text{ pF}$   
 $CL = 18.5 \text{ pF}$



# Config pins

CFG0 - I2C address config  
R1 = OPEN, R2 = 10k  
address = 0x4E



CFG1 - GMSL mode config  
R1 = OPEN, R2 = 10k  
mode = COAX GMSL2 6Gbps



# Deserializer IC with passives

