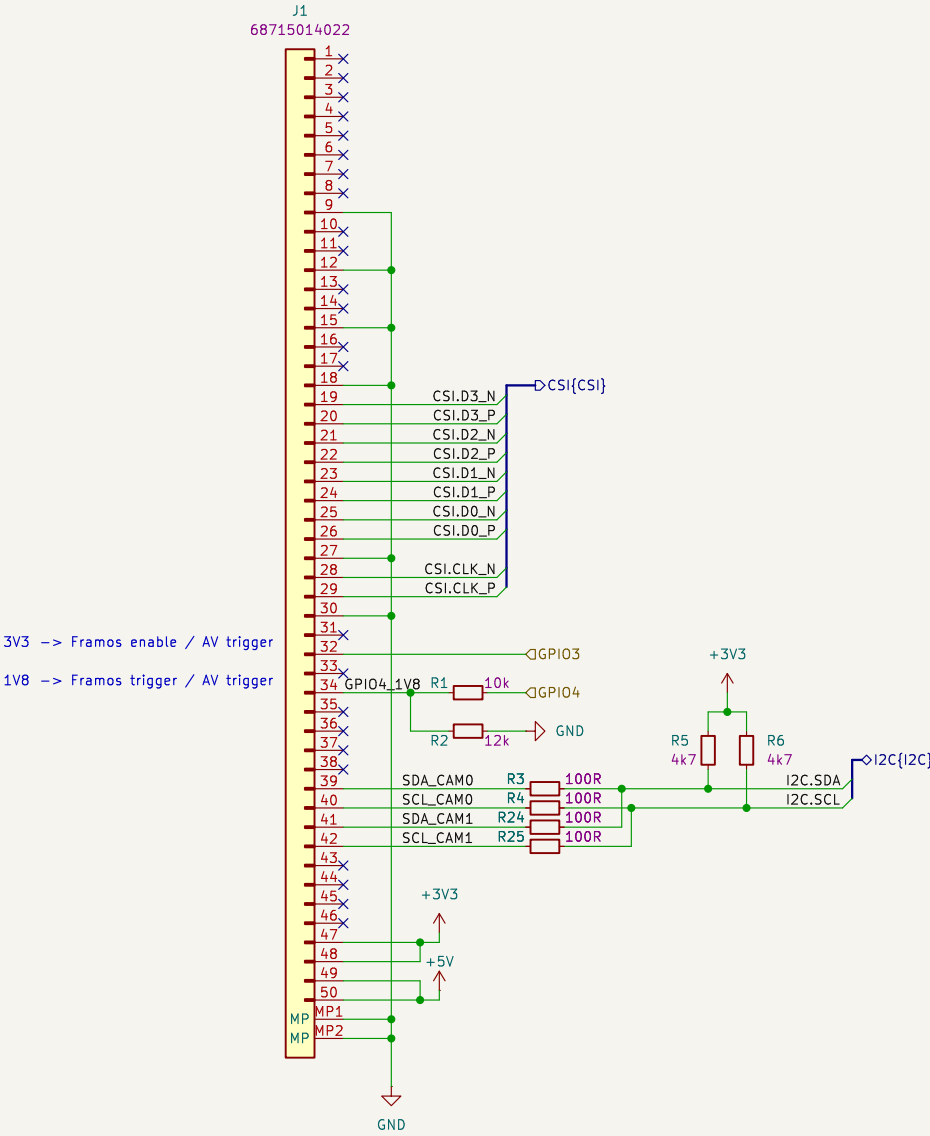
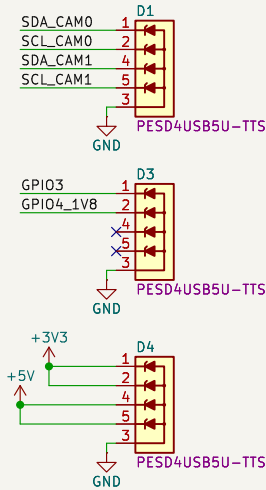


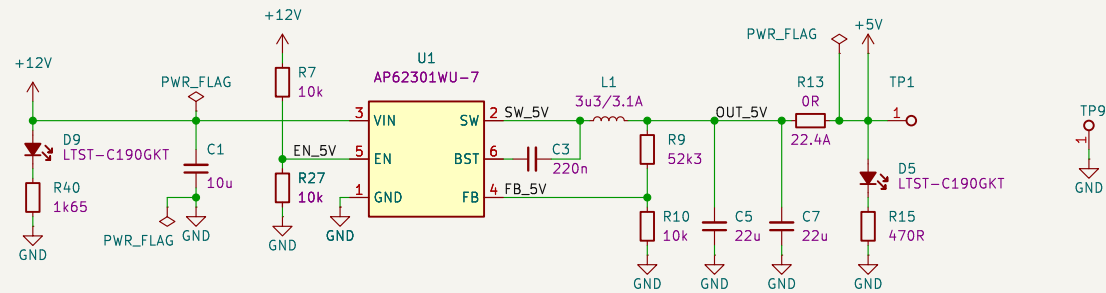
FFC CSI Connector



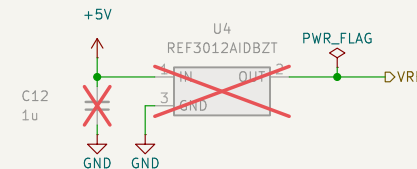
TVS diodes



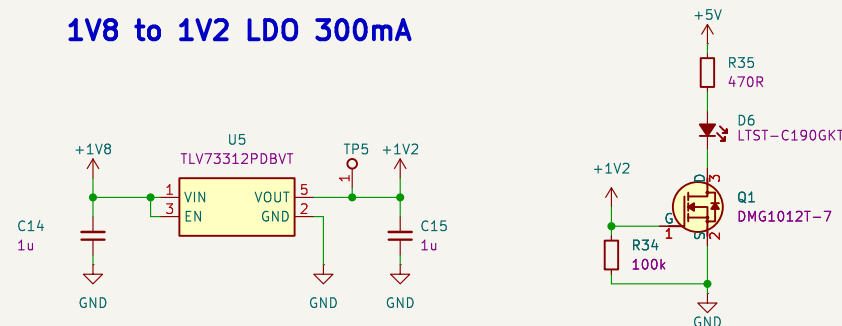
5V BUCK CONVERTER



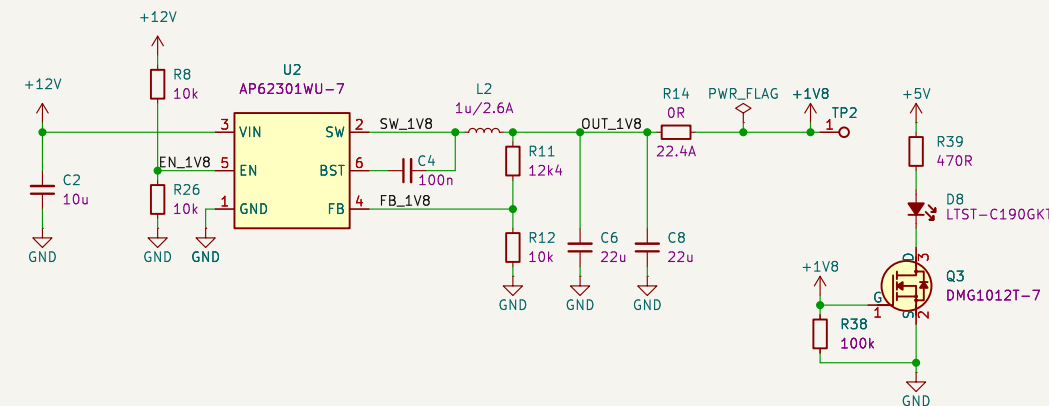
1V25 voltage reference DNP



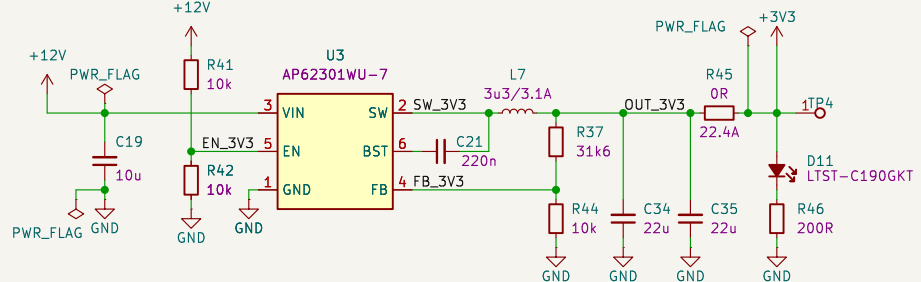
1V8 to 1V2 LDO 300mA



1V8 BUCK CONVERTER



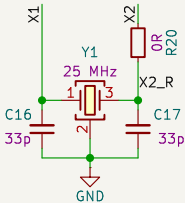
3V3 BUCK CONVERTER



Crystal

Cinx1 = 3 pF (from MAX96717 data sheet)
Ctrace = 2 pF (estimated)
Cinx2 = 1 pF (from MAX96717 data sheet)
CL = 18 pF (from crystal data sheet)
Cx1total = Cinx1 + CL1 + Ctrace
Cx2total = Cinx2 + CL2 + Ctrace
CL = (Cx1total * Cx2total) / (Cx1total + Cx2total)

CL1 = 33 pF
CL2 = 33 pF
CX1total = 38 pF
Cx2total = 36 pF
CL = 18.5 pF (meets the requirements)

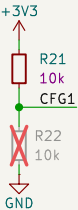


Crystal and crystal load capacitors as close as possible to X1 X2 pins

Config pins

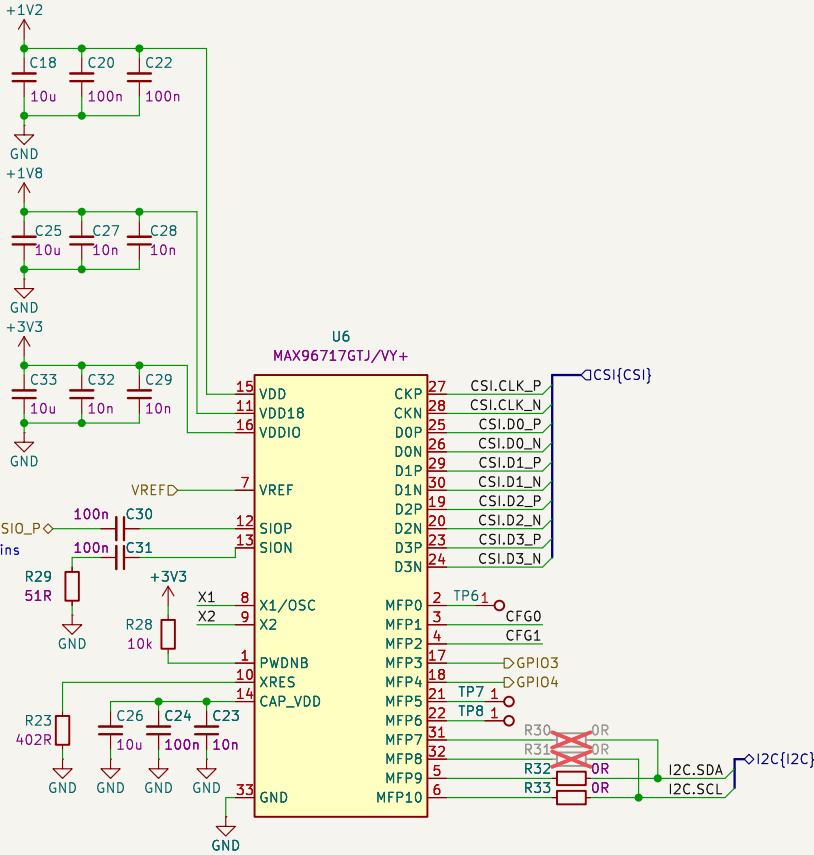
CFG0 -- I2C address config
R1 = OPEN, R2 = 10k
I2C: Address = 0x40 (7-bit);
Clock= RoR

CFG1 -- GSML mode config
R1 = 10k, R2 = OPEN
mode = COAX 6Gbps; Pixel mode



Serializer

Decoupling capacitors as close as possible to supply pins



Link capacitors as close as possible to SIO pins



