

1 2 3 4 5 6 7 8

Jetson Orin Baseboard

github.com/antmicro/jetson-orin-baseboard

A

B

C

D

E

F

A

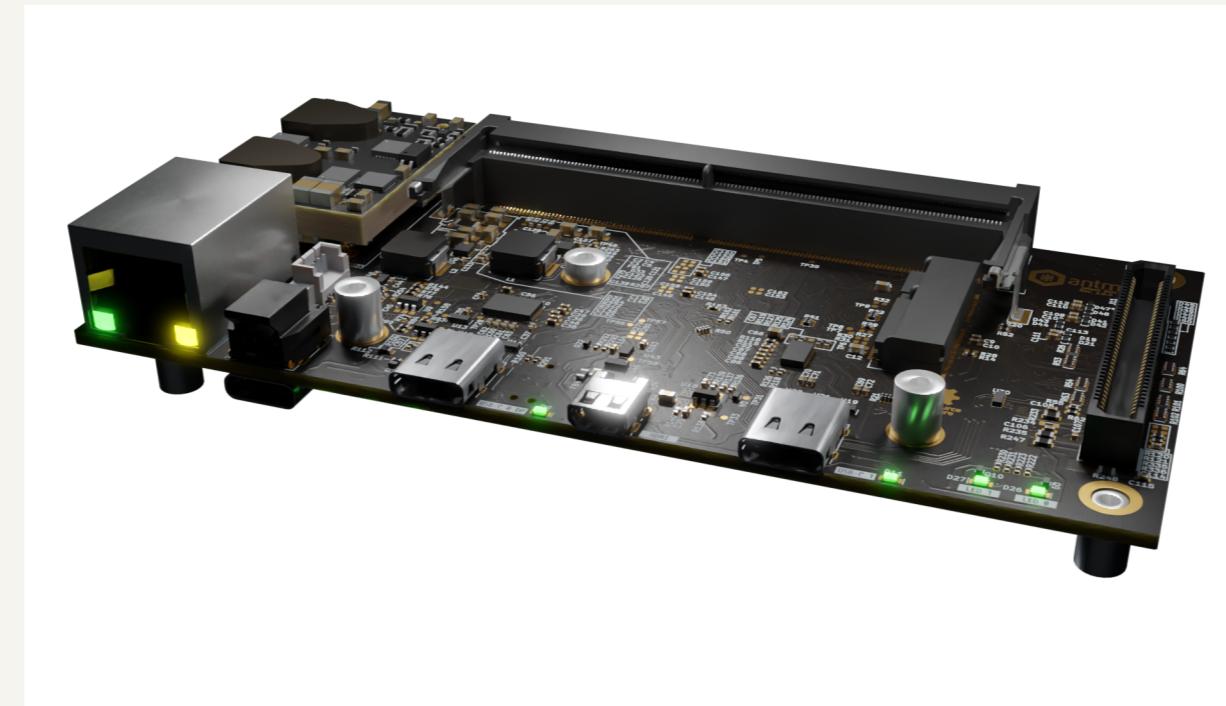
B

C

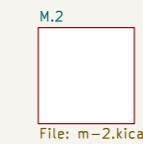
D

E

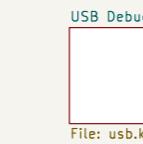
F



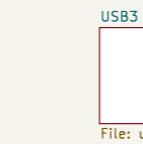
File: som.kicad_sch



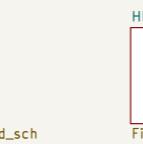
File: m_2.kicad_sch



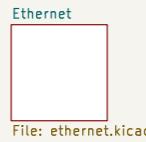
File: usb.kicad_sch



File: usb3.kicad_sch



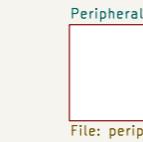
File: hdmi.kicad_sch



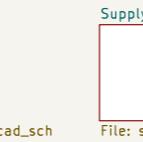
File: ethernet.kicad_sch



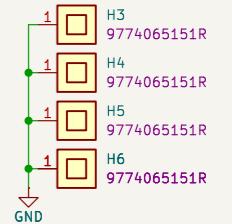
File: csi.kicad_sch



File: peripherals.kicad_sch



File: supply.kicad_sch



www.antmicro.com
Antmicro Ltd

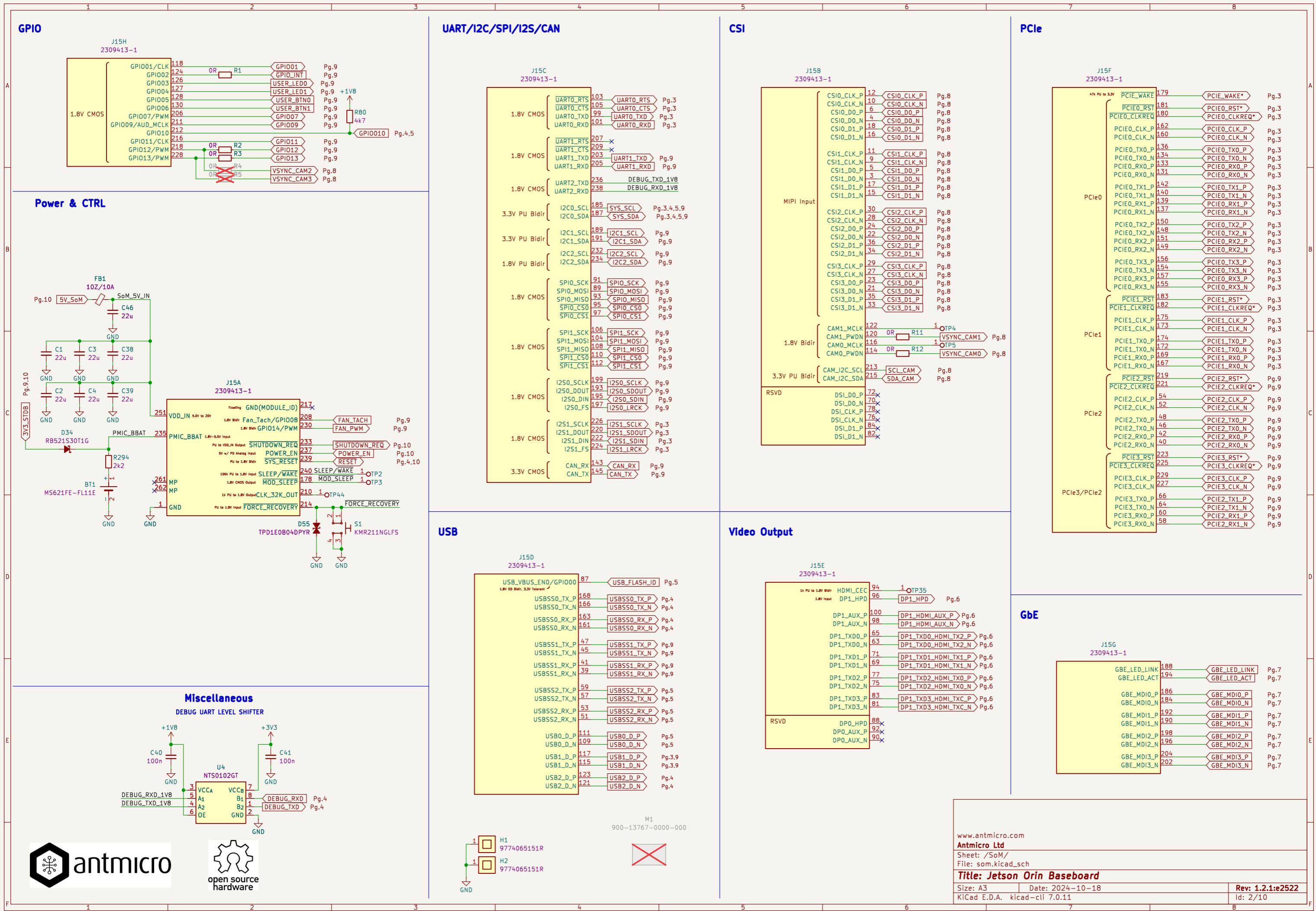
Sheet: / File: jetson-orin-baseboard.kicad_sch

Title: Jetson Orin Baseboard

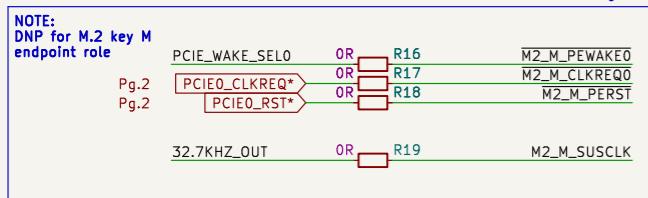
Size: A3 Date: 2024-10-18
KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.2.1:e2522 Id: 1/10

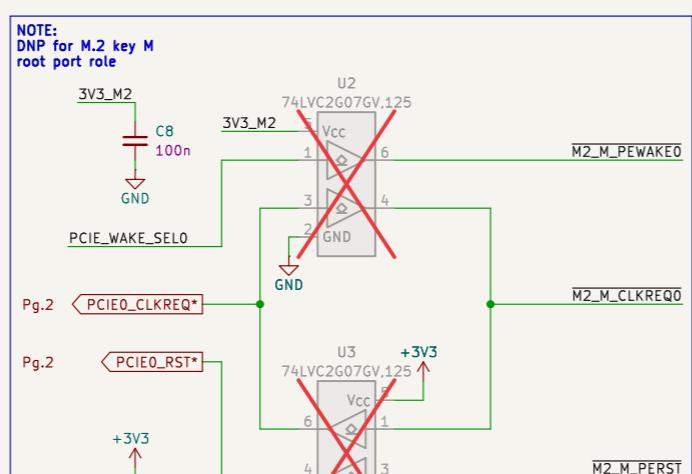
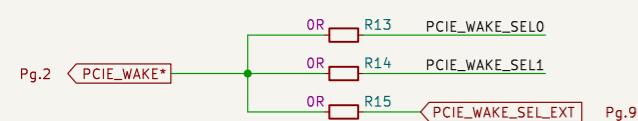
1 2 3 4 5 6 7 8



PCIe0 Root Port / Endpoint mode selector

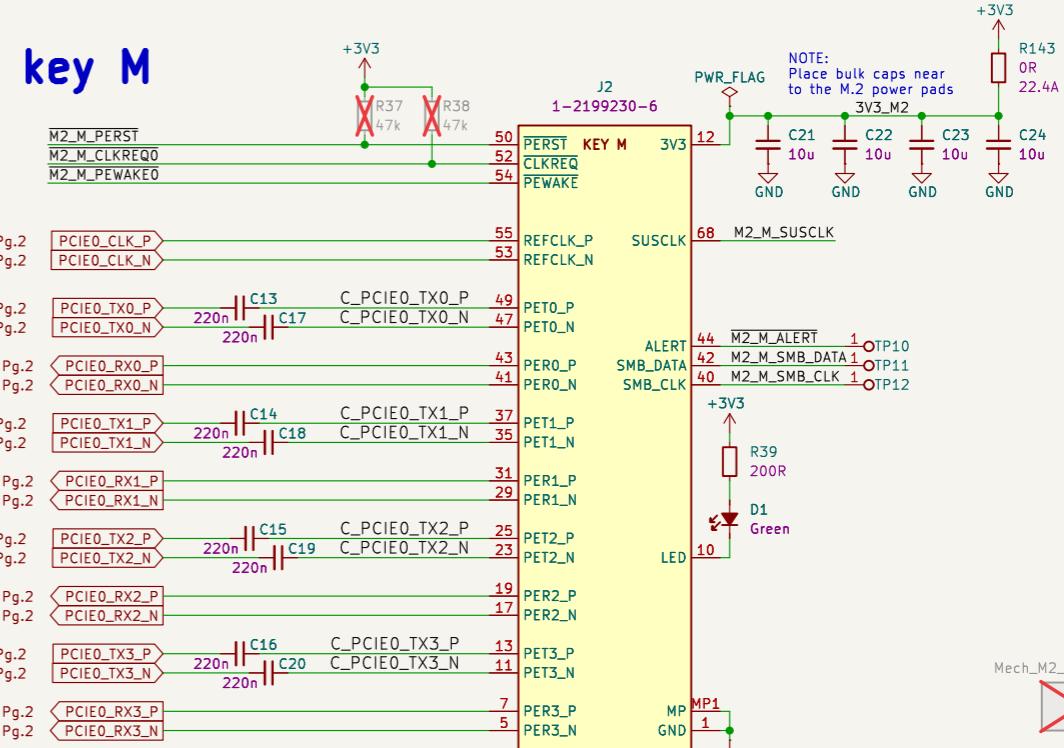


M.2 PCIE_WAKE selector

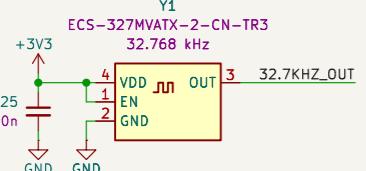


NOTE about PCIe 0 role switching:
For details, refer to the PCIe section of
'Jetson Orin NX Series Product Design Guide'
<https://developer.nvidia.com/jetson-orin-nx-series-design-guide>

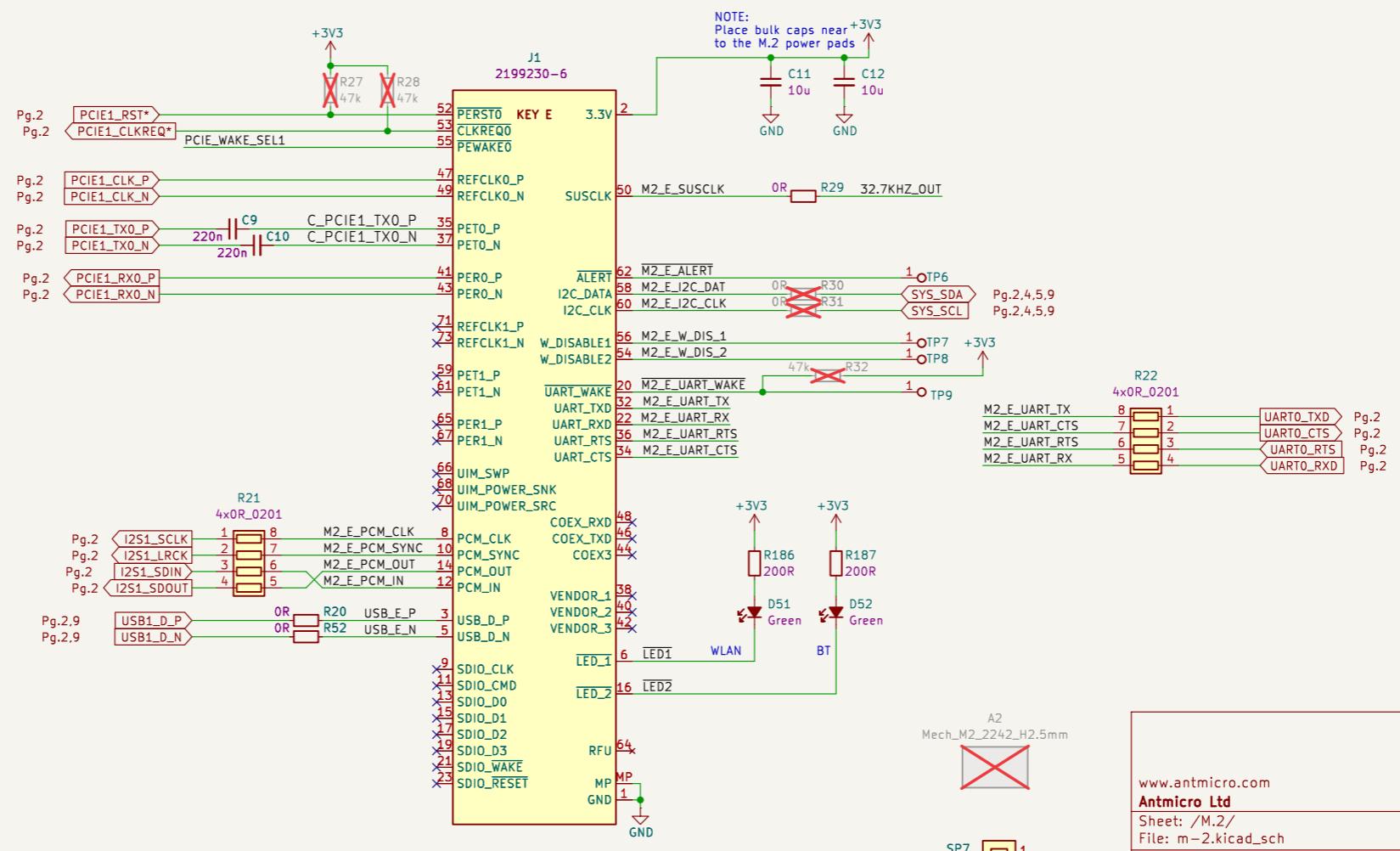
PCIe0 1x4 key M



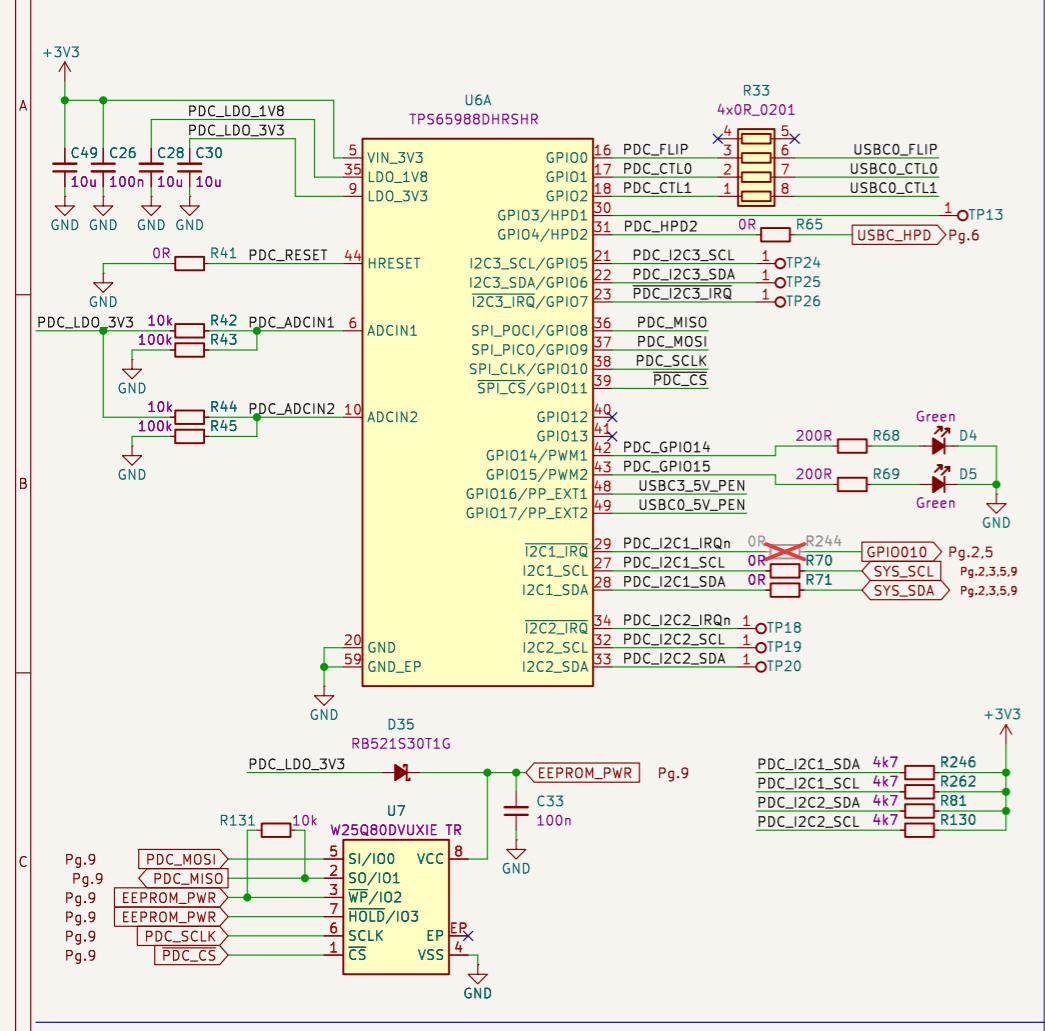
SUSCLK SOURCE



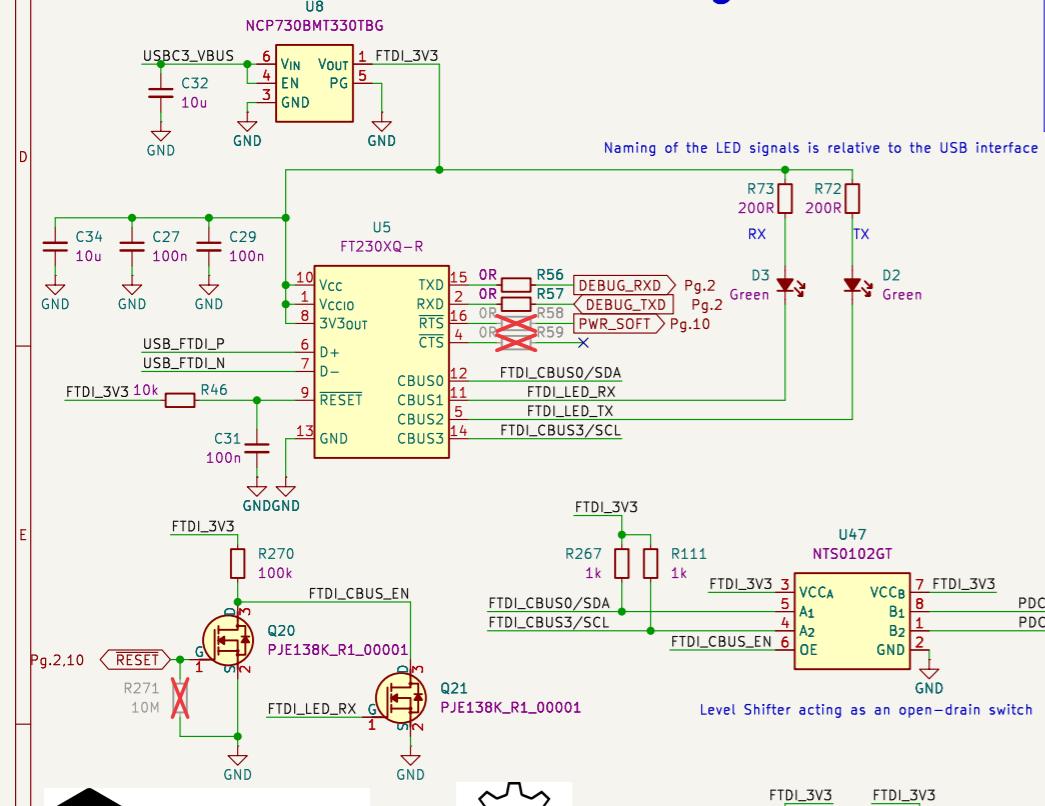
PCIe1 1x1 key E



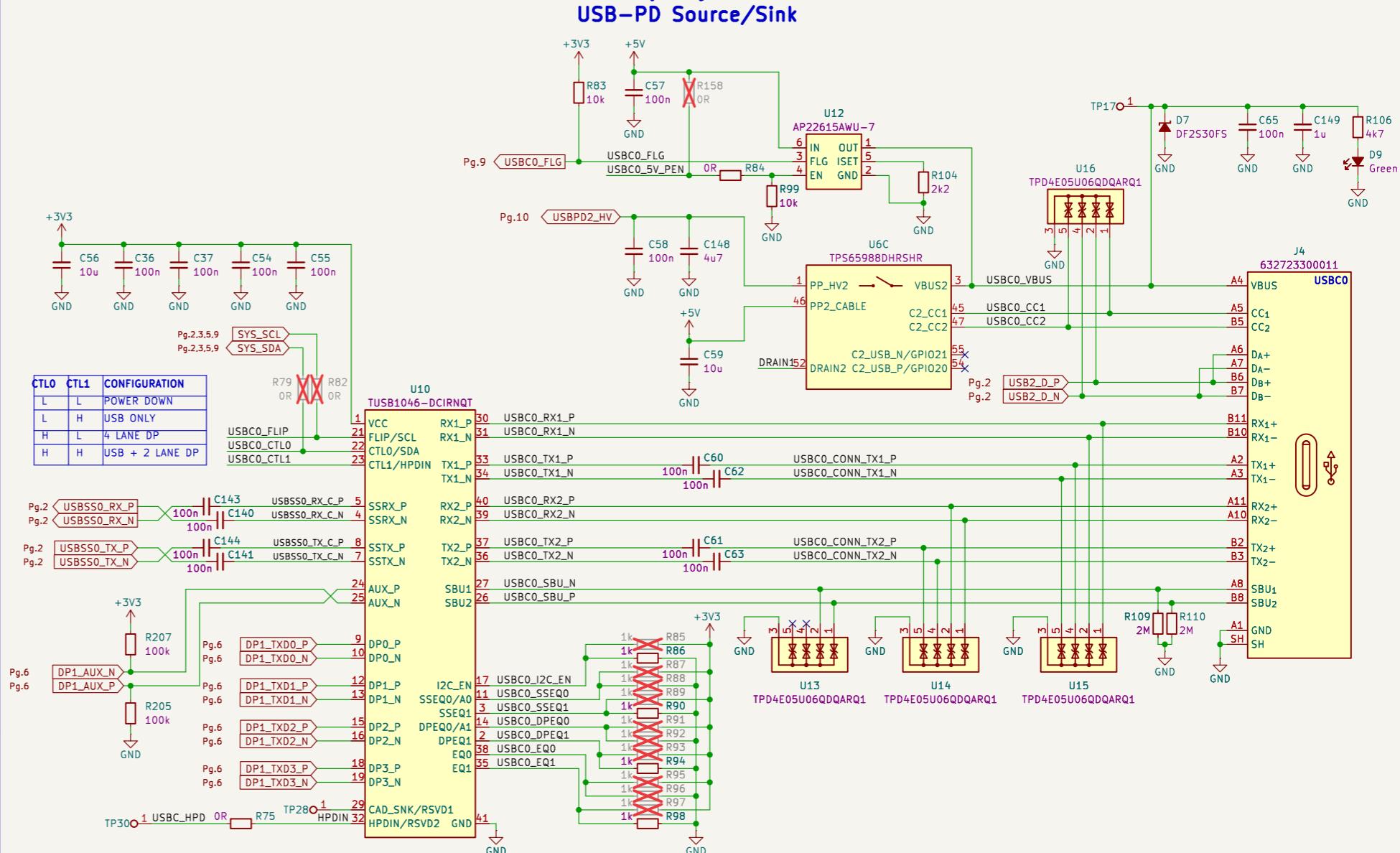
USB PD Controller



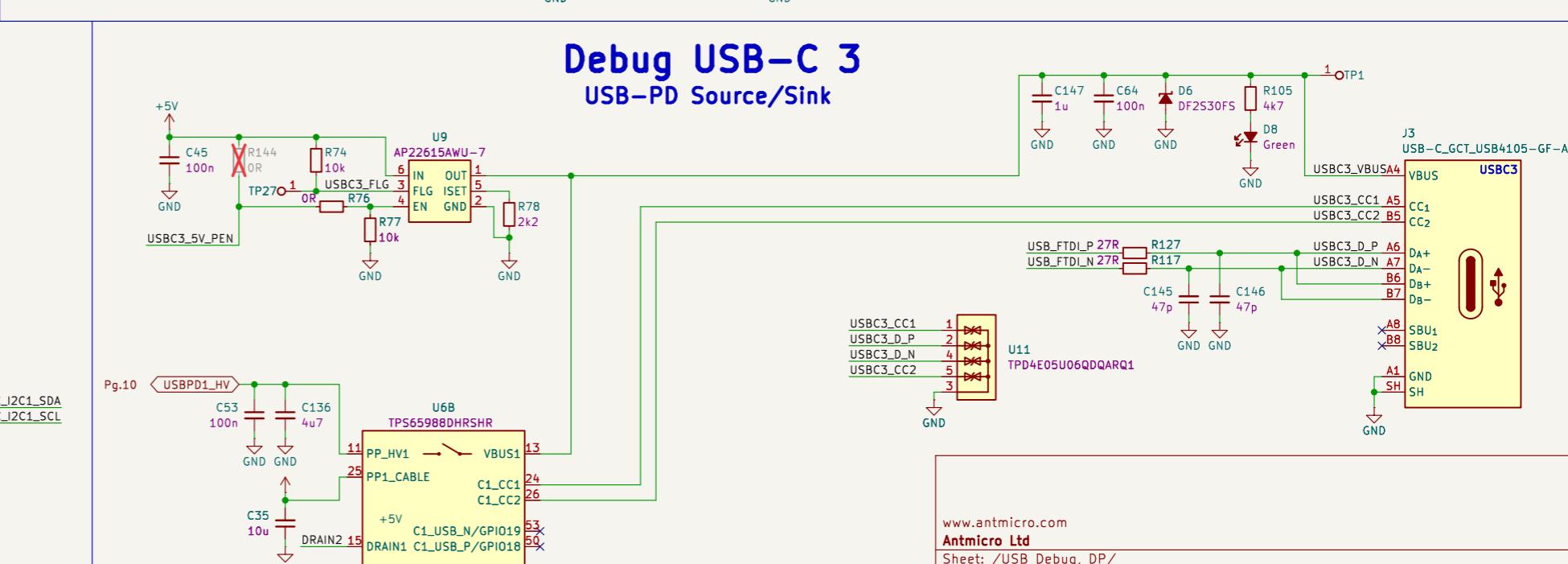
USB-UART Bridge



USB-C 0 Display Port alt mode



Debug USB-C 3



www.antmicro.com

Antmicro Ltd

Sheet: /USB Debug, DP/

File: usb.kicad_sch

Title: Jetson Orin Baseboard

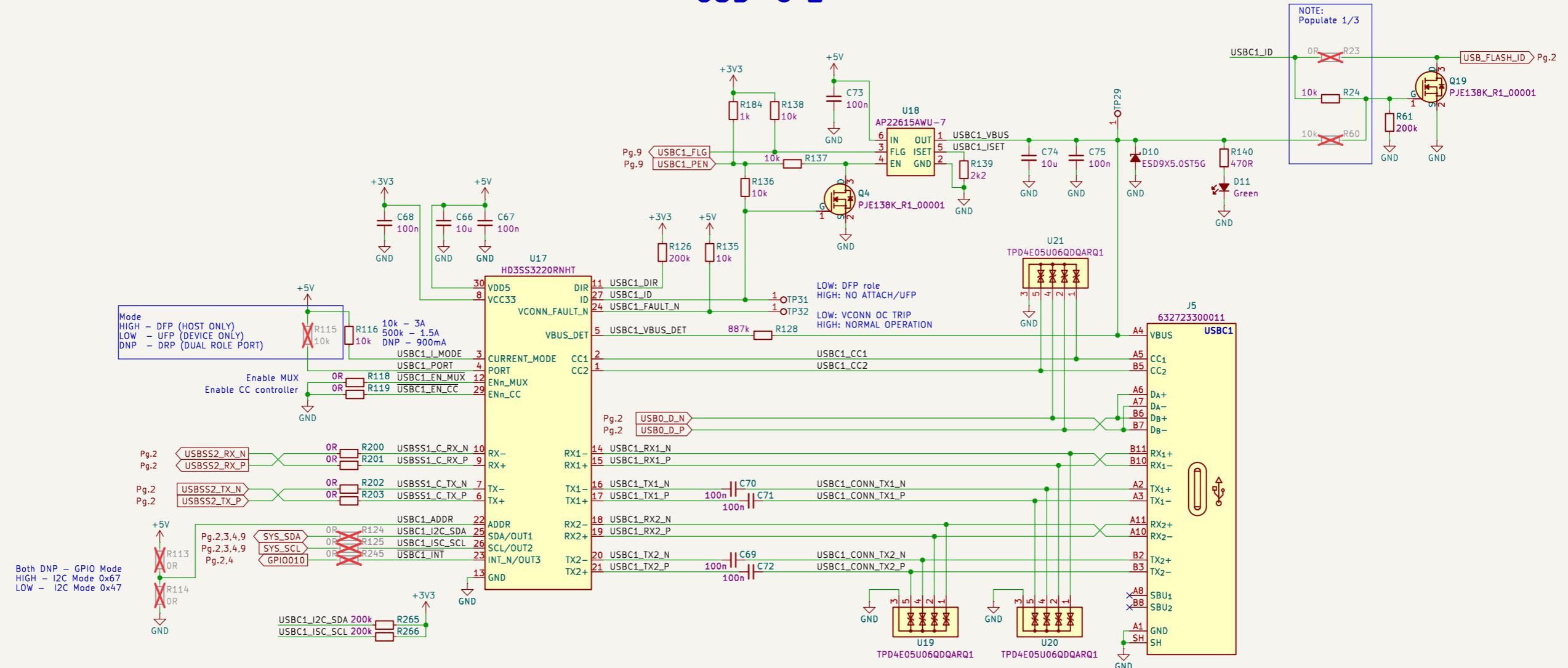
Size: A3 Date: 2024-10-18

KiCad E.D.A. kicad-cl 7.0.11

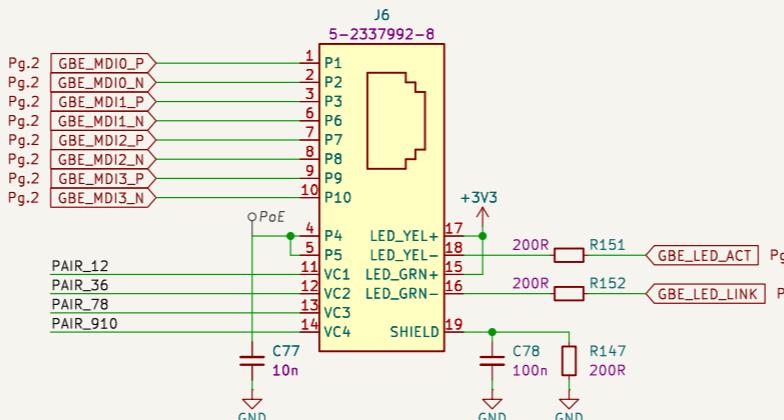
Rev: 1.2.1:e2522

Id: 4/10

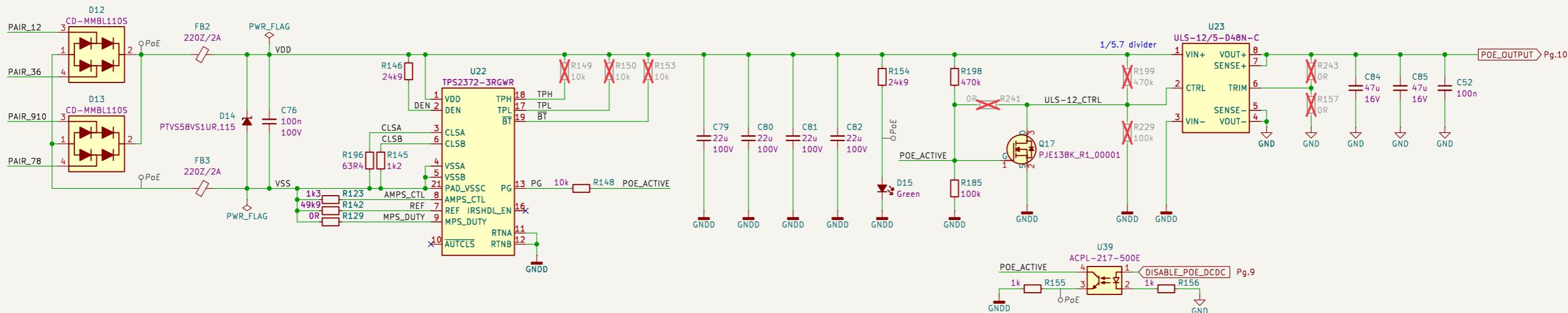
USB-C 1



Ethernet Connector



Power over Ethernet



www.antmicro.com

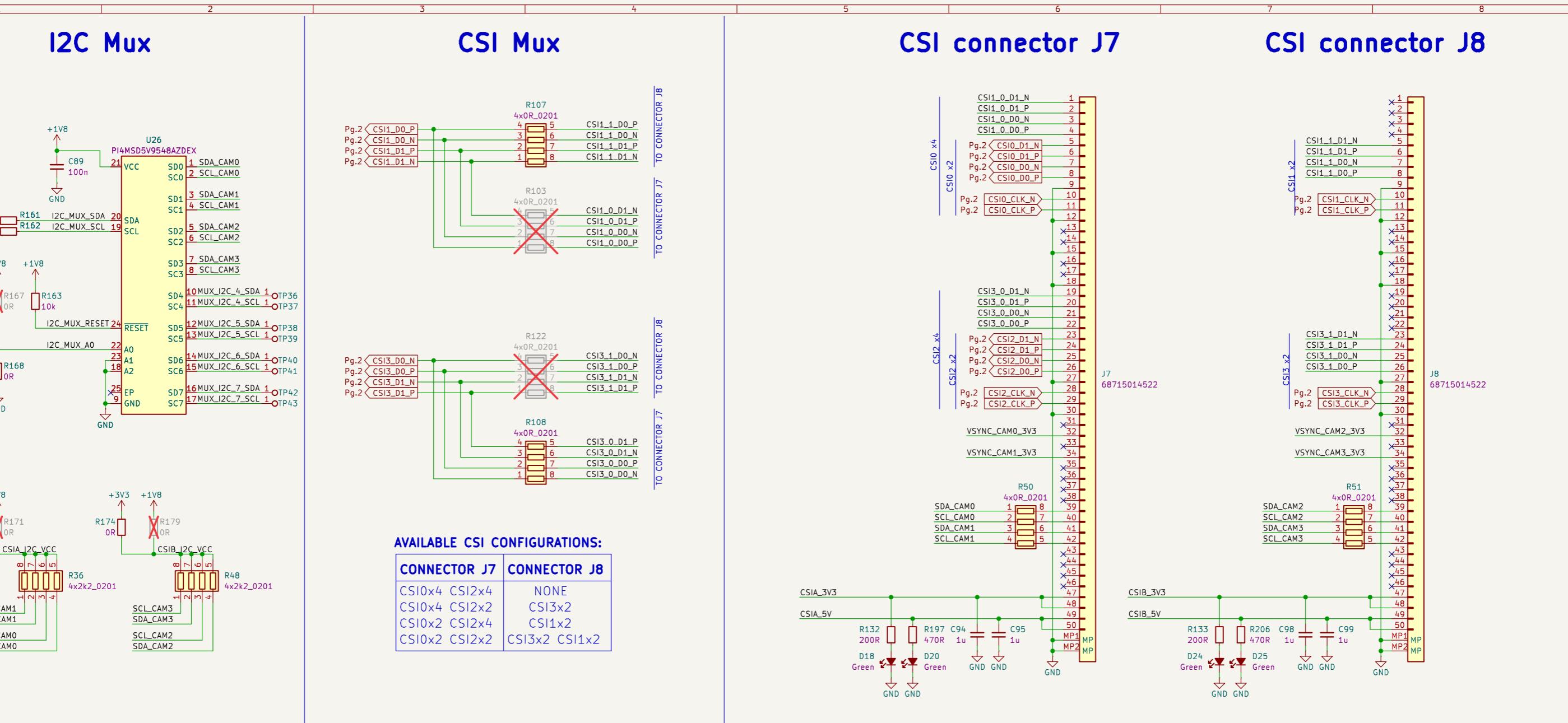
Antmicro Ltd

Sheet: /Ethernet/
File: ethernet.kicad_sch

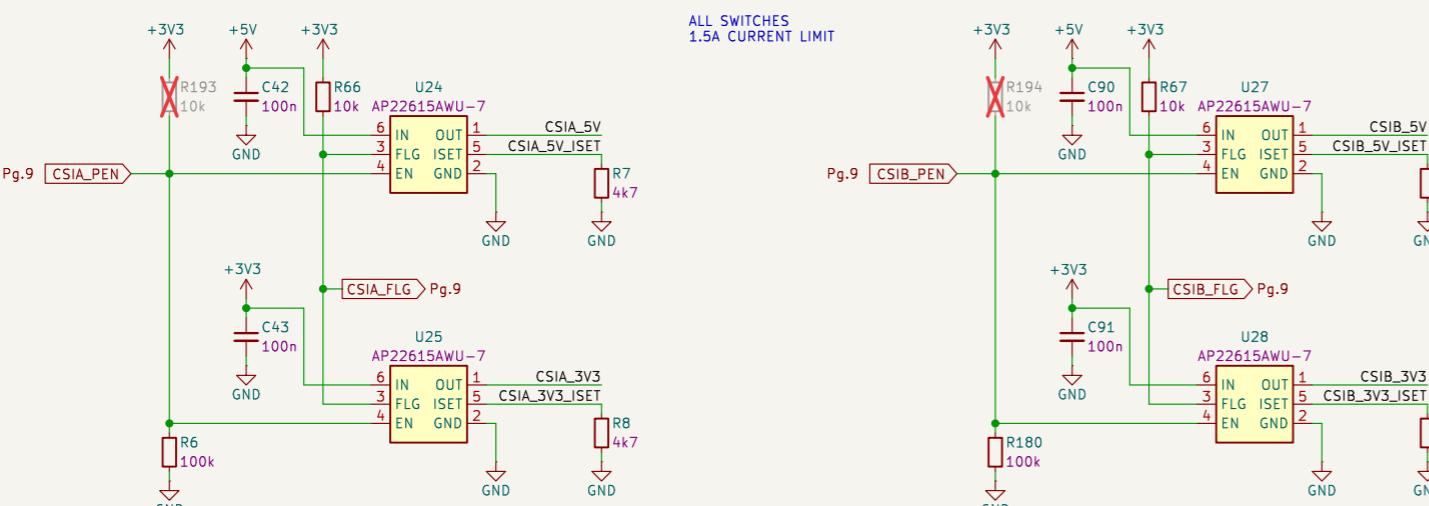
Title: Jetson Orin Baseboard

Size: A3 Date: 2024-10-18
KiCad E.D.A. kicad-cl 7.0.11

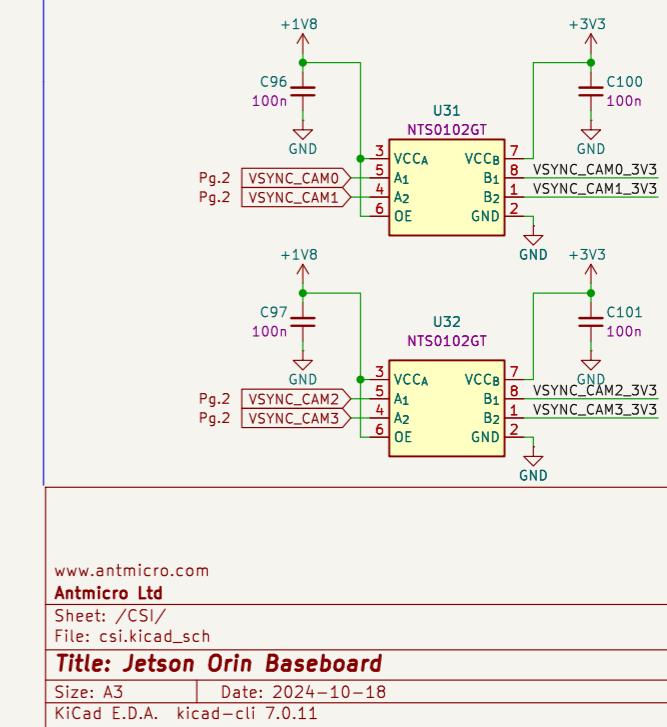
Rev: 1.2.1:e2522
Id: 7/10



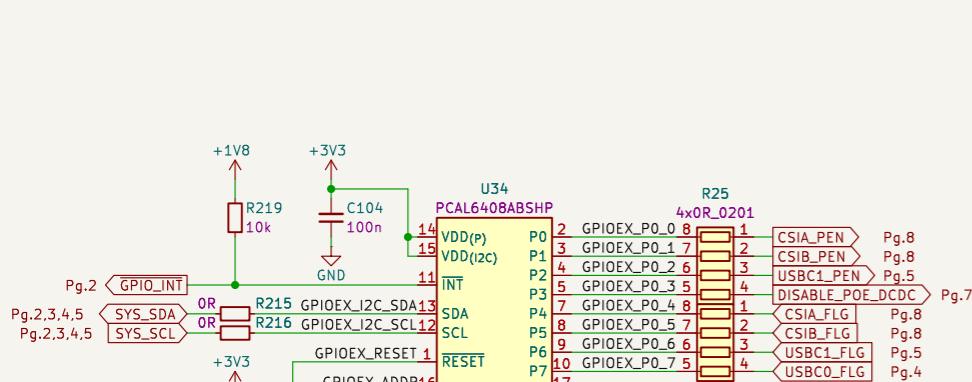
Power switches



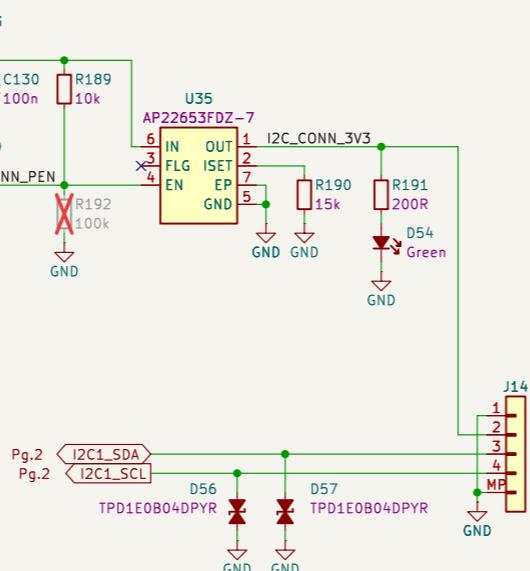
Level shifters



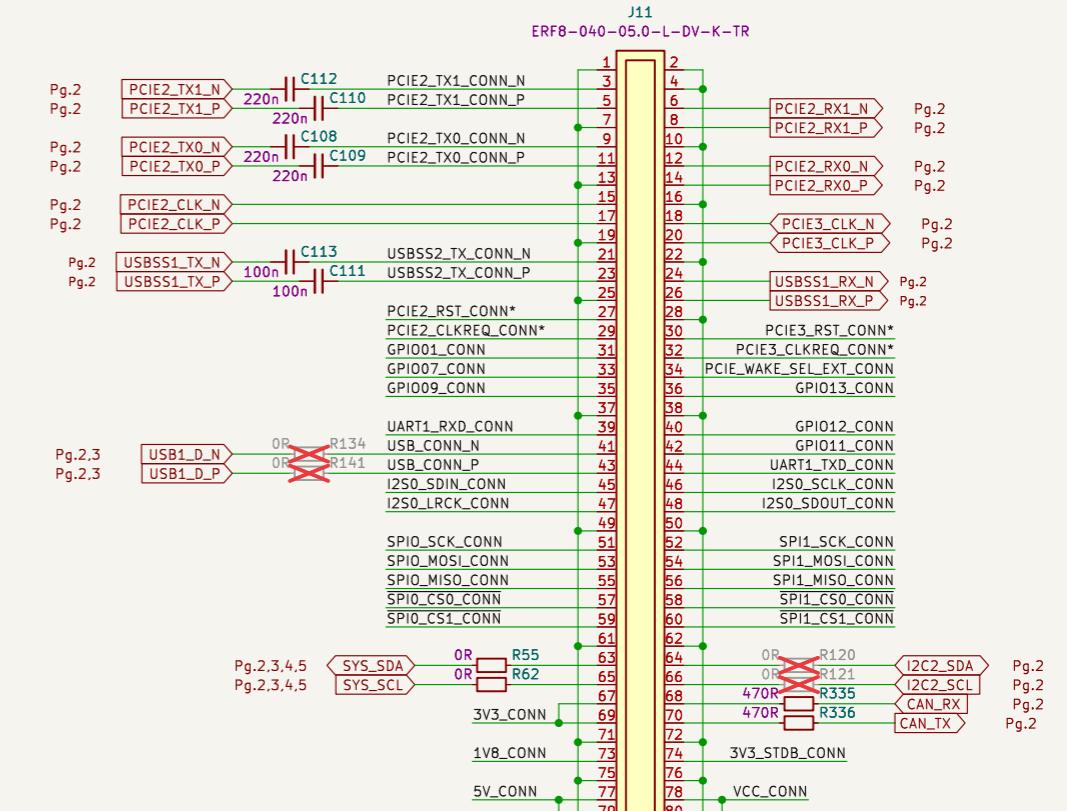
GPIO expander



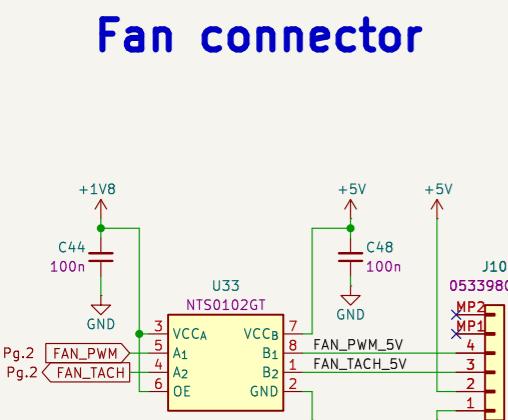
I₂C connector



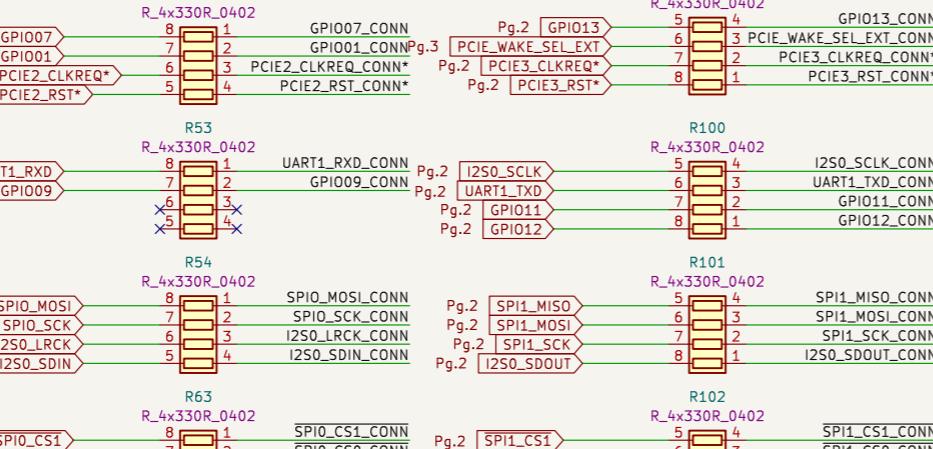
Expansion connector



Fan connector



Expansion connector resistors



Debug connector

