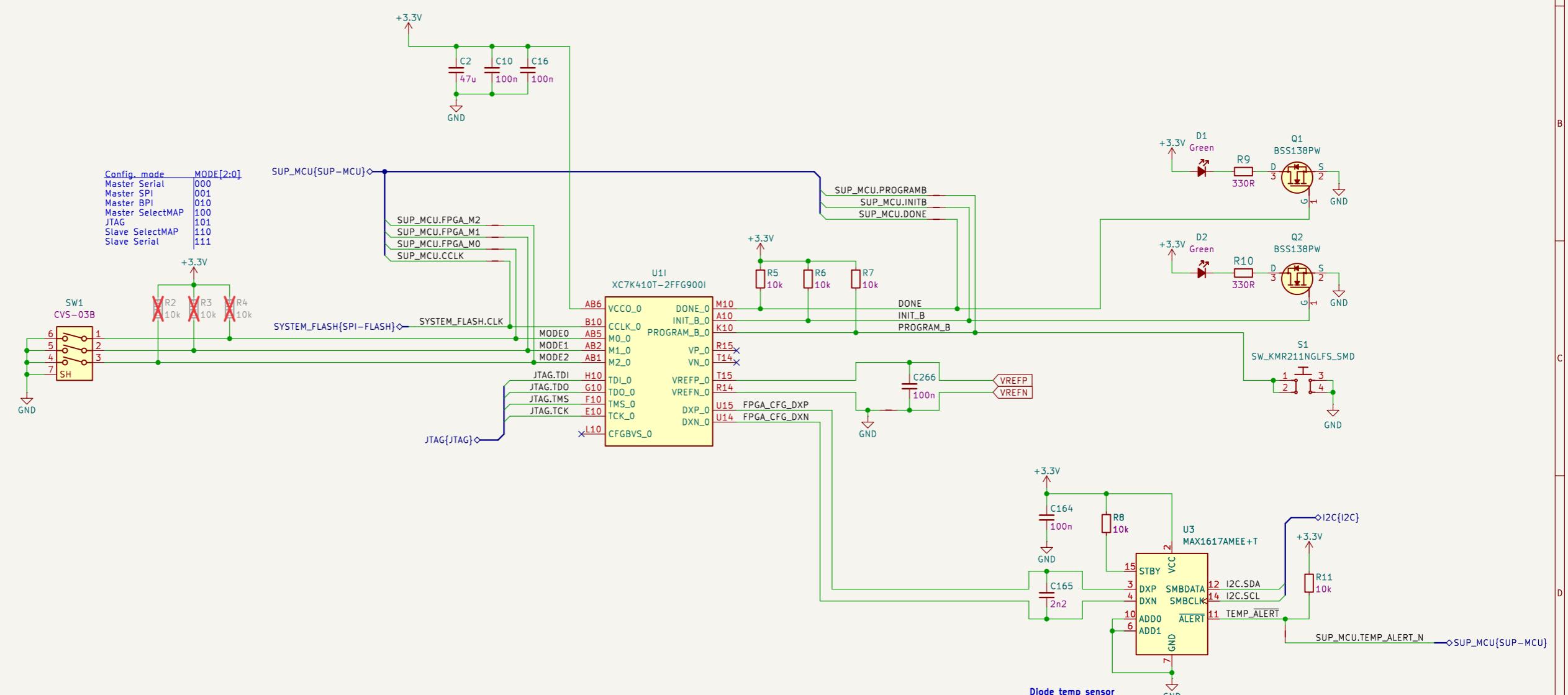


Sheet: /FPGA supply/
File: fpga-supply.kicad_sch

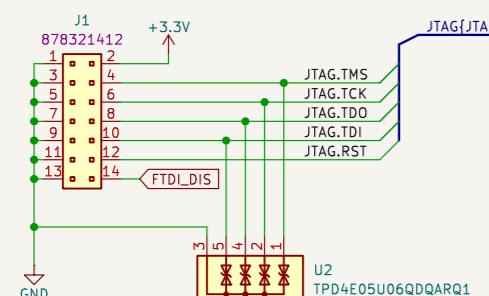
Title: Kintex 410T devboard

Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.1.2:86f82 | Id: 2/19



JTAG Connector
Compatible with Xilinx Platform Cable



antmicro
open source
hardware

Sheet: /FPGA Config/
File: fpga-config.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.2:86f82
Id: 3/19

A

A

B

B

C

C

D

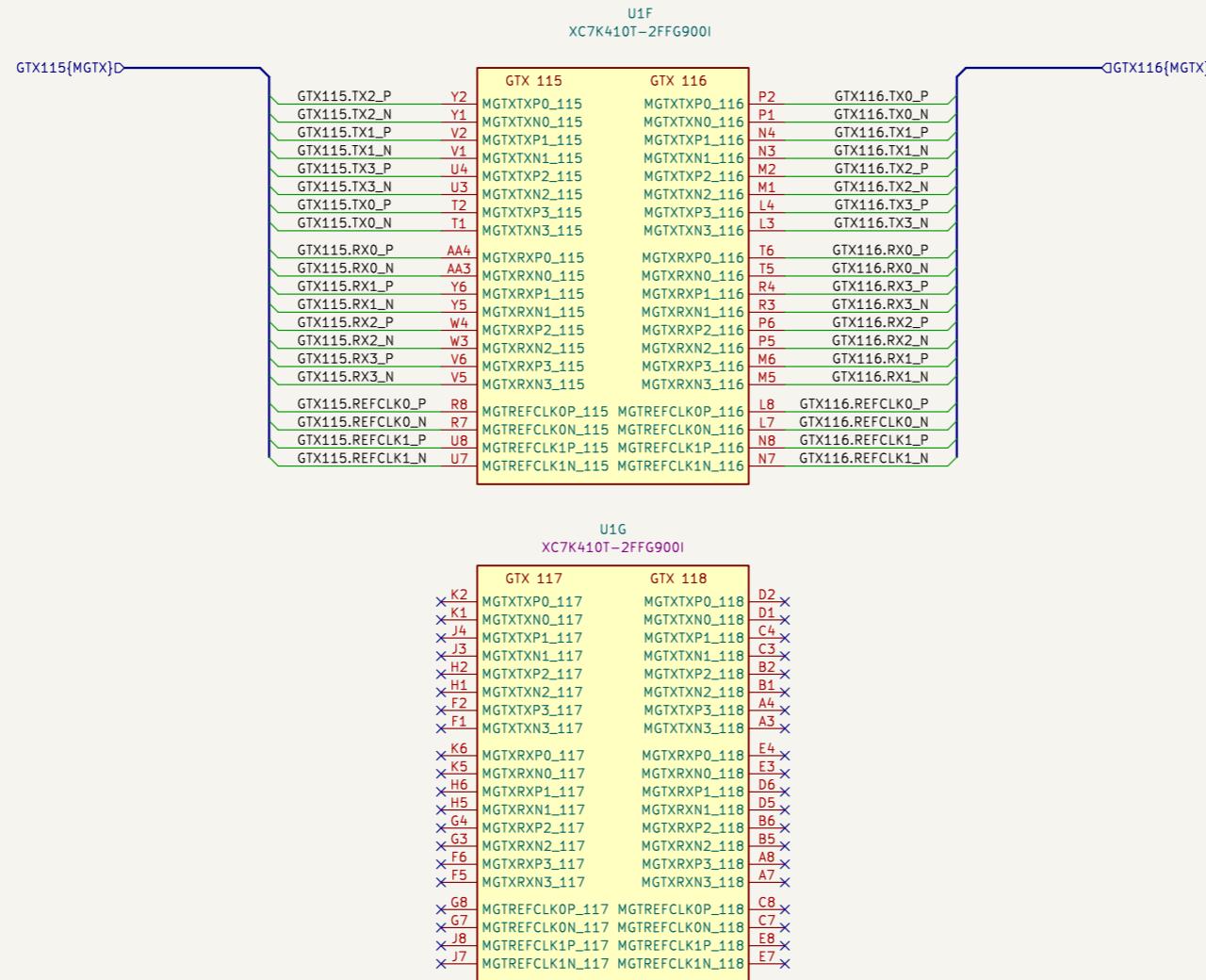
D

E

E

F

F

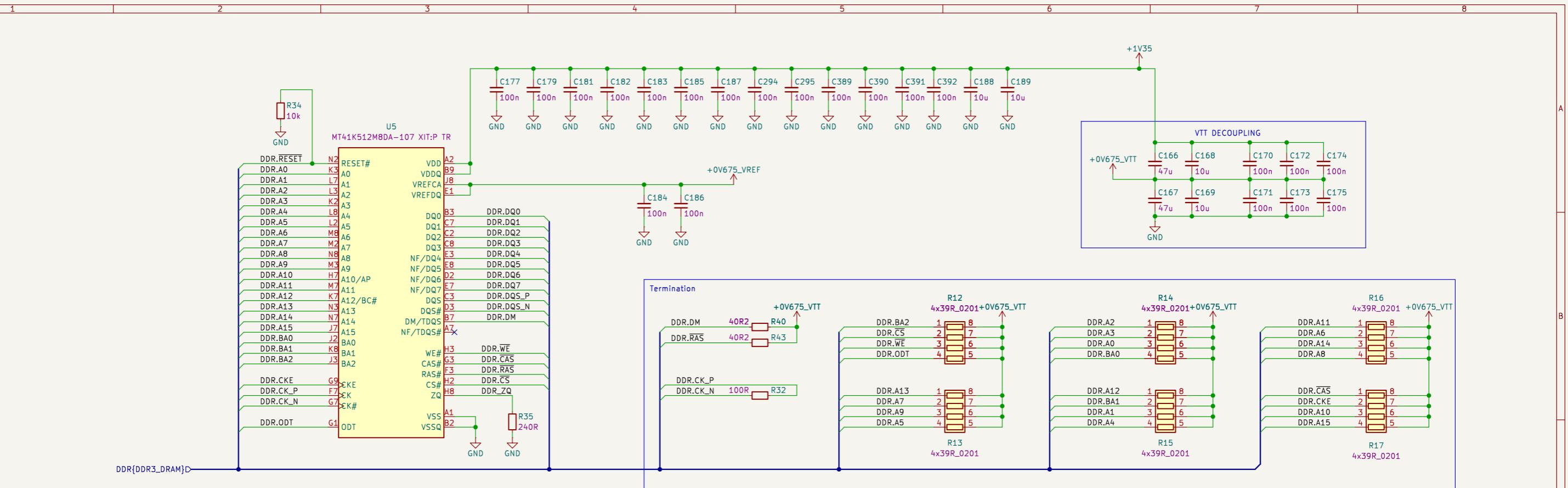


Sheet: /FPGA MGT Interface/
File: fpga-mgt.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.1.2:86f82
Id: 4/19

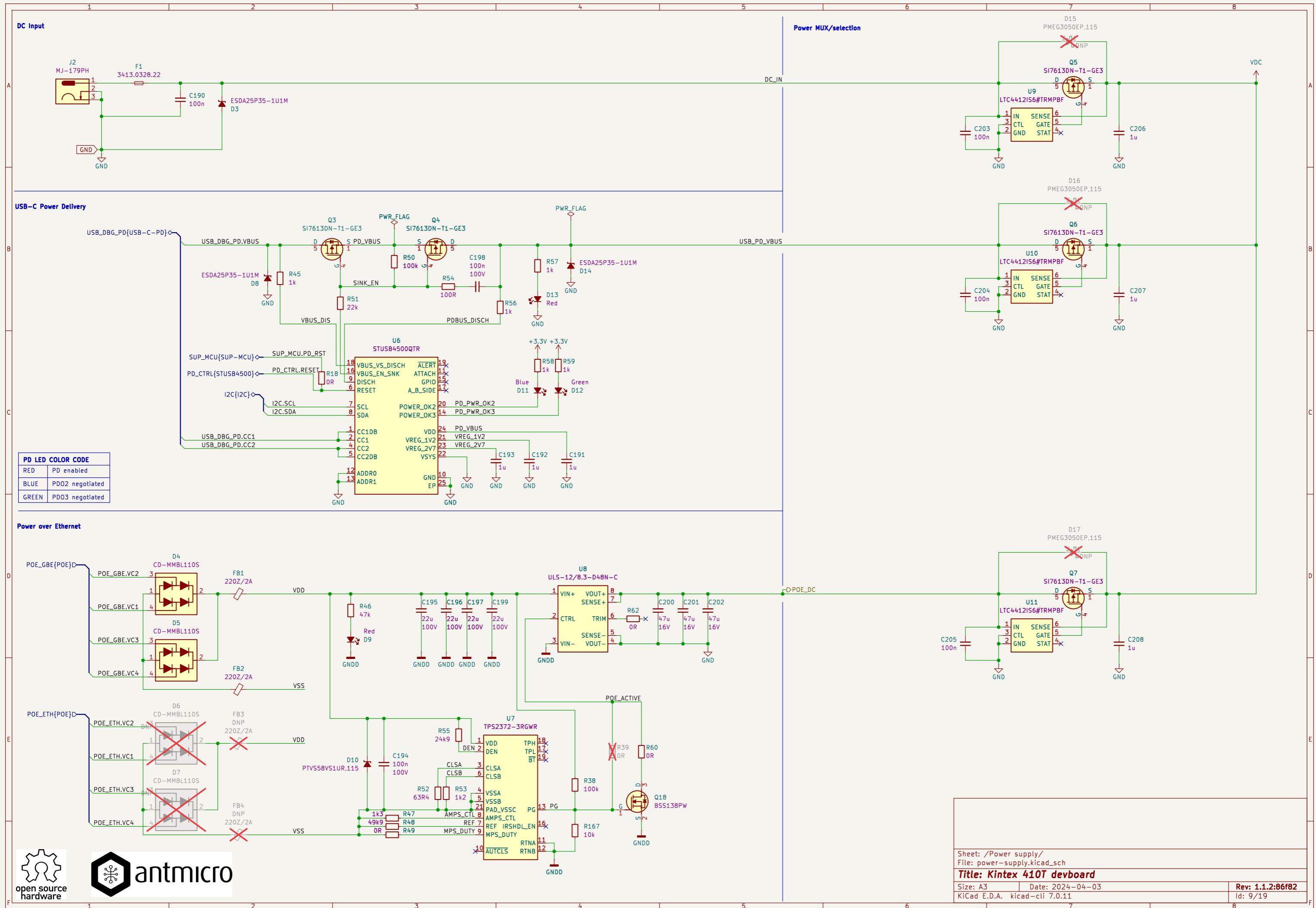


Sheet: /DRAM + SRAM/
File: ram.kicad_sch

Title: Kintex 410T devboard

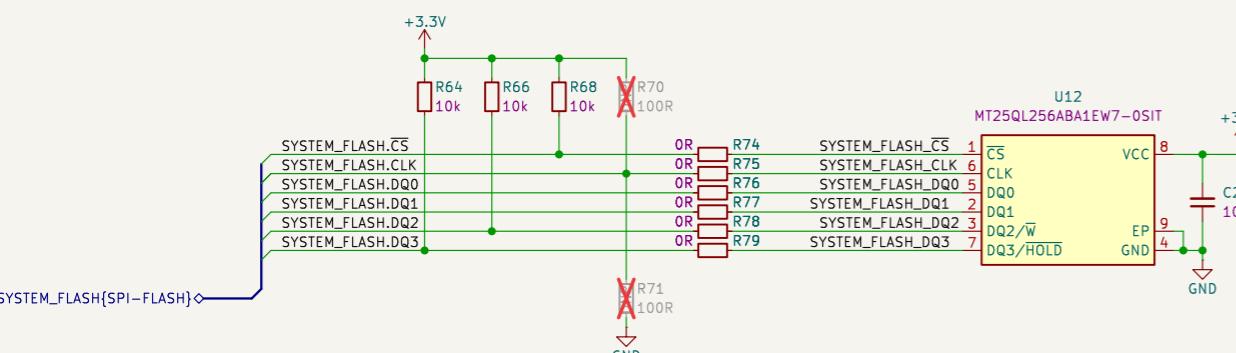
Size: A3 Date: 2024-04-03
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.2:86f82
Id: 8/19

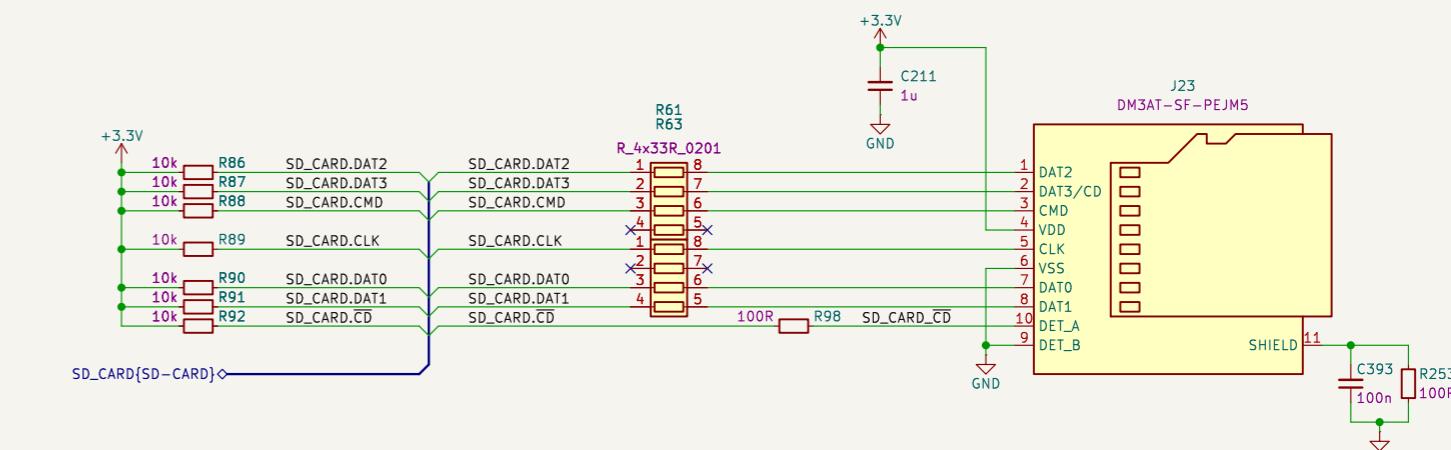


1 2 3 4 5 6 7 8

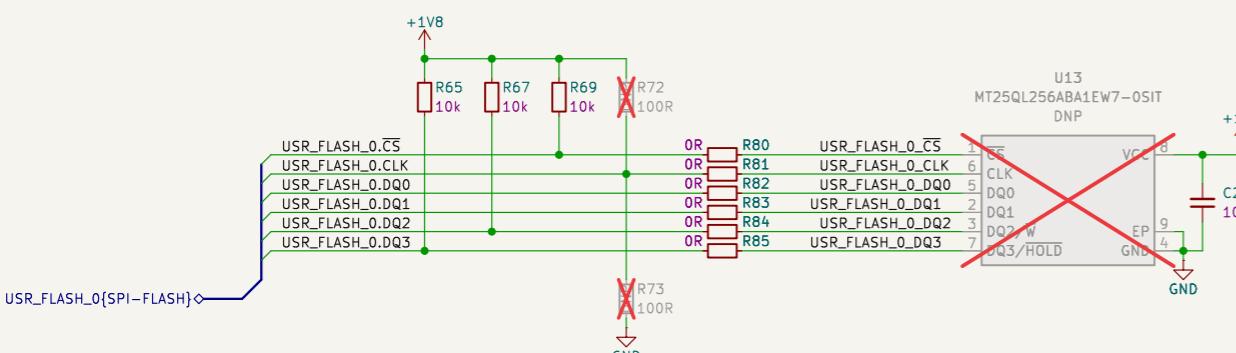
(Q)SPI flash

Master SPI Quad (x4) configuration schemeFollows Figure 2-14 7 Series FPGAs Configuration User Guide
UG470 (v1.15)

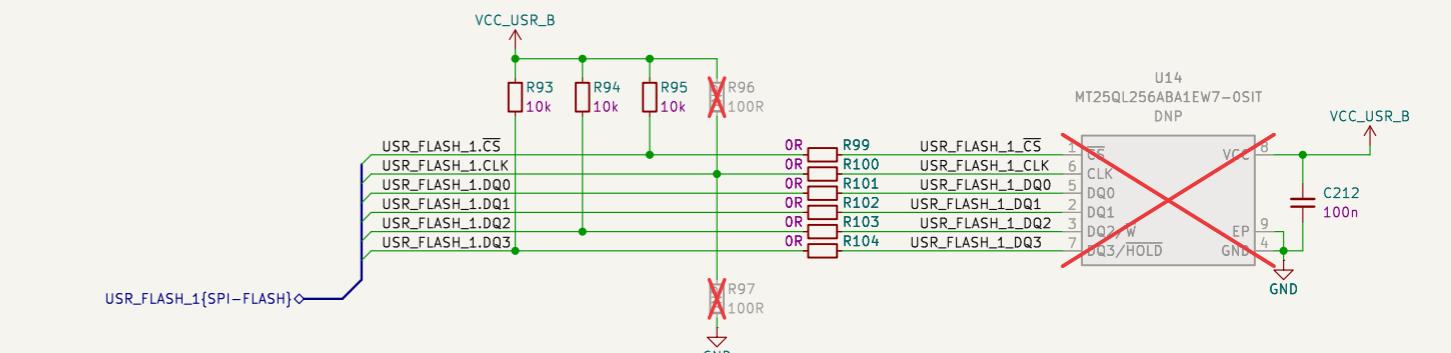
USER SD CARD



USER SPI flash 0



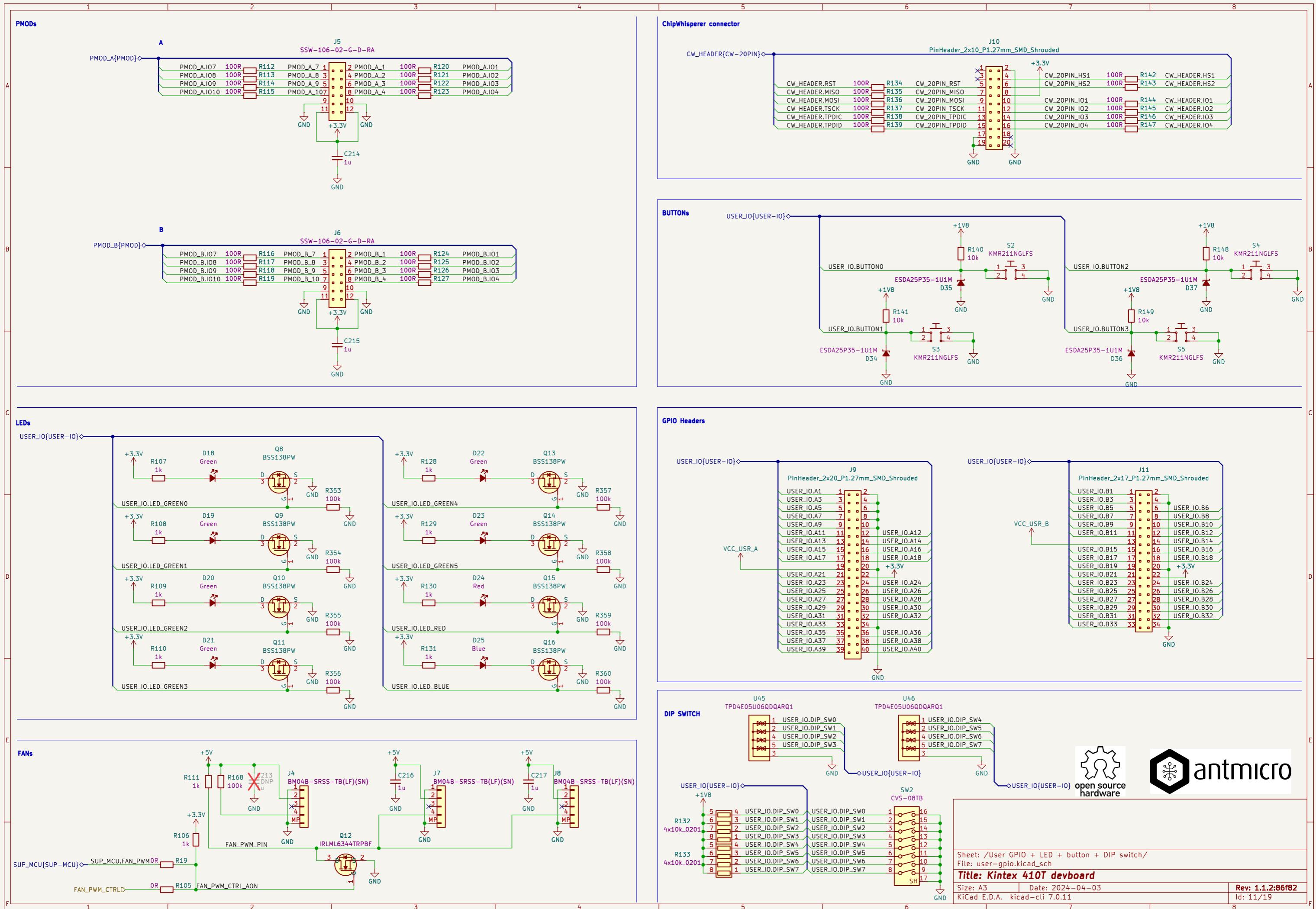
USER SPI flash 1

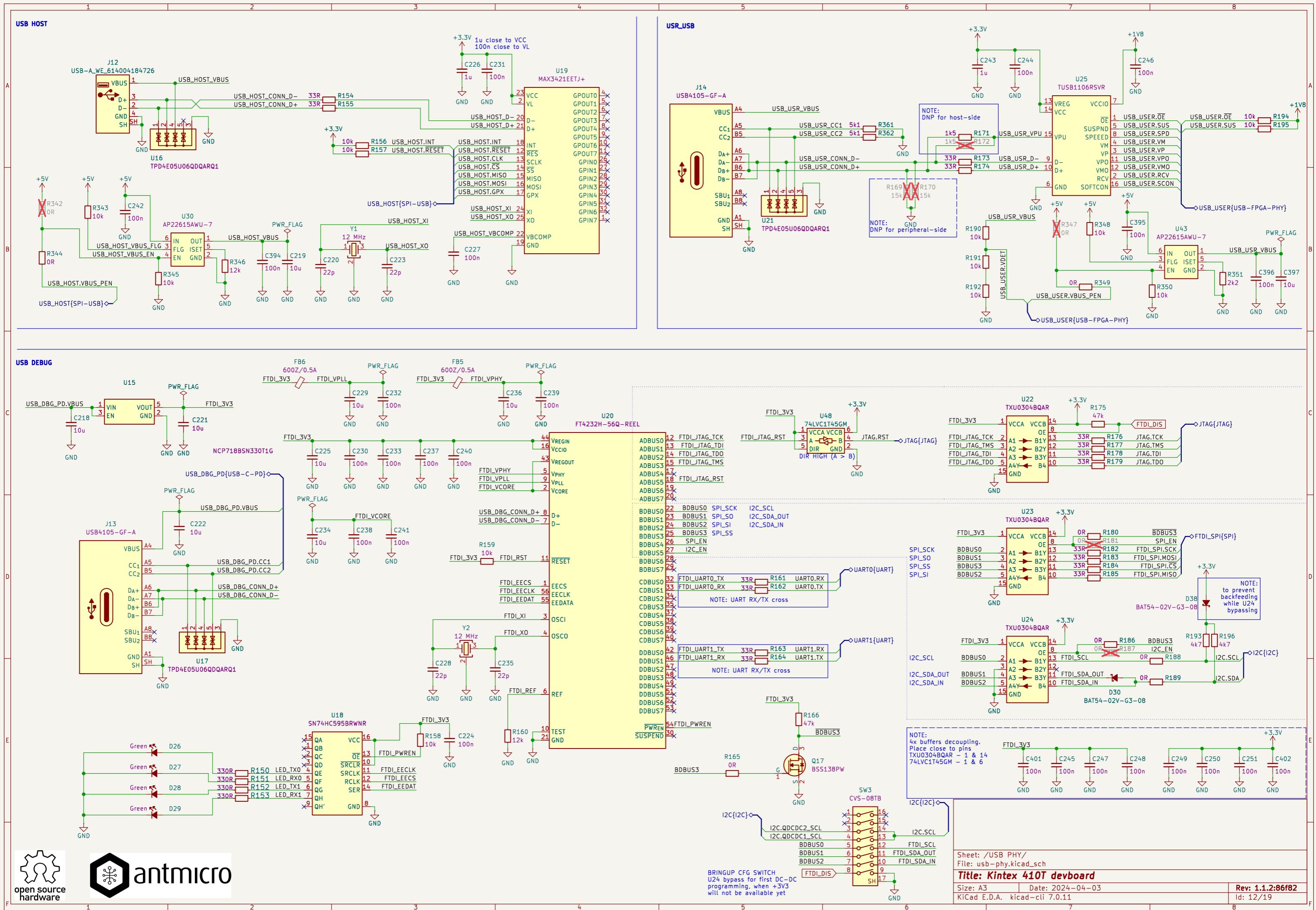


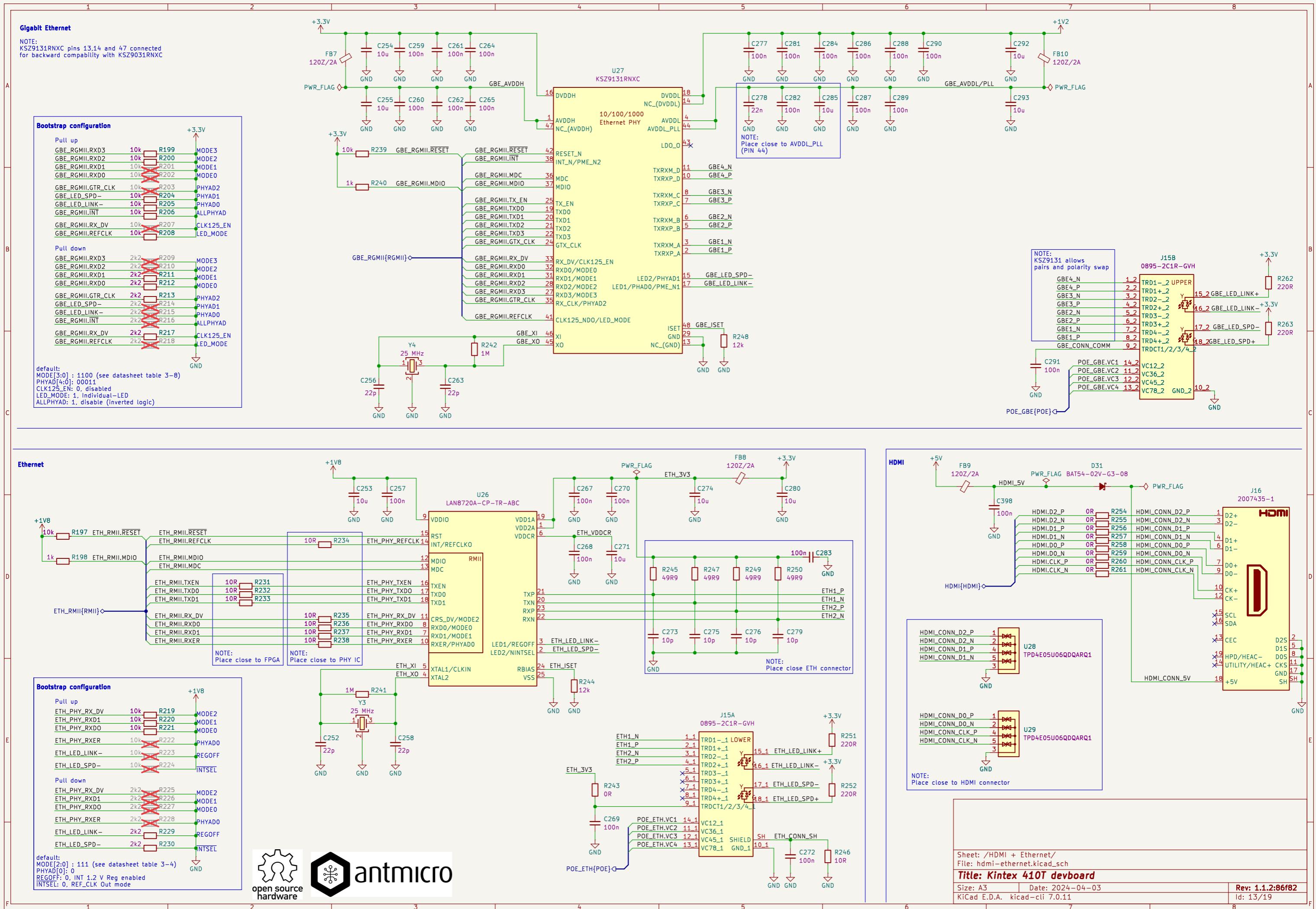
antmicro

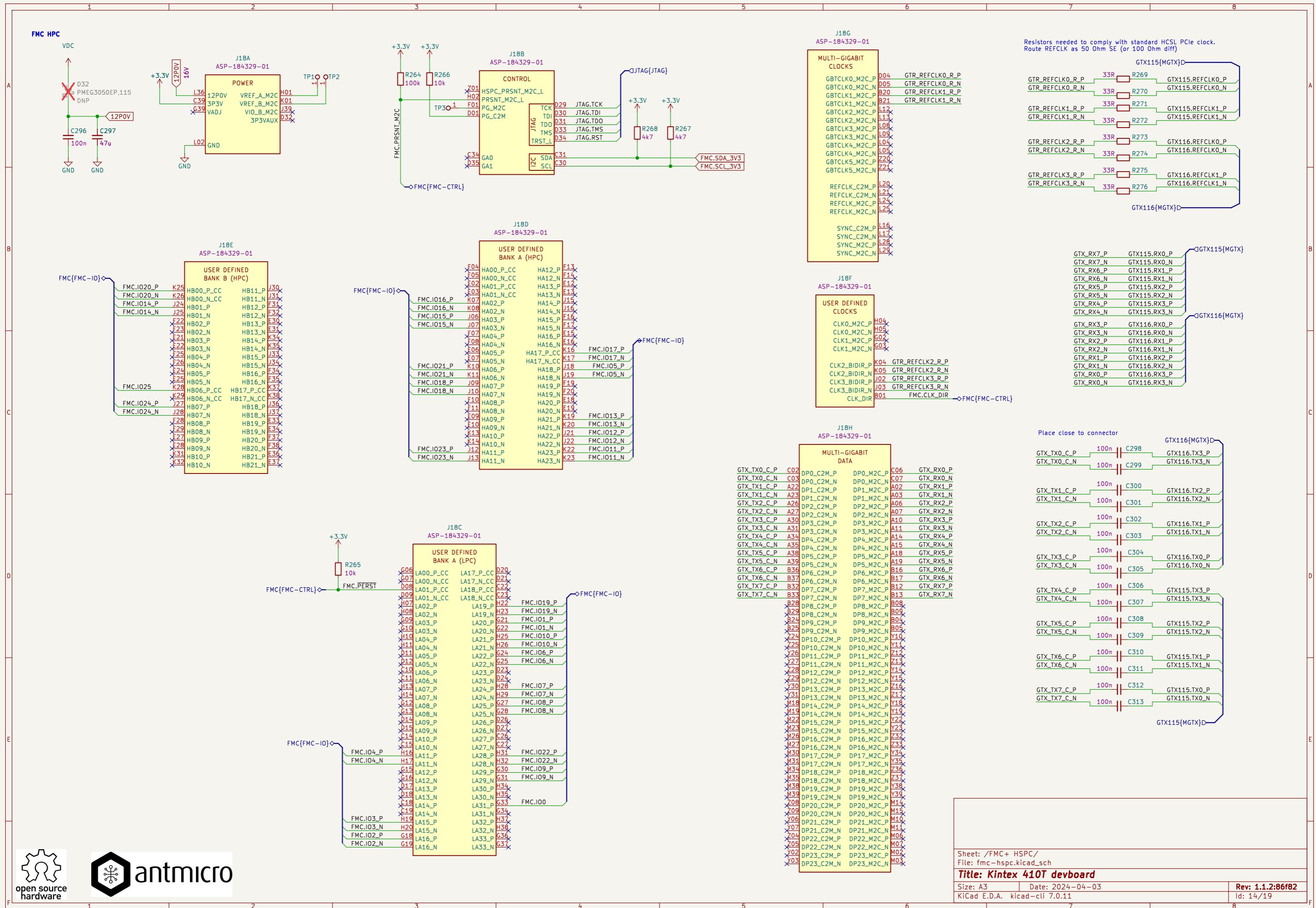
Sheet: /SPI Flash + SD Card/
File: spi-flash.kicad_sch**Title: Kintex 410T devboard**Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cl 7.0.11Rev: 1.1.2:86f82
Id: 10/19

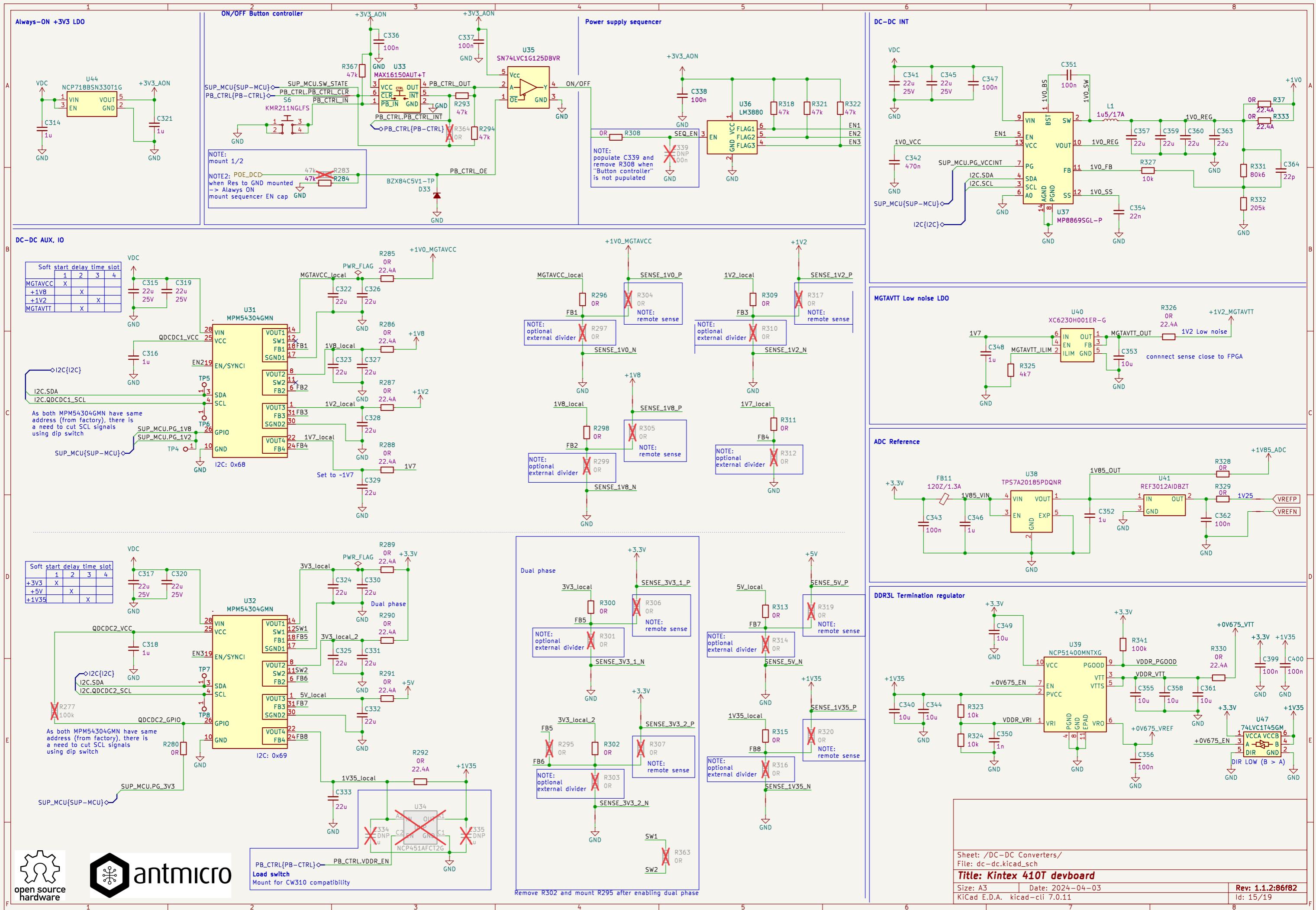
1 2 3 4 5 6 7 8











Main system clock

A

A

B

B

C

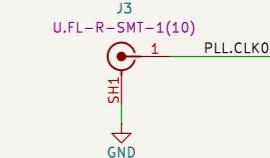
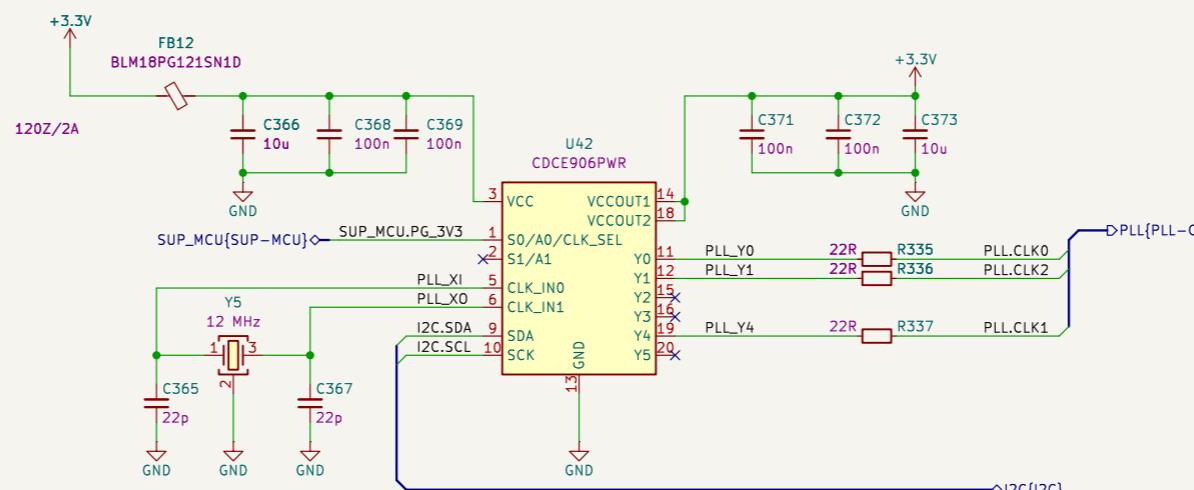
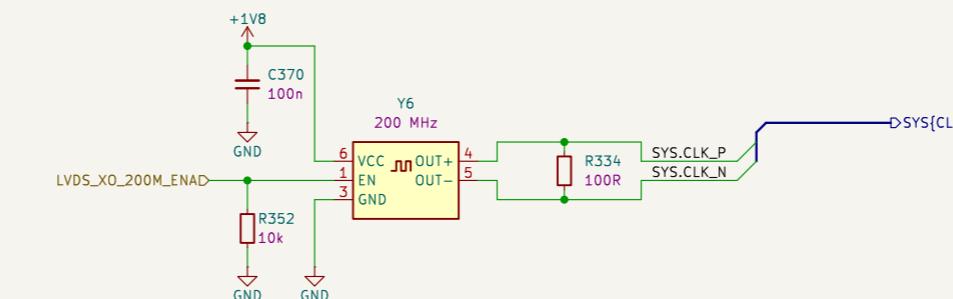
C

D

D

E

E

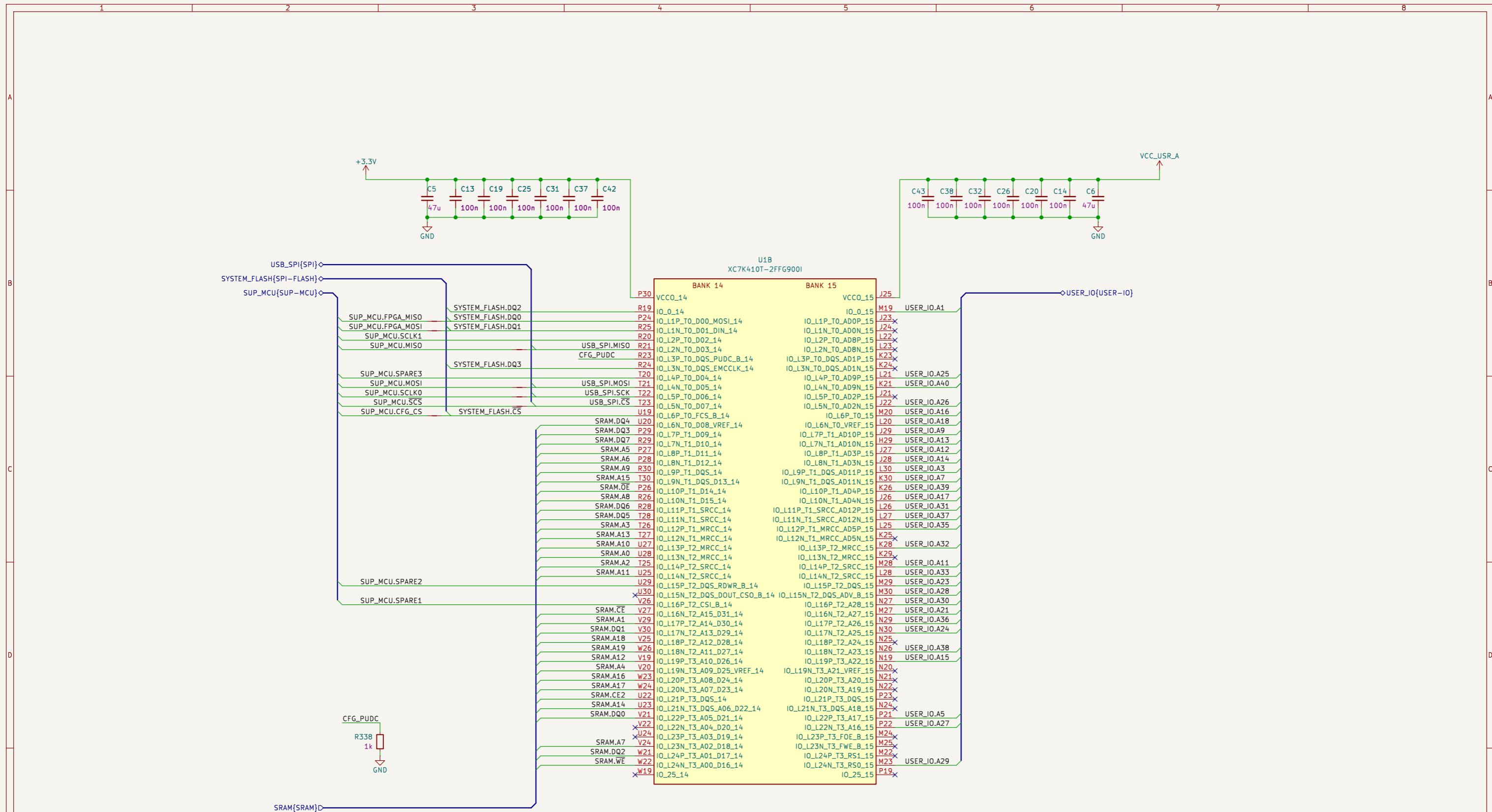


Sheet: /Clocks/
File: clocks.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cliv 7.0.11

Rev: 1.1.2:86f82 | Id: 16/19

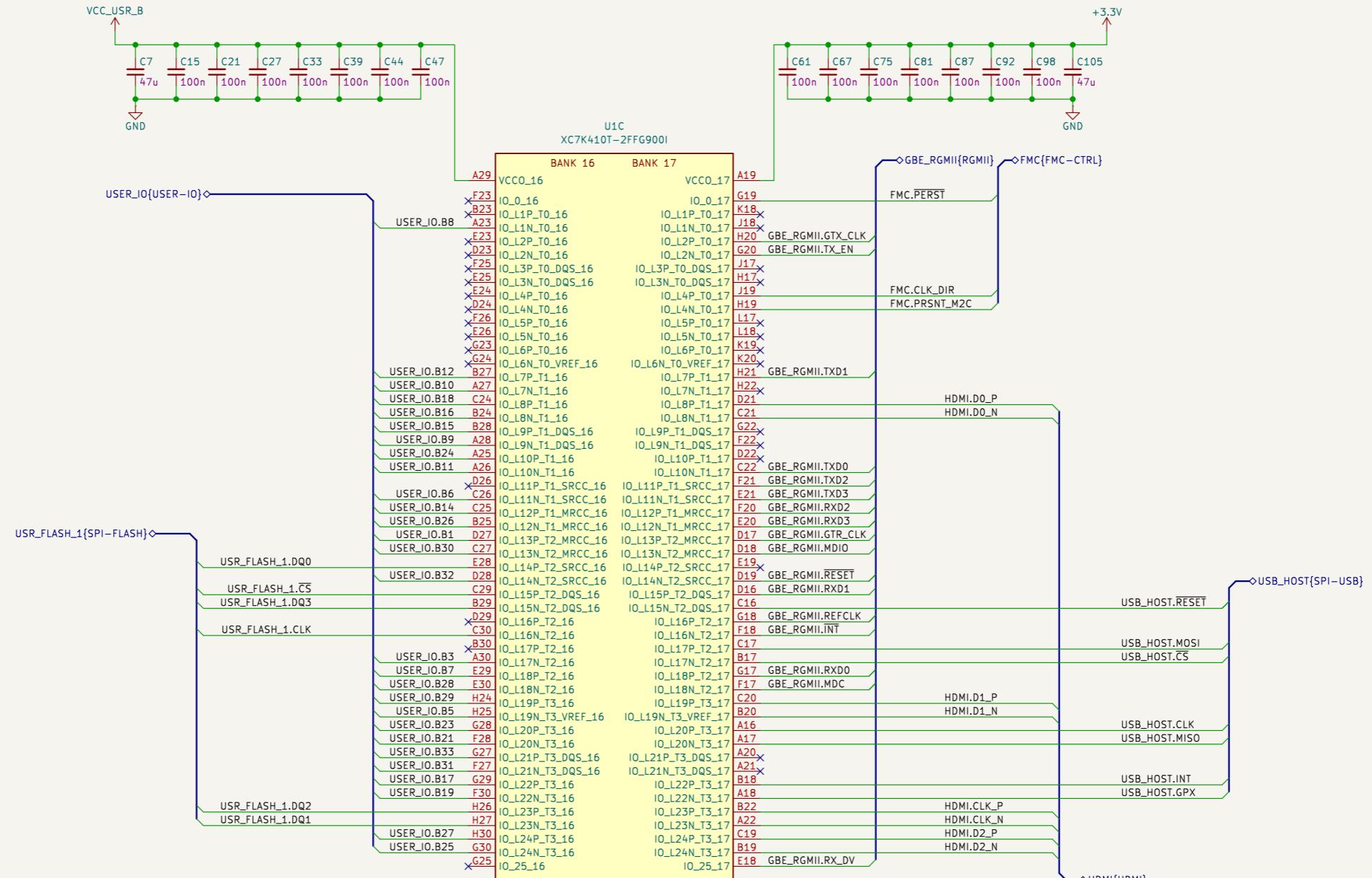


Sheet: /FPGA Bank 14 & 15/
File: fpga-bank-14-15.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2024-04-03
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.2:86f82
Id: 18/19



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Sheet: /FPGA Bank 16 & 17/
File: fpga-bank-16-17.kicad_sch

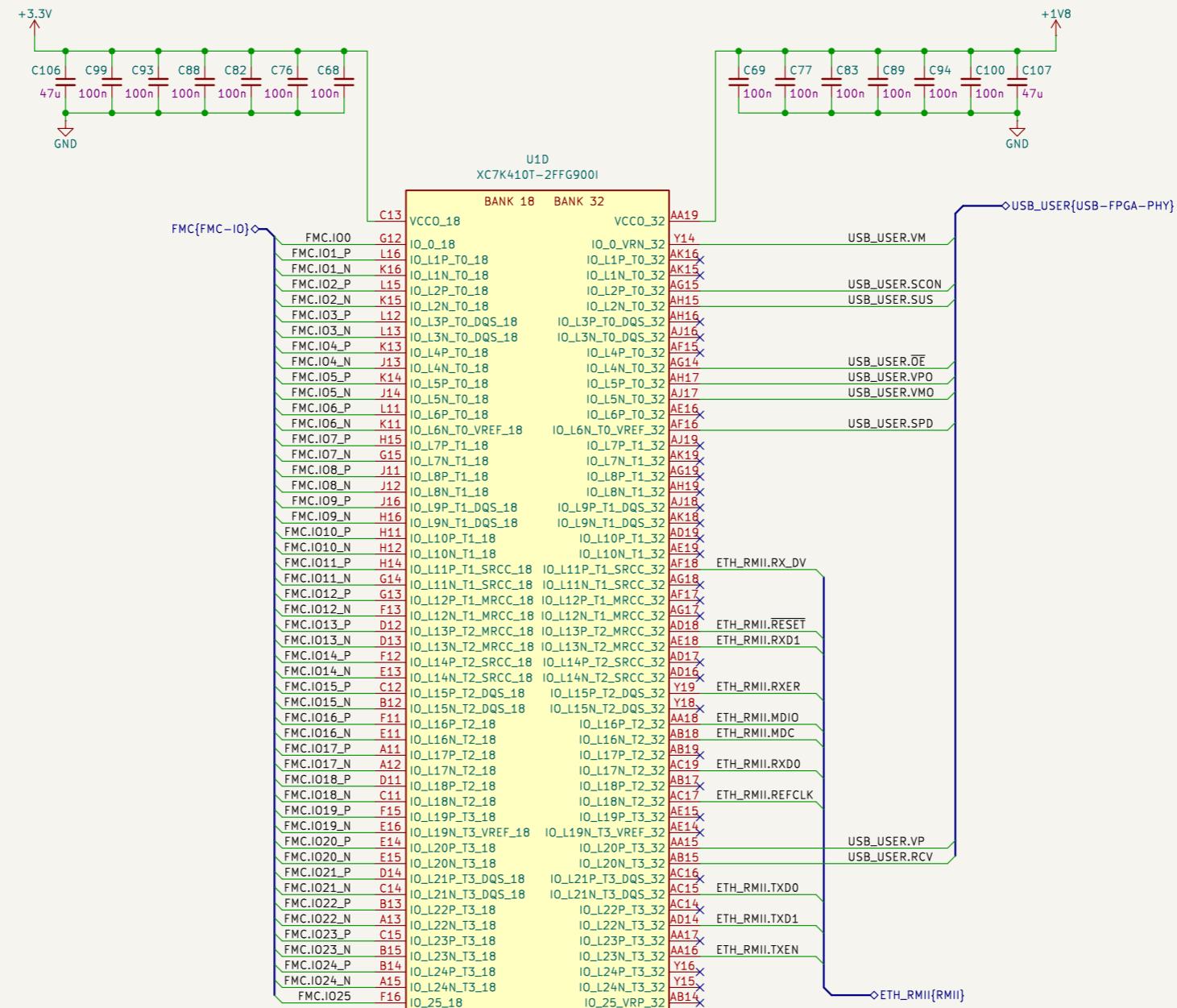
Title: Kintex 410T devboard

Size: A3 Date: 2024-04-03
Kodak E.D.A. KODAK EASYSHARE Z7011

KiCad E.D.A. kicad-cli 7.0.11

Id: 20/19

A



Sheet: /FPGA Bank 18 & 32/
File: fpga-bank-18-32.kicad_sch

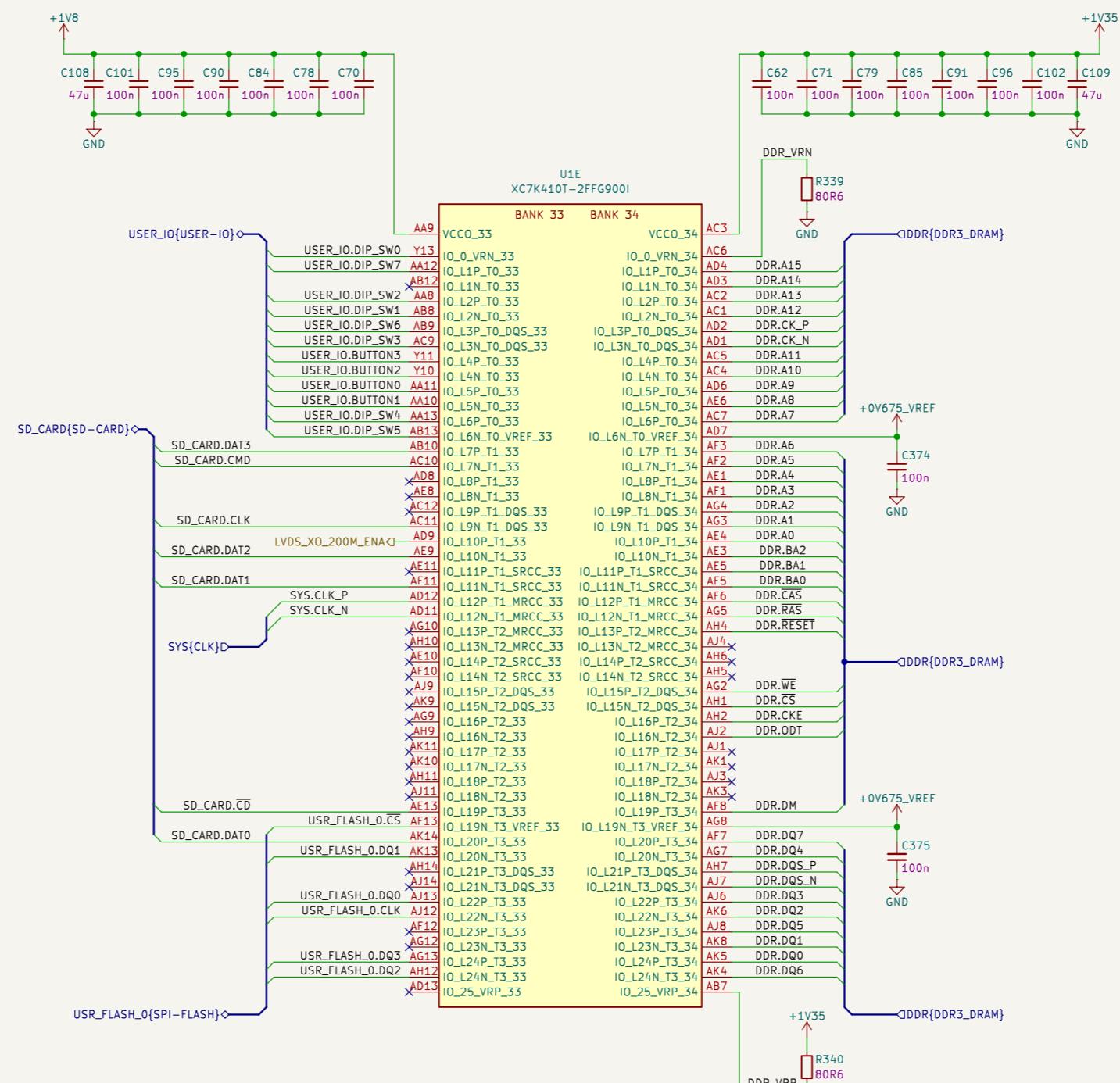
Title: Kintex 410T devboard

Size: A3 Date: 2024-04-03

KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.1.2:86f82

Id: 23/19



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Sheet: /FPGA Bank 33 & 34/
File: fpga-bank-33-34.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2024-04-03

KiCad E.D.A. kicad-cli 7.0.11

Id: 24/19

Supervisor MCU

A

B

C

D

E

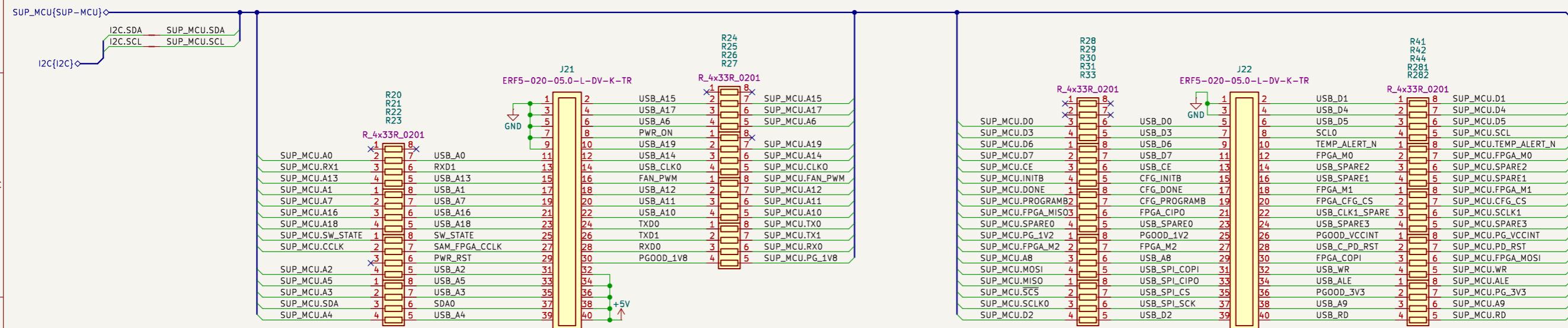
A

B

C

D

E



Sheet: /Supervisor MCU/
File: supervisor-mcu.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2024-04-03
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.2:86f82 | Id: 24/19

