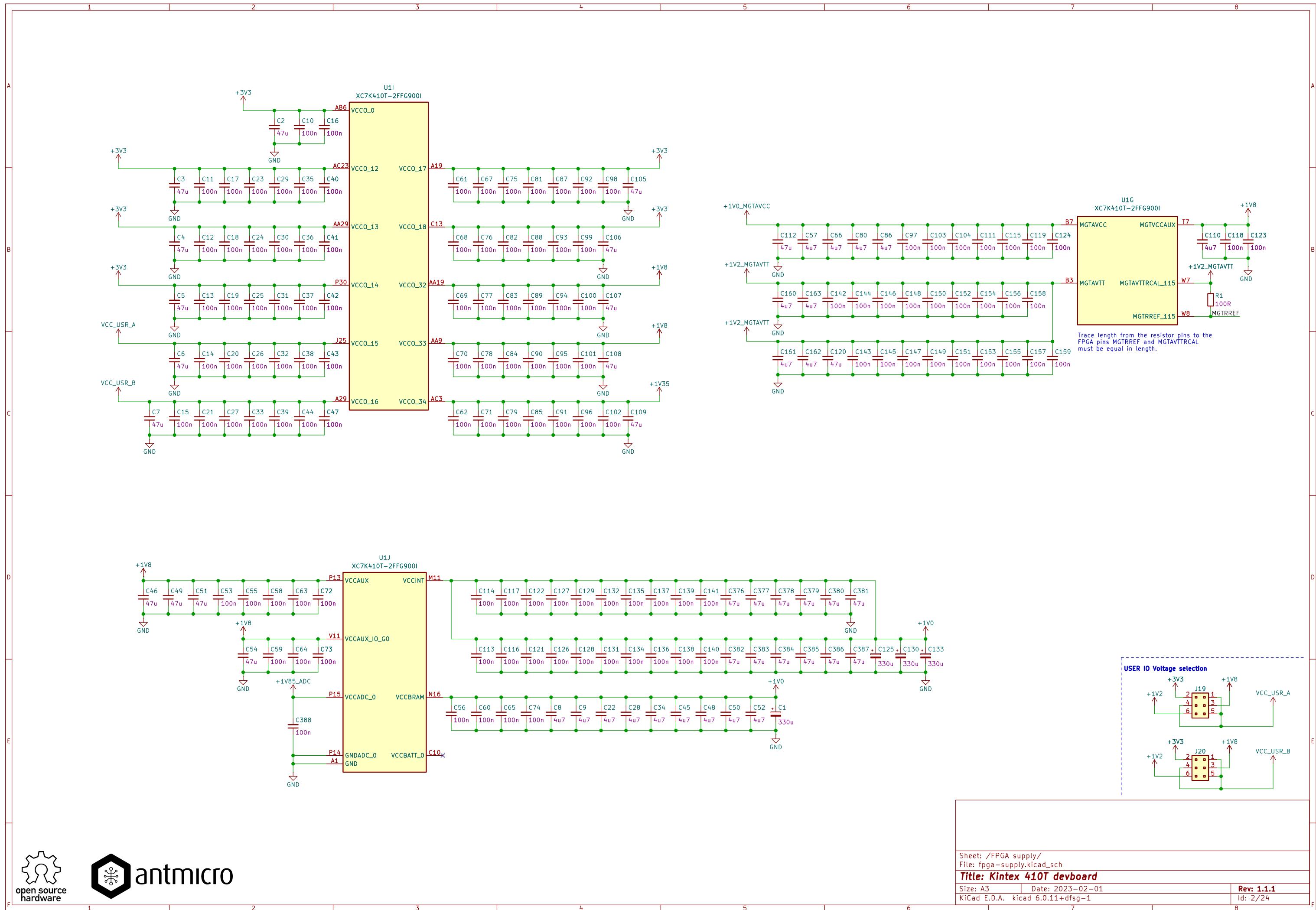
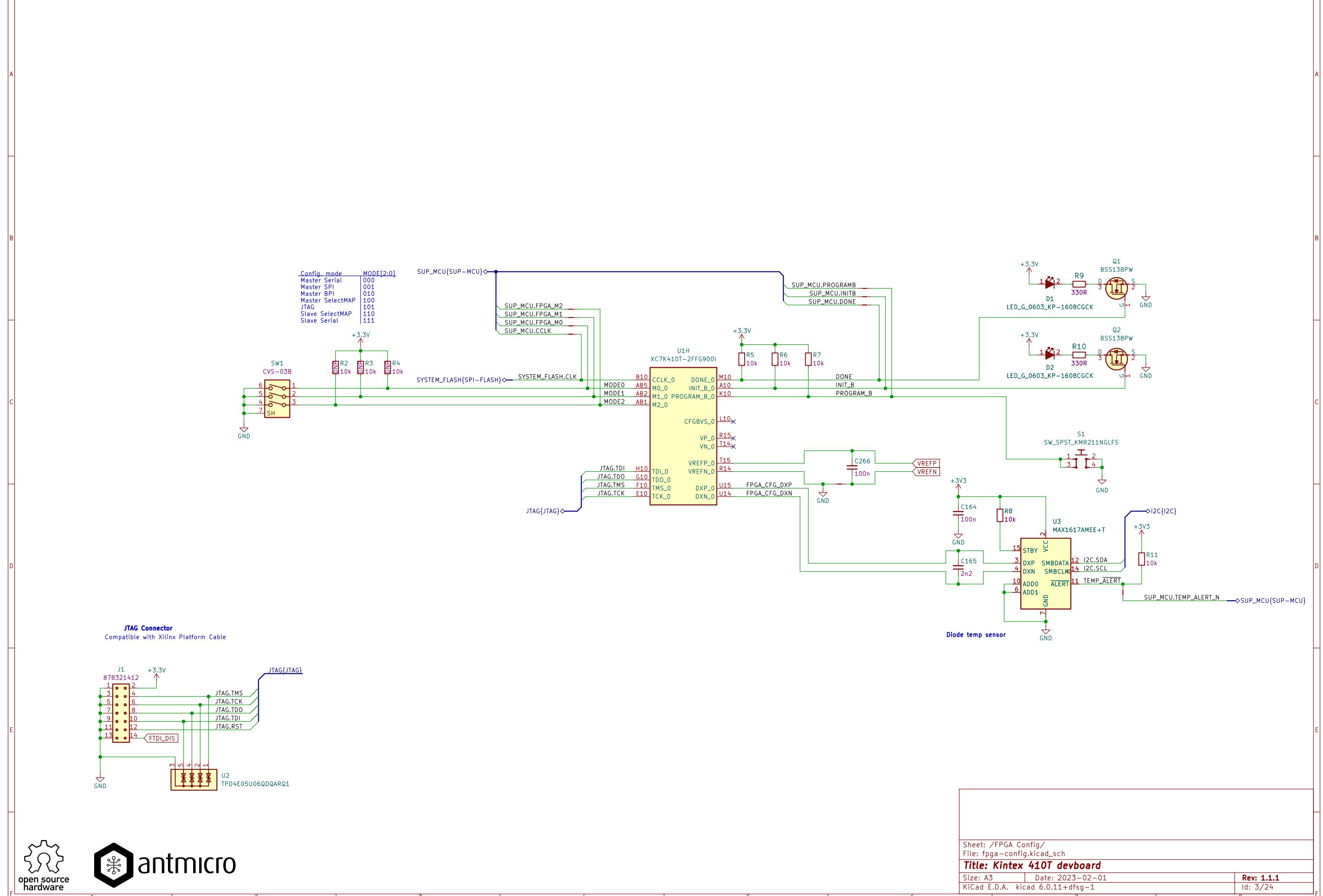


1 2 3 4 5 6 7 8



1 2 3 4 5 6 7 8



A

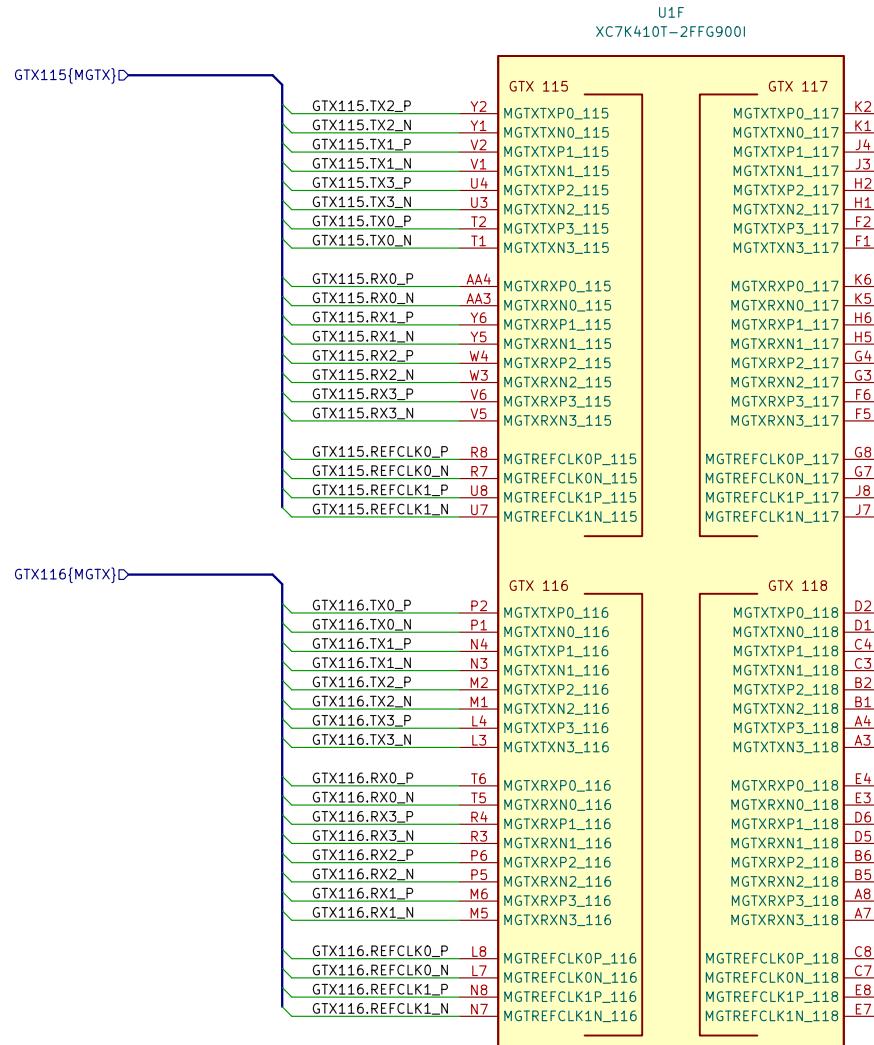
B

C

D

E

F

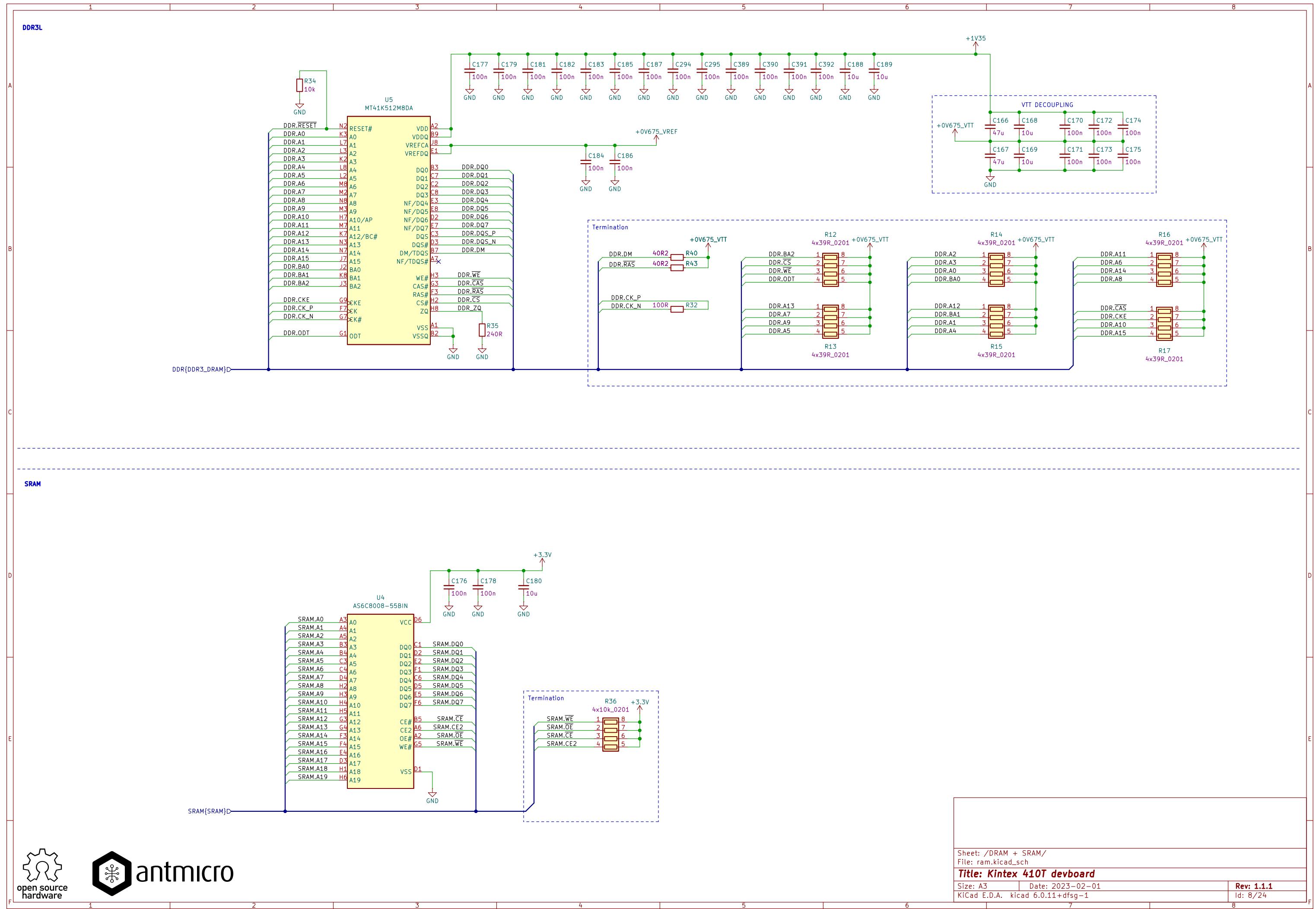
U1F
XC7K410T-2FFG900I

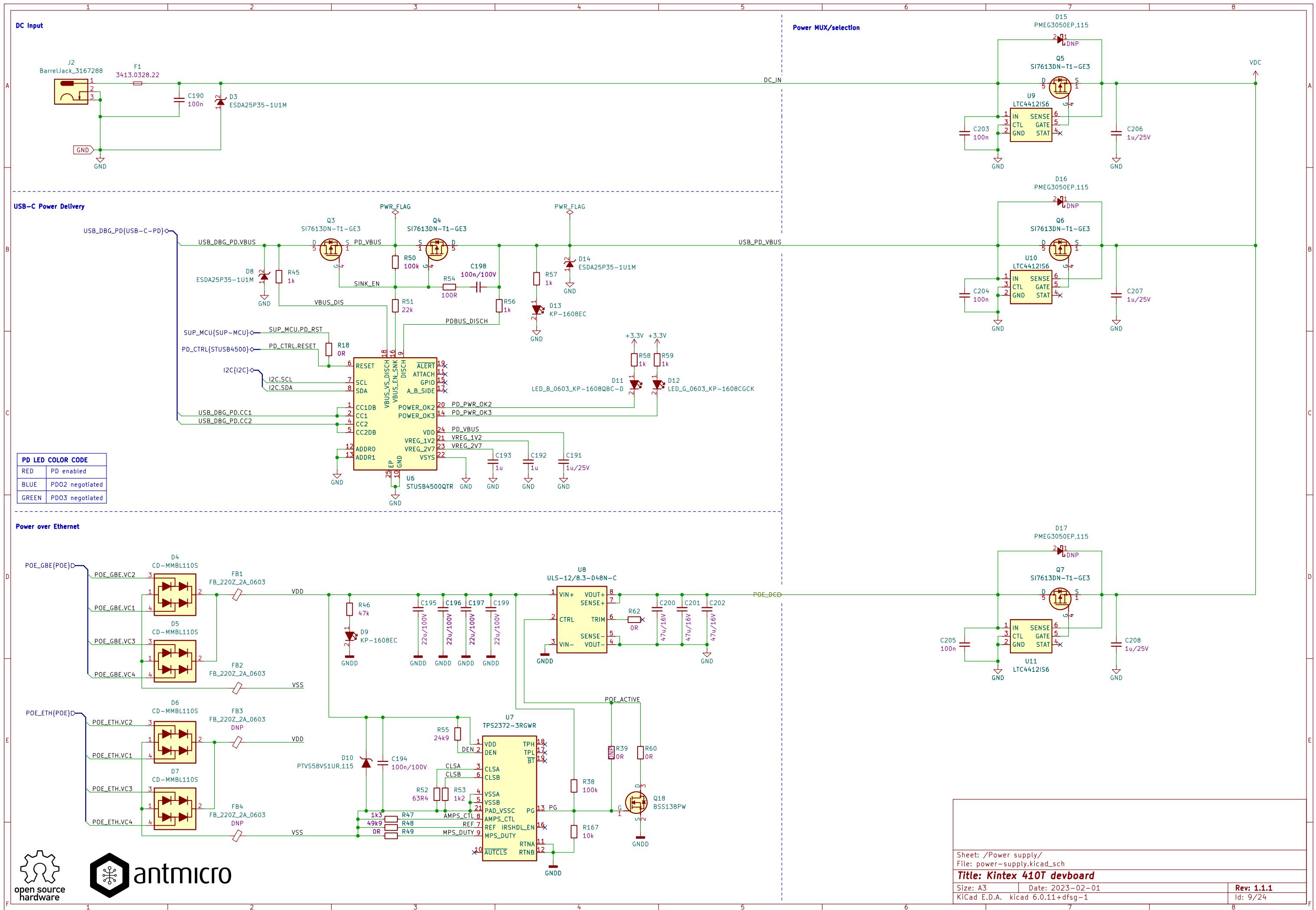
Sheet: /FPGA MGT Interface/
File: fpga-mgt.kicad_sch

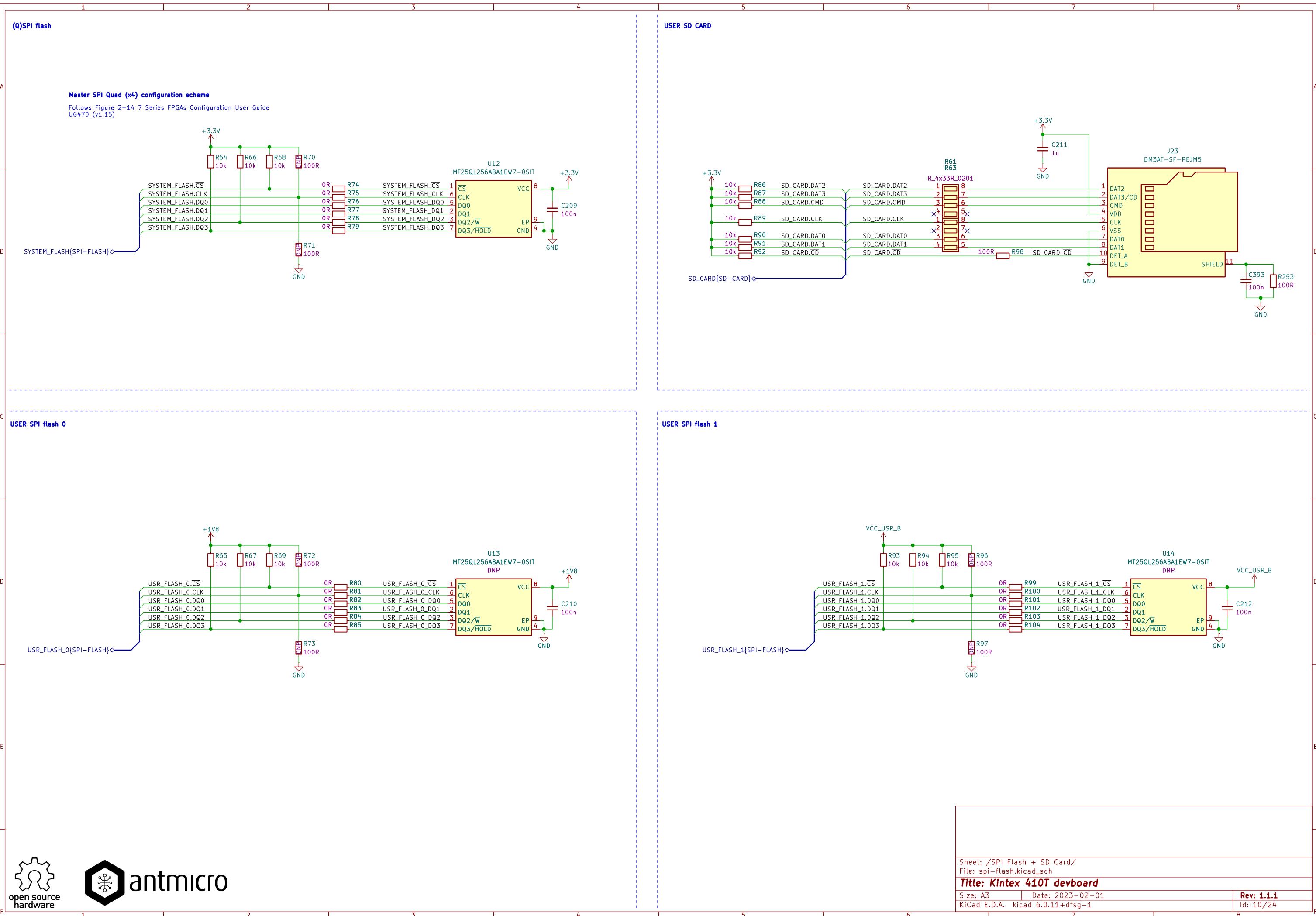
Title: Kintex 410T devboard

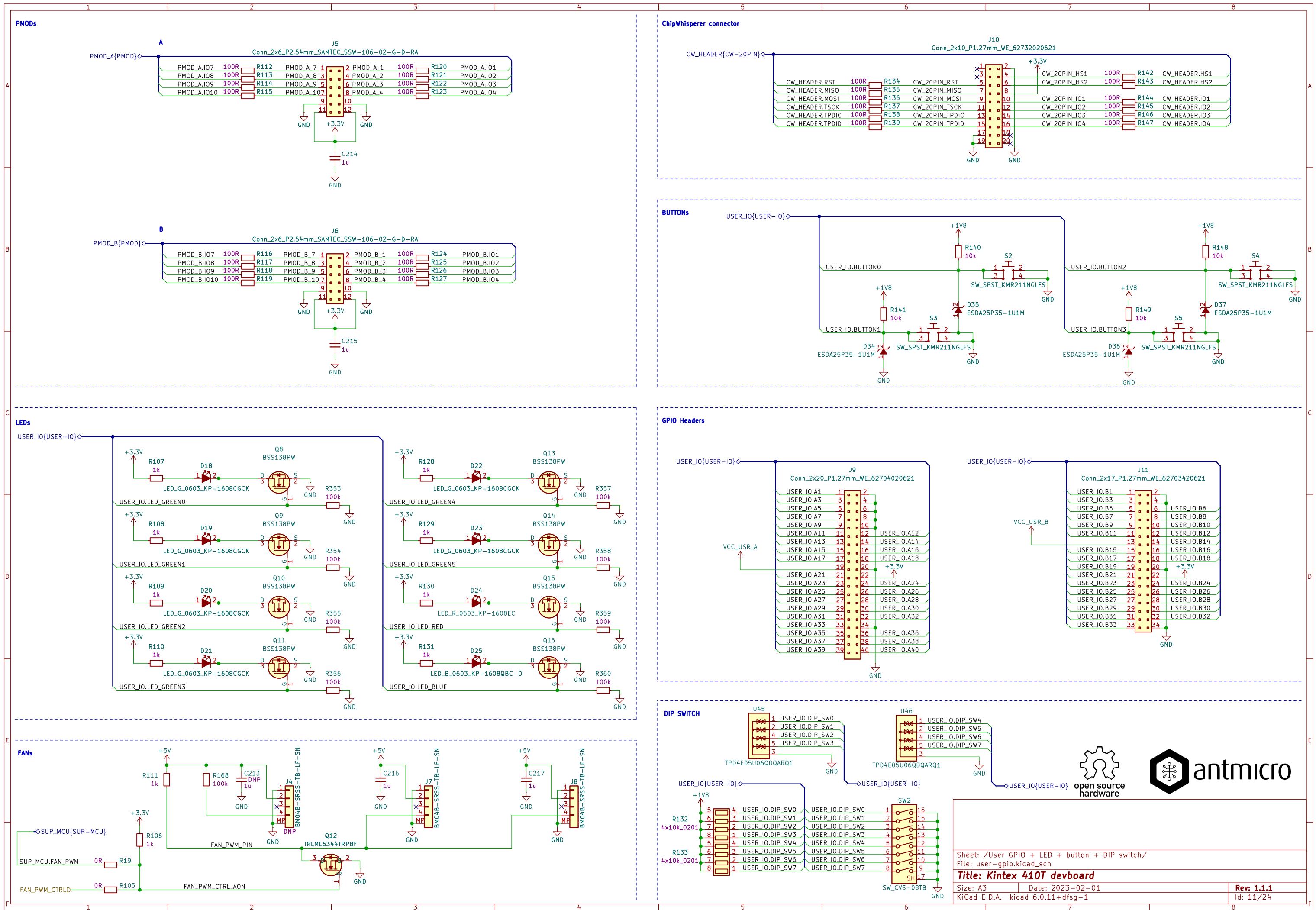
Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

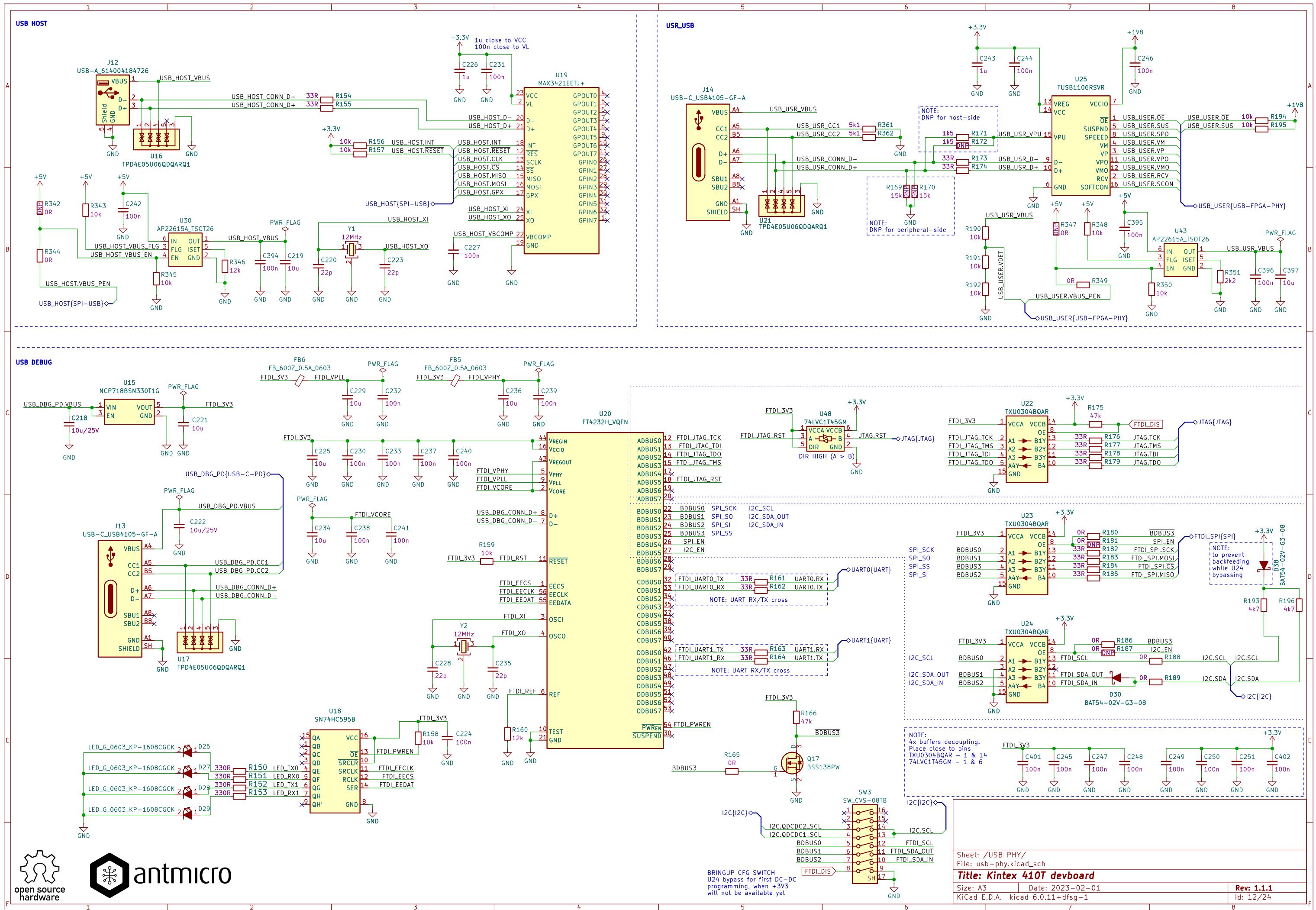
Rev: 1.1.1 | Id: 4/24

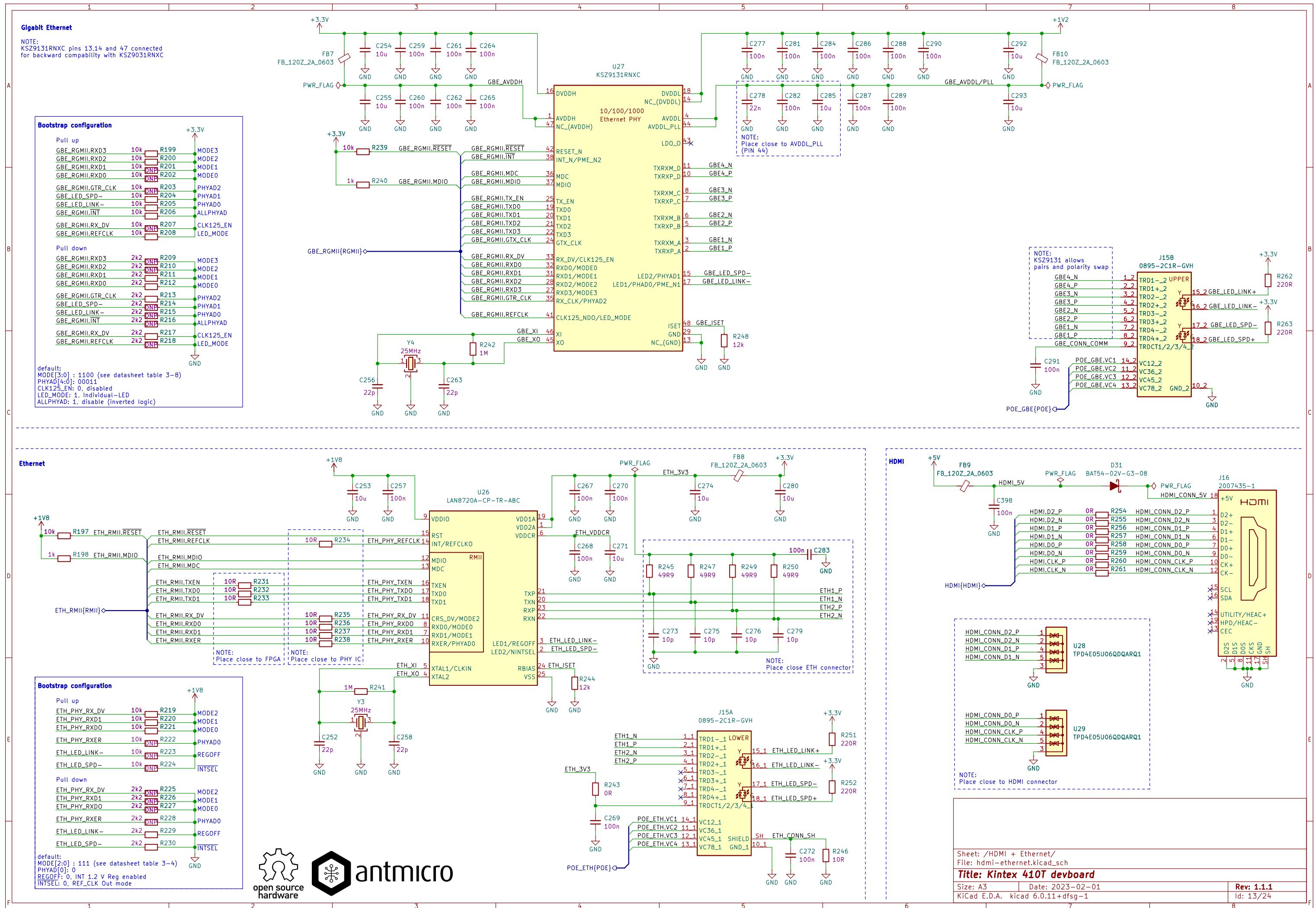


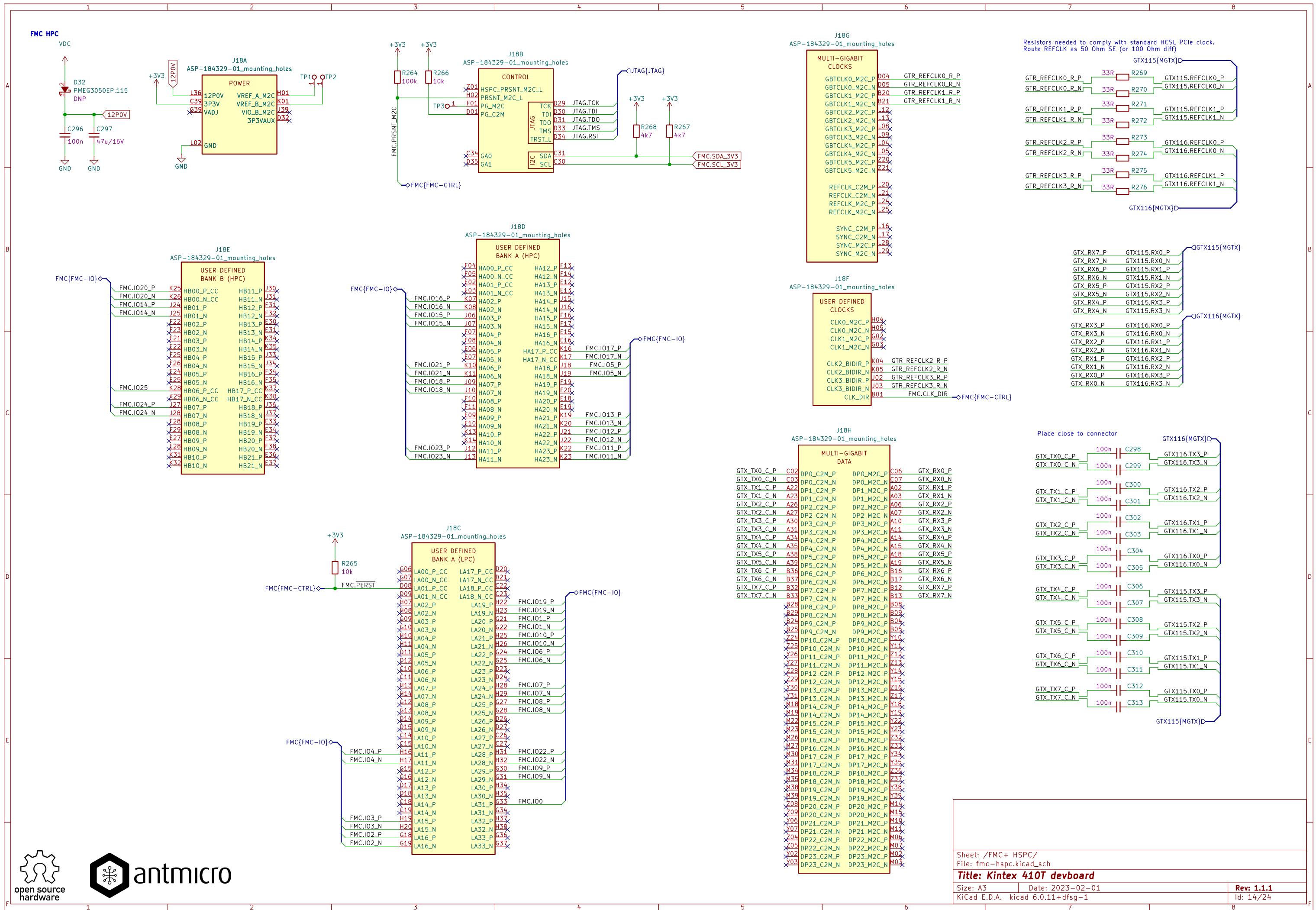


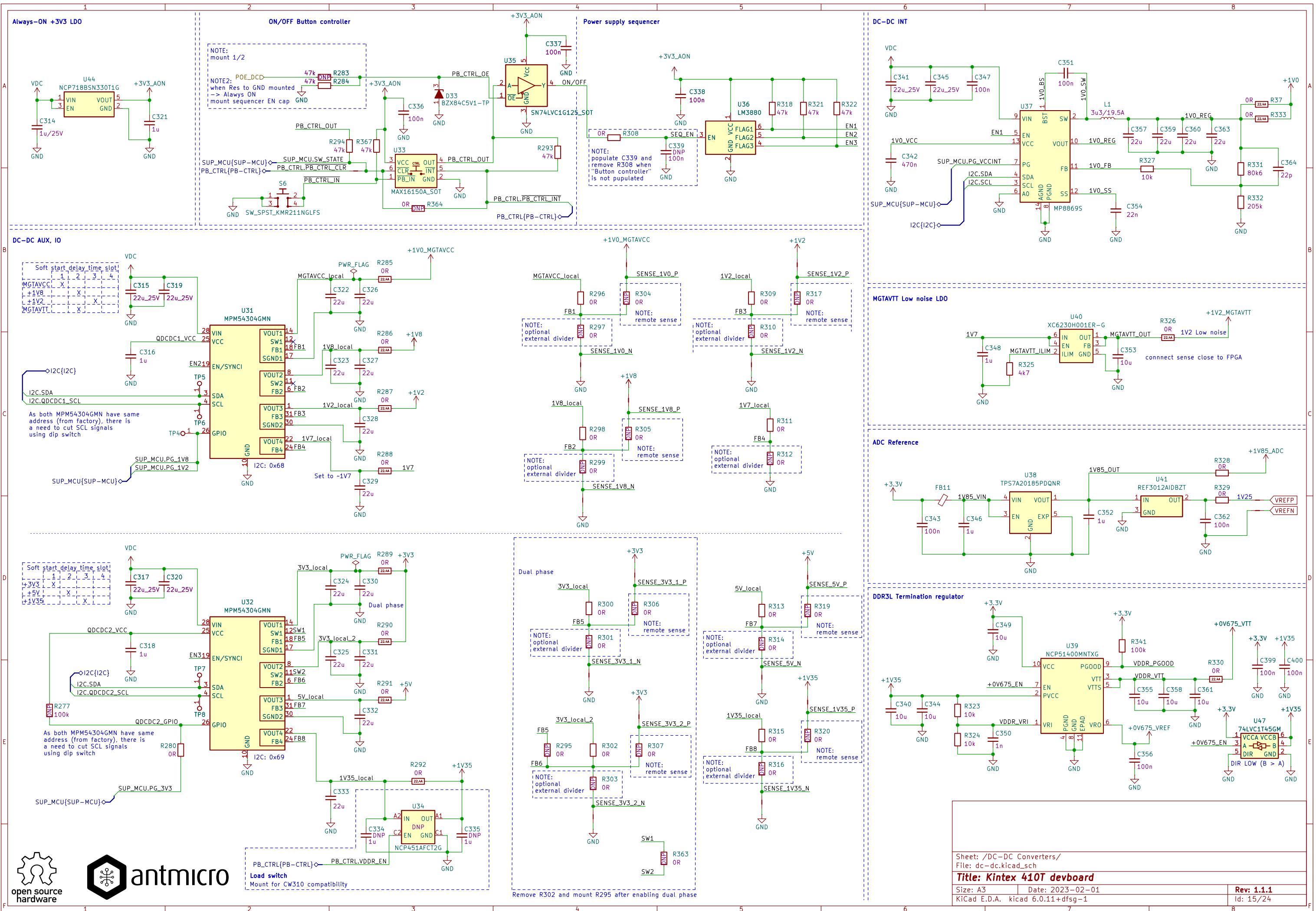












Main system clock

A

A

B

B

C

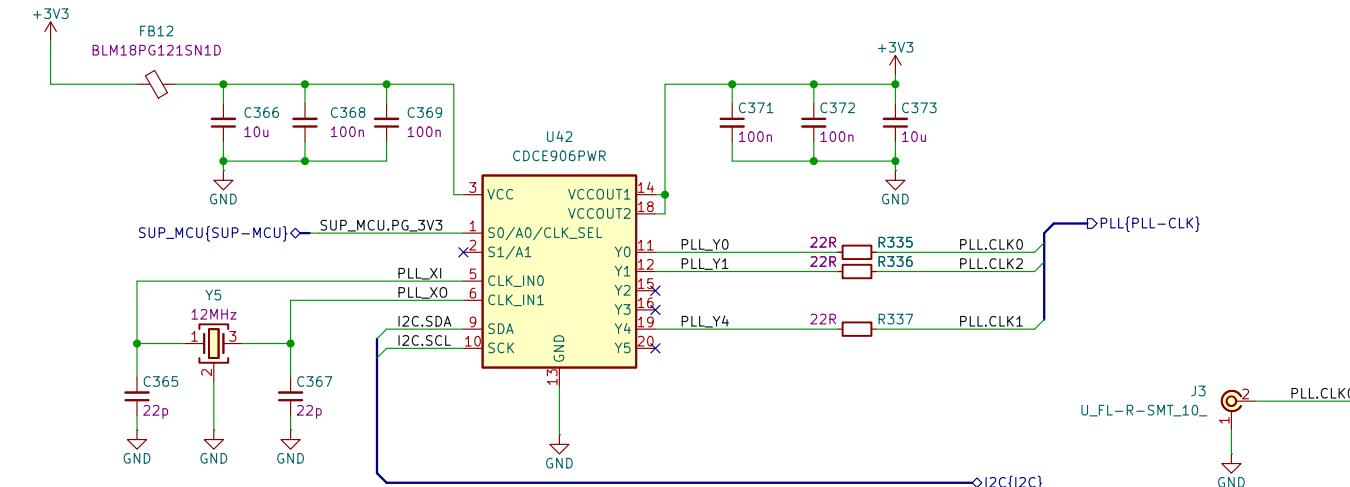
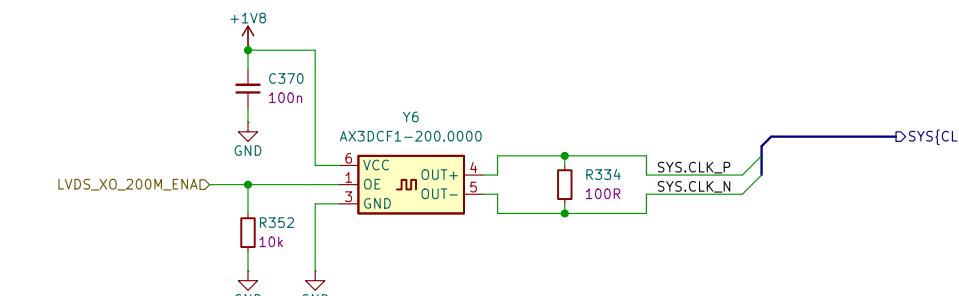
C

D

D

E

E



A

B

C

D

E

F

A

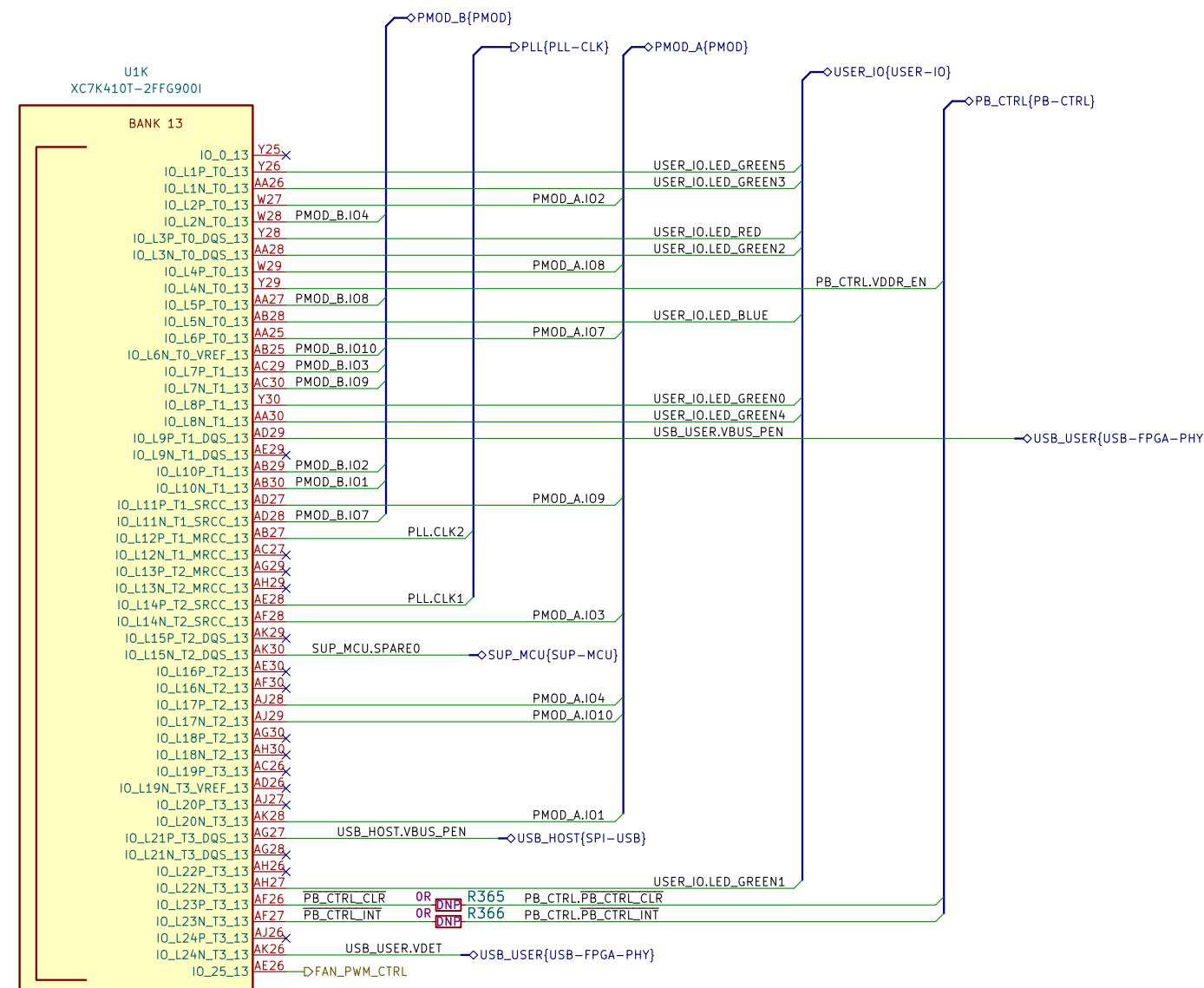
B

C

D

E

F

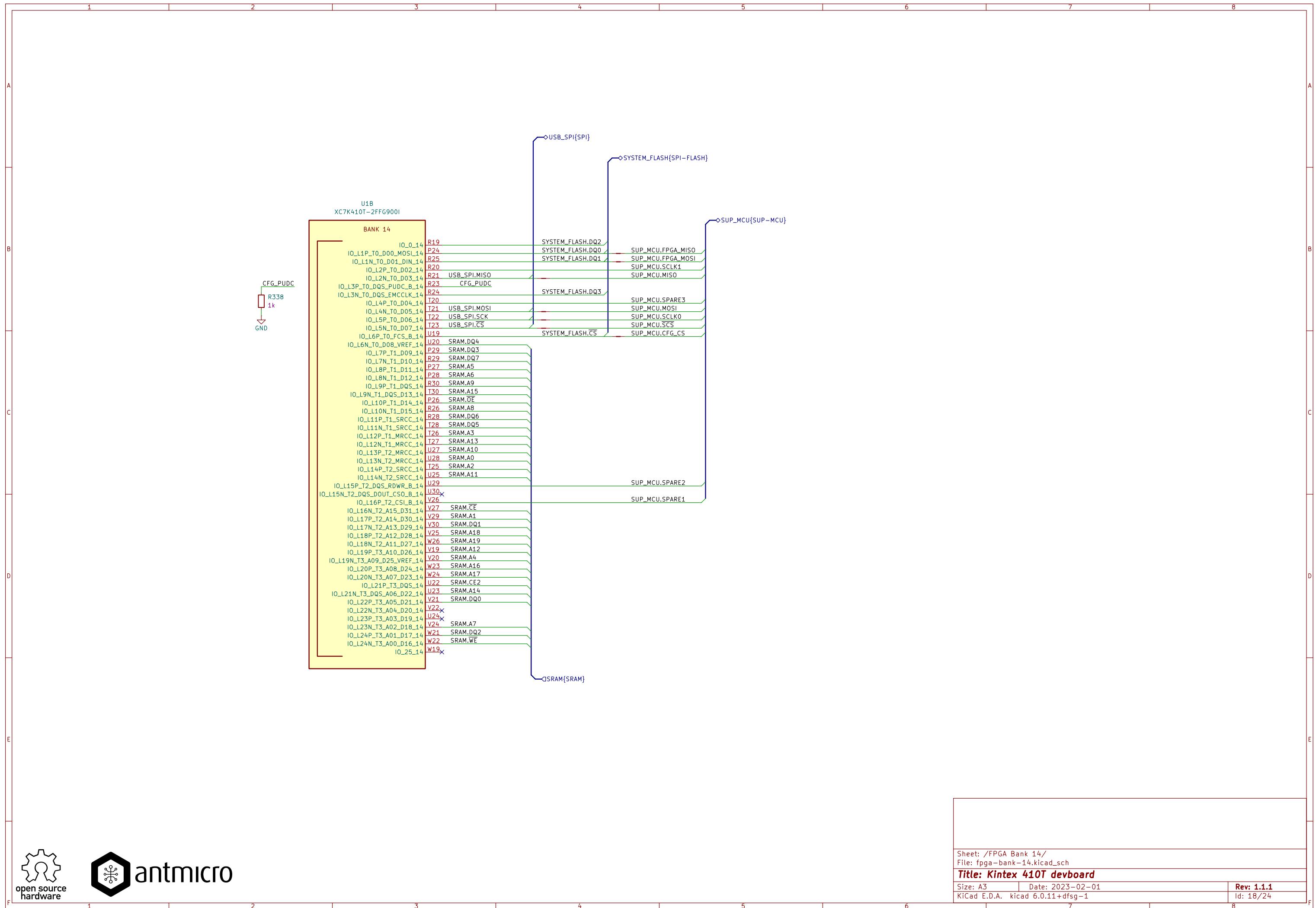


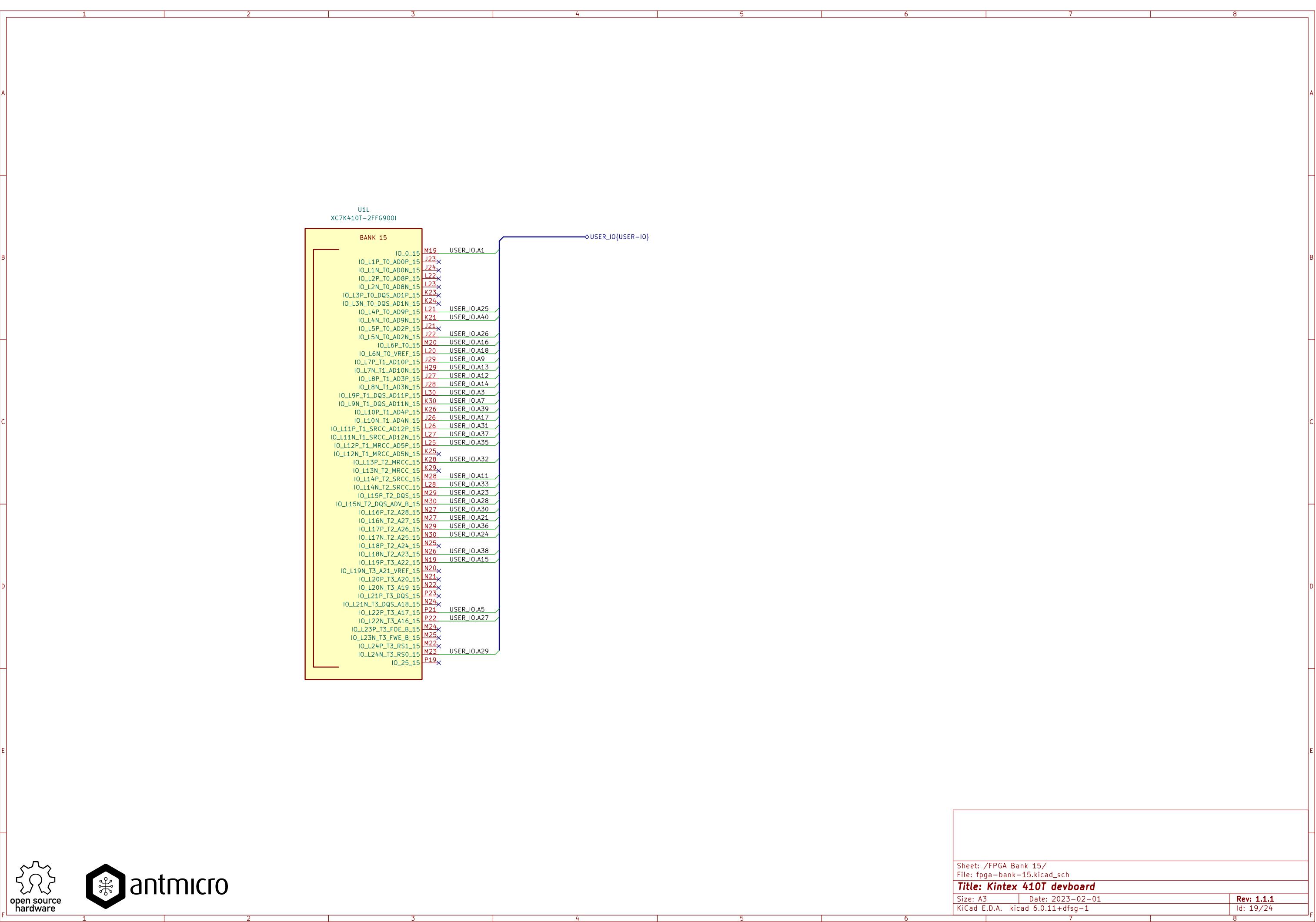
Sheet: /FPGA Bank 13/
File: fpga-bank-13.kicad_sch

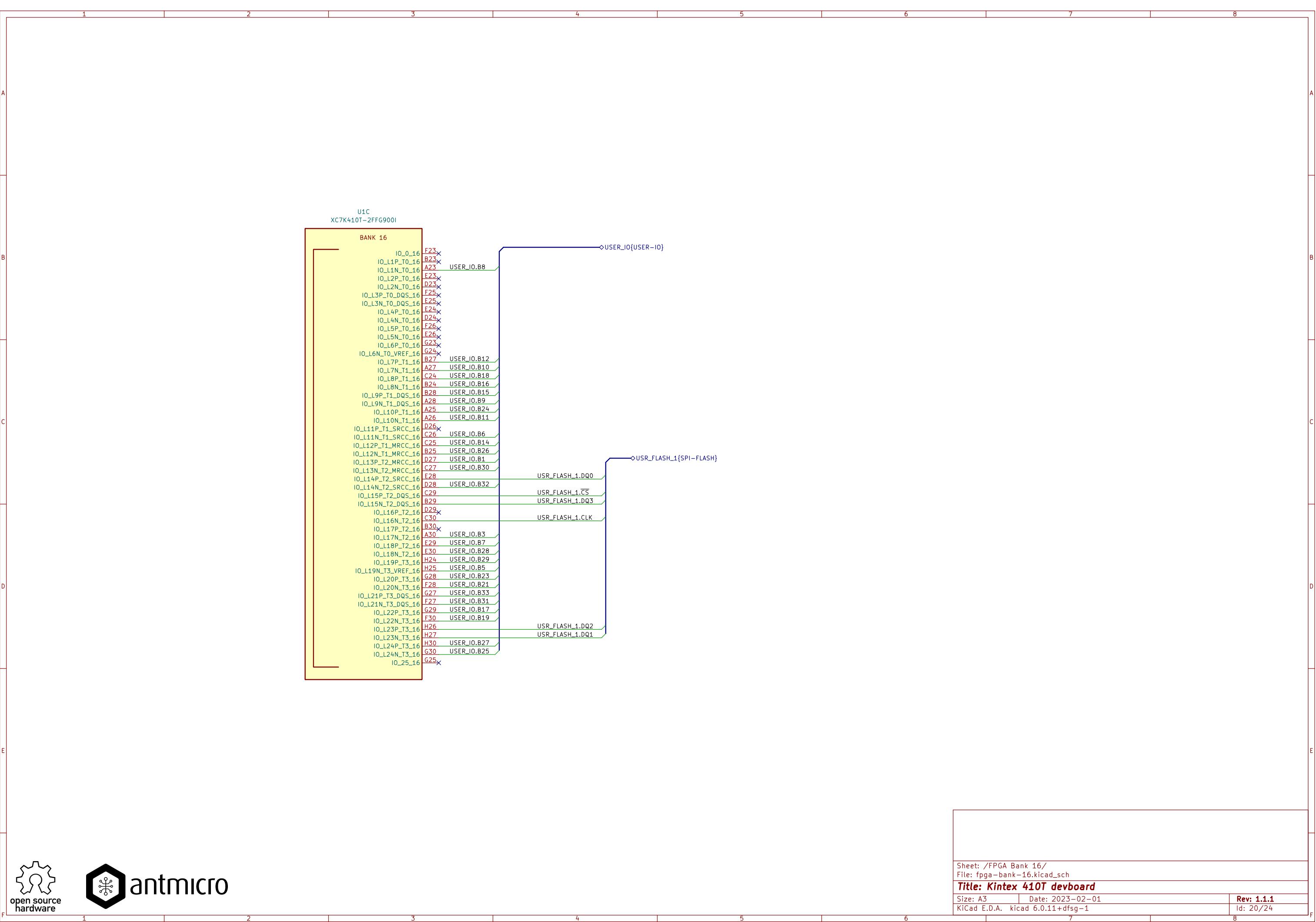
Title: Kintex 410T devboard

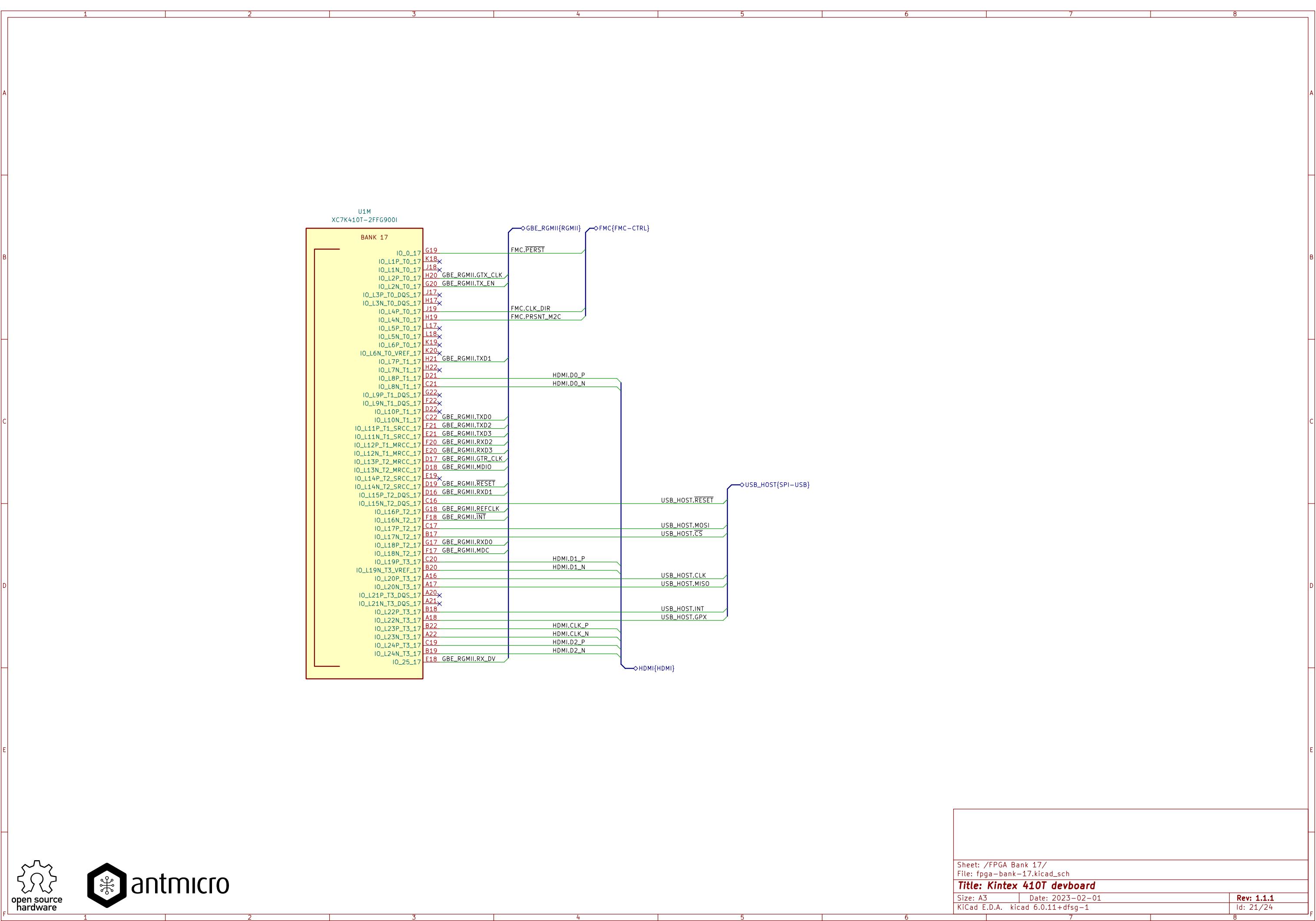
Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

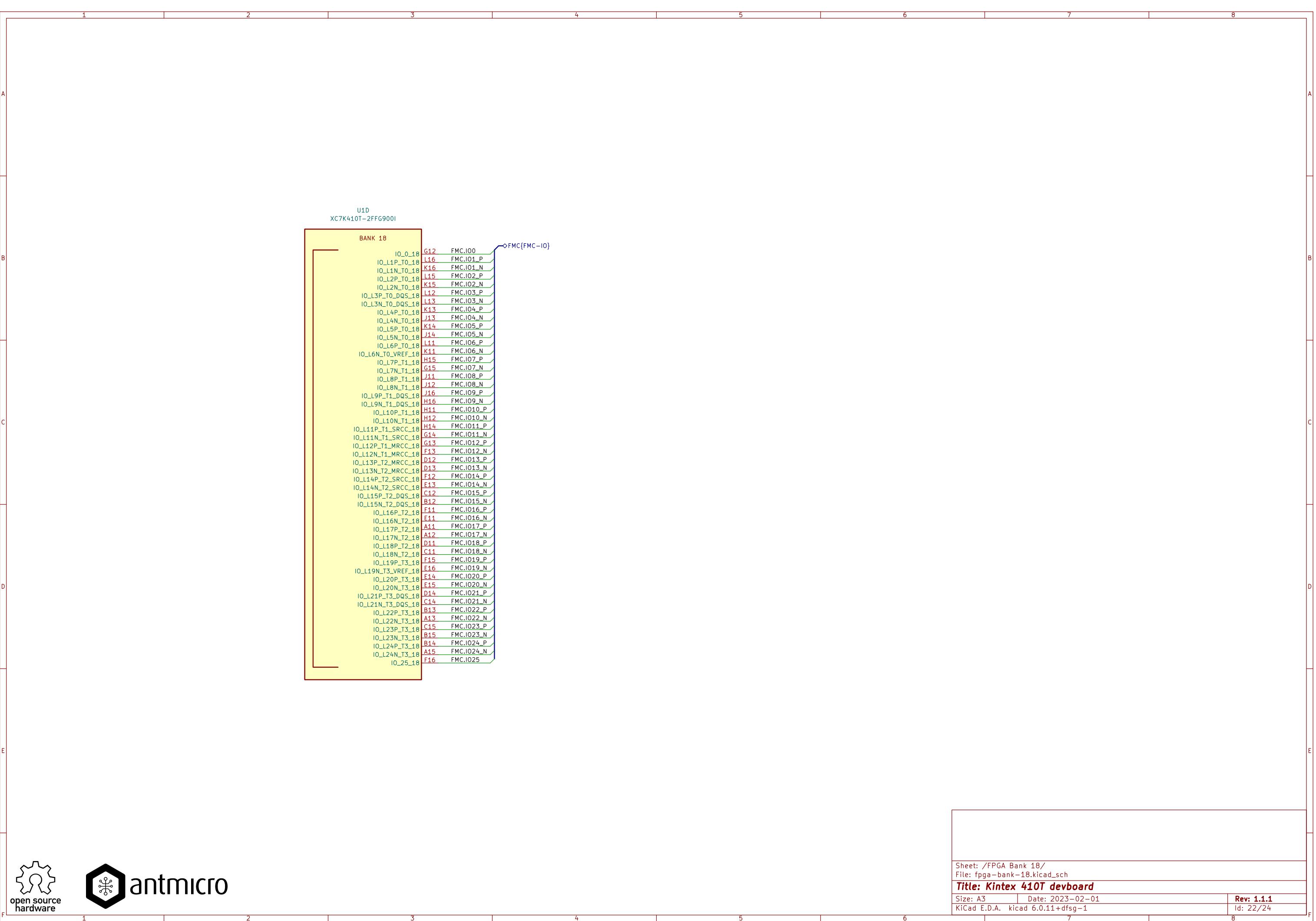
Rev: 1.1.1 | Id: 17/24











Sheet: /FPGA Bank 18/

File: fpga-bank-18.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01

KiCad E.D.A. kicad 6.0.11+dfsg-1

Rev: 1.1.1

Id: 22/24

A

B

C

D

E

F

A

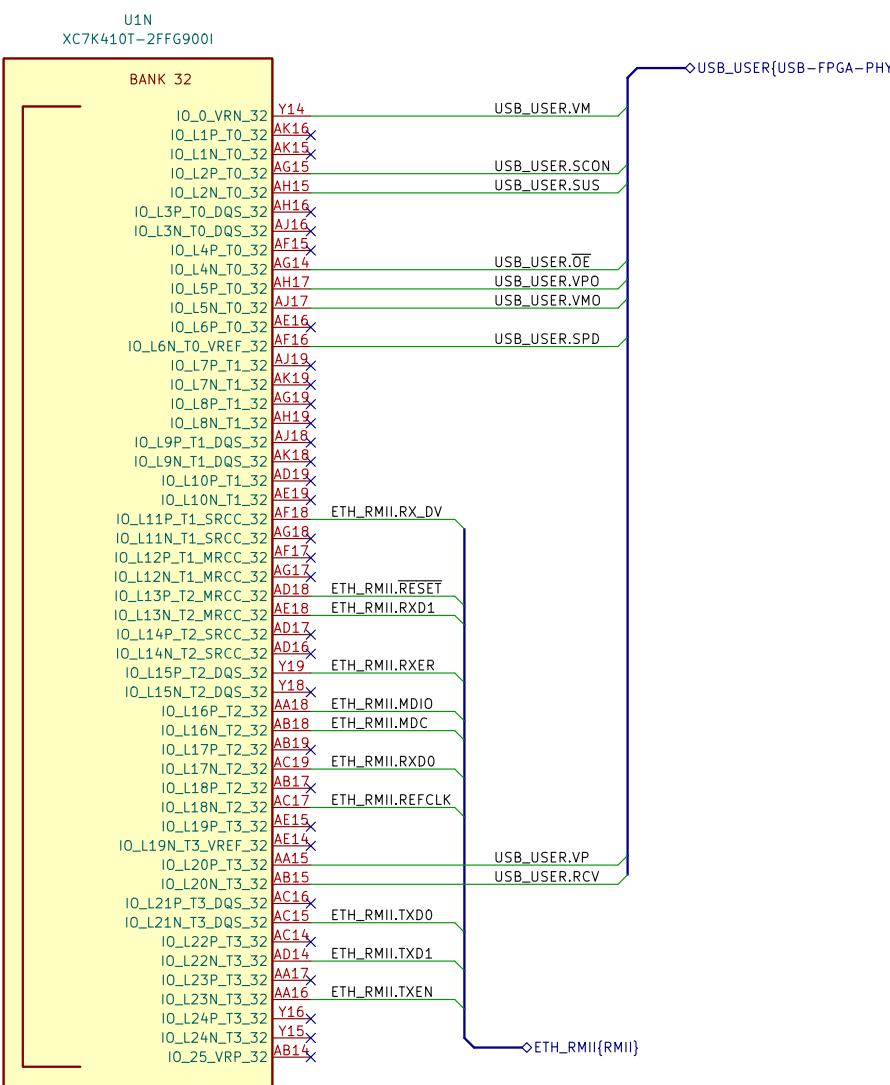
B

C

D

E

F

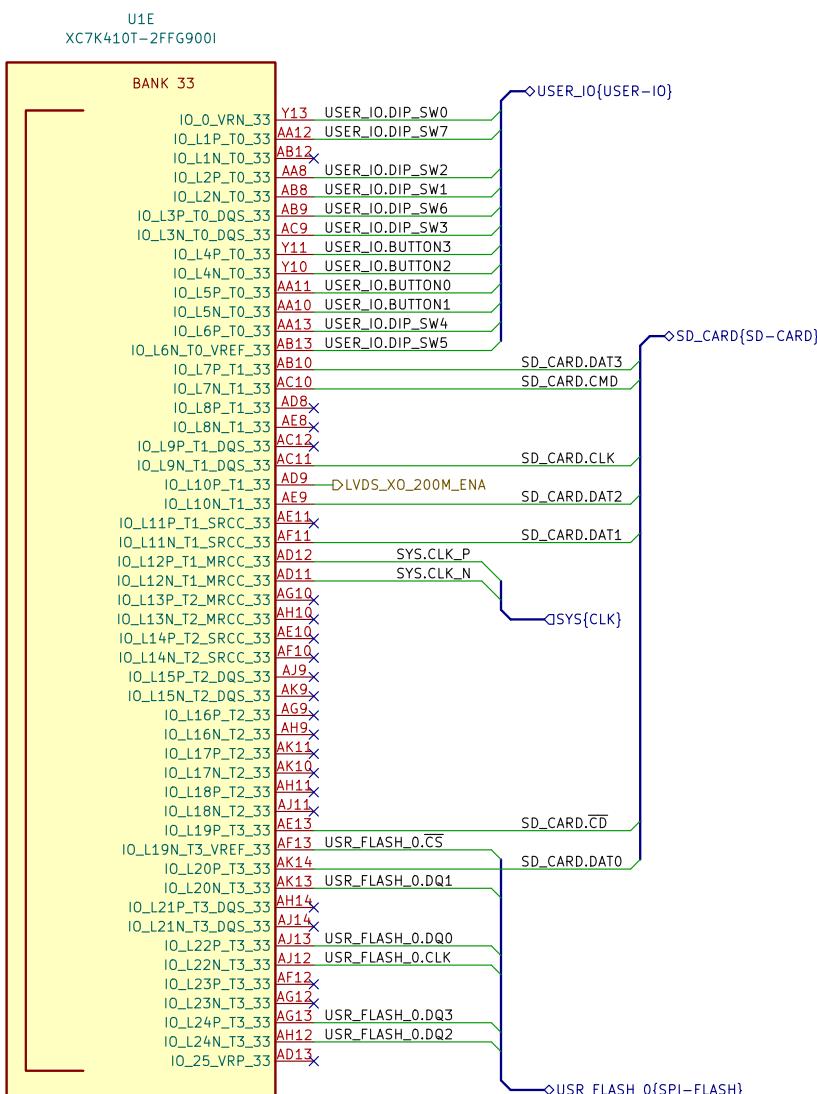


Sheet: /FPGA Bank 32/
File: fpga-bank-32.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

Rev: 1.1.1
Id: 23/24



Supervisor MCU

A

A

B

B

C

C

D

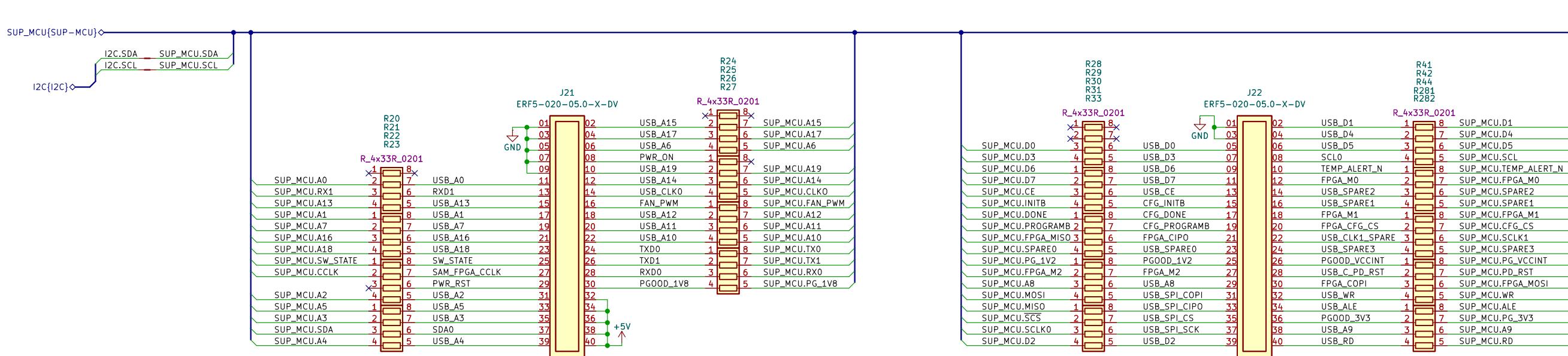
D

E

E

F

F



antmicro
open source hardware

Sheet: /Supervisor MCU/
File: supervisor-mcu.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

Rev: 1.1.1 | Id: 24/24

A

A

B

B

C

C

D

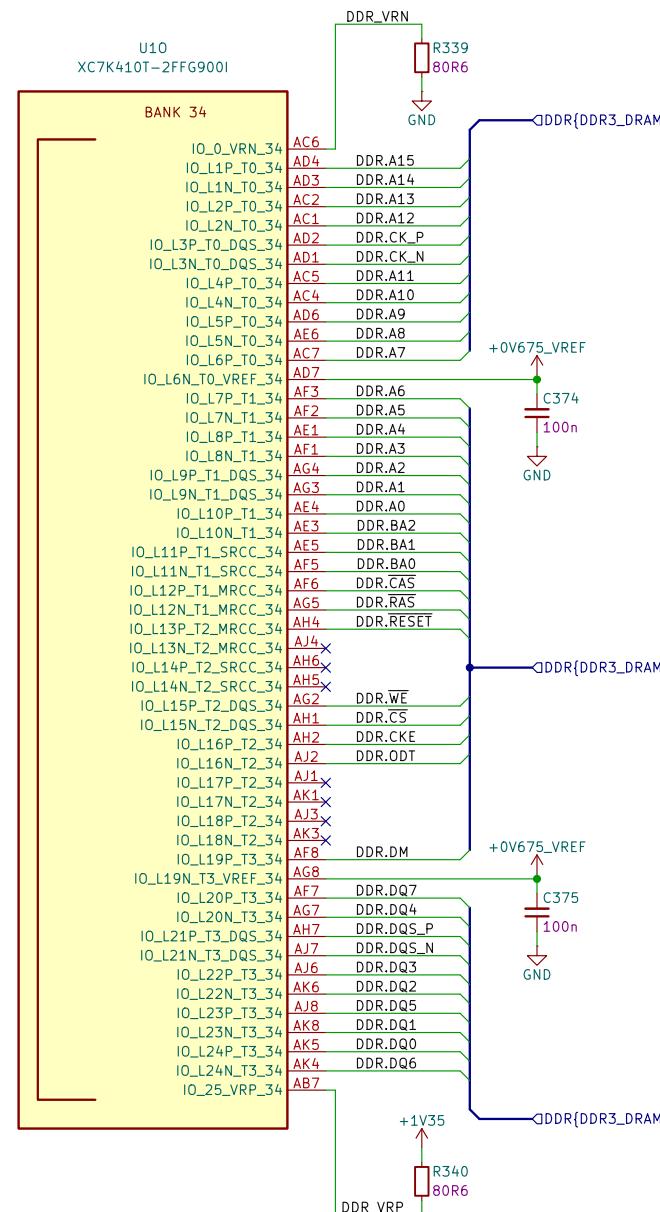
D

E

E

F

F

Sheet: /FPGA Bank 34/
File: fpga-bank-34.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

Rev: 1.1.1 | Id: 25/24

A

B

C

D

E

F

A

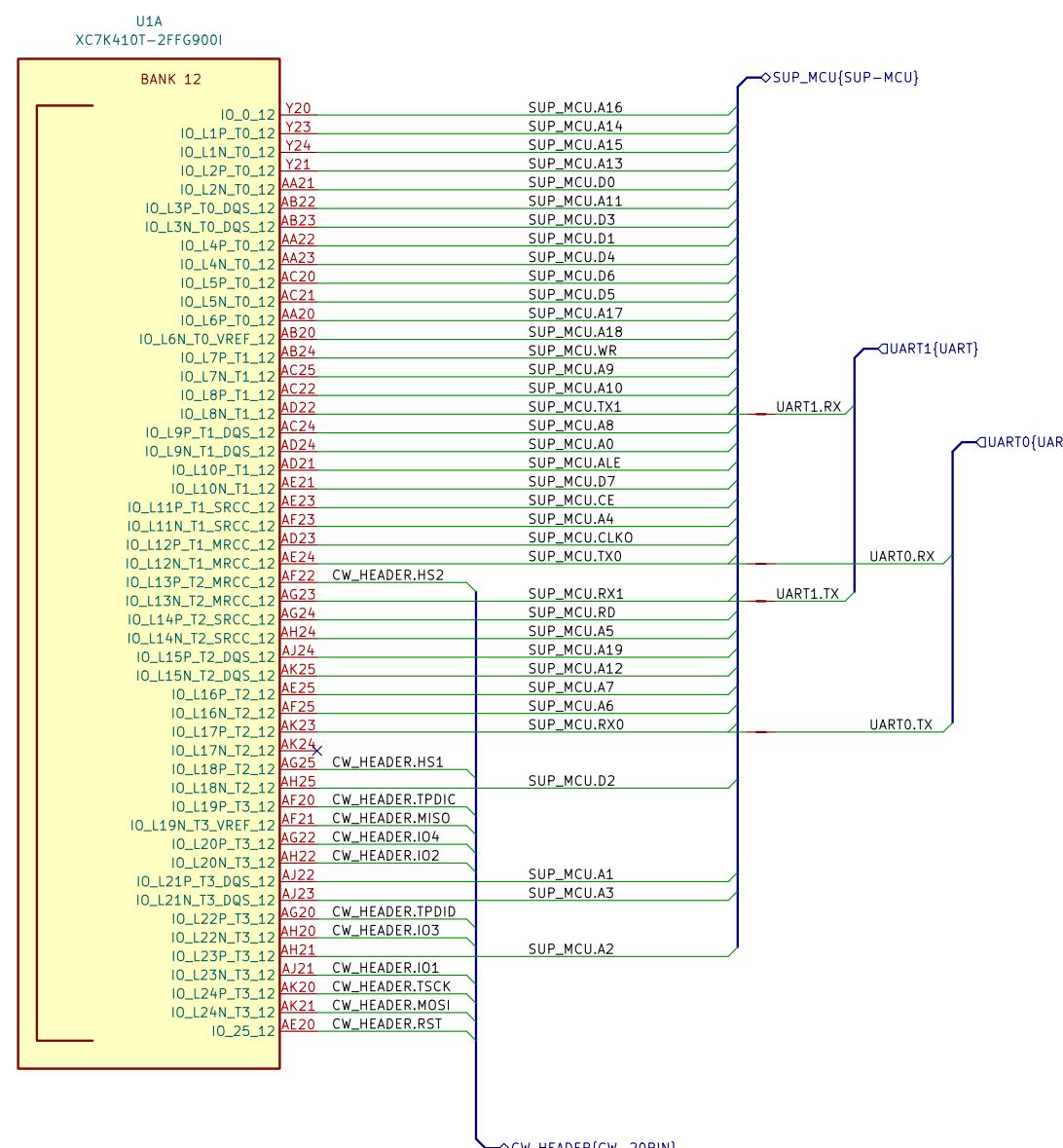
B

C

D

E

F

Sheet: /FPGA Bank 12/
File: fpga-bank-12.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad 6.0.11+dfsg-1

Rev: 1.1.1 | Id: 26/24