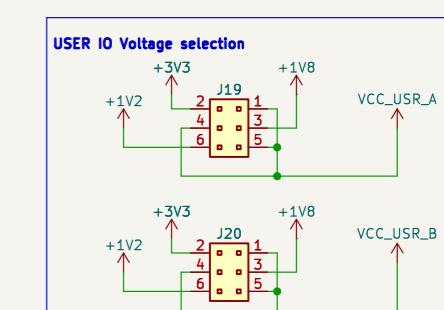
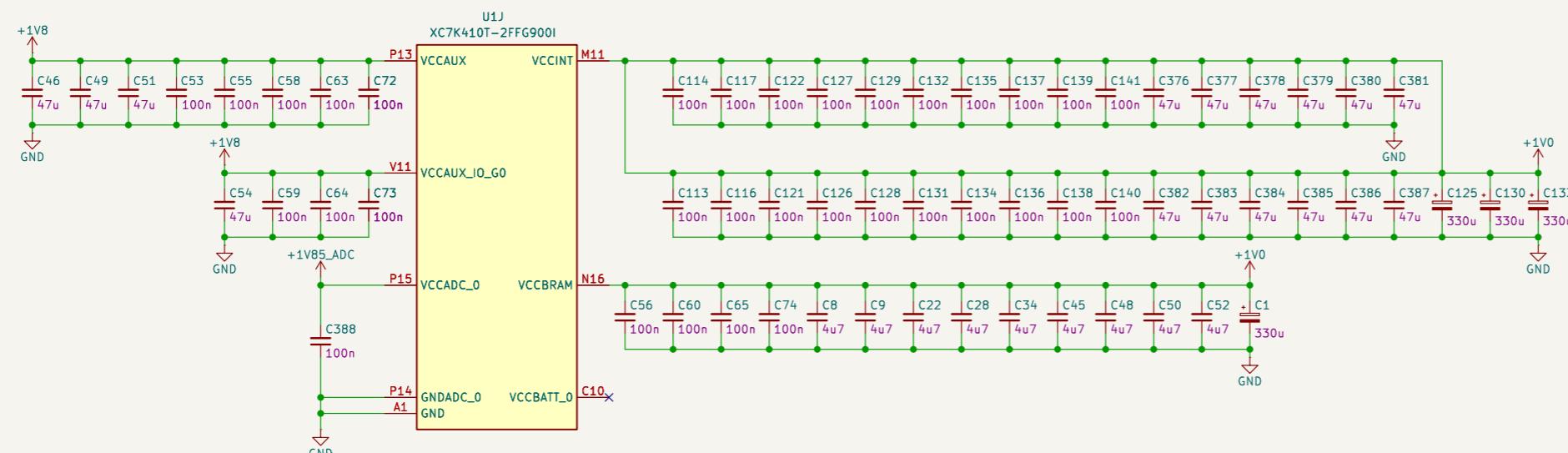
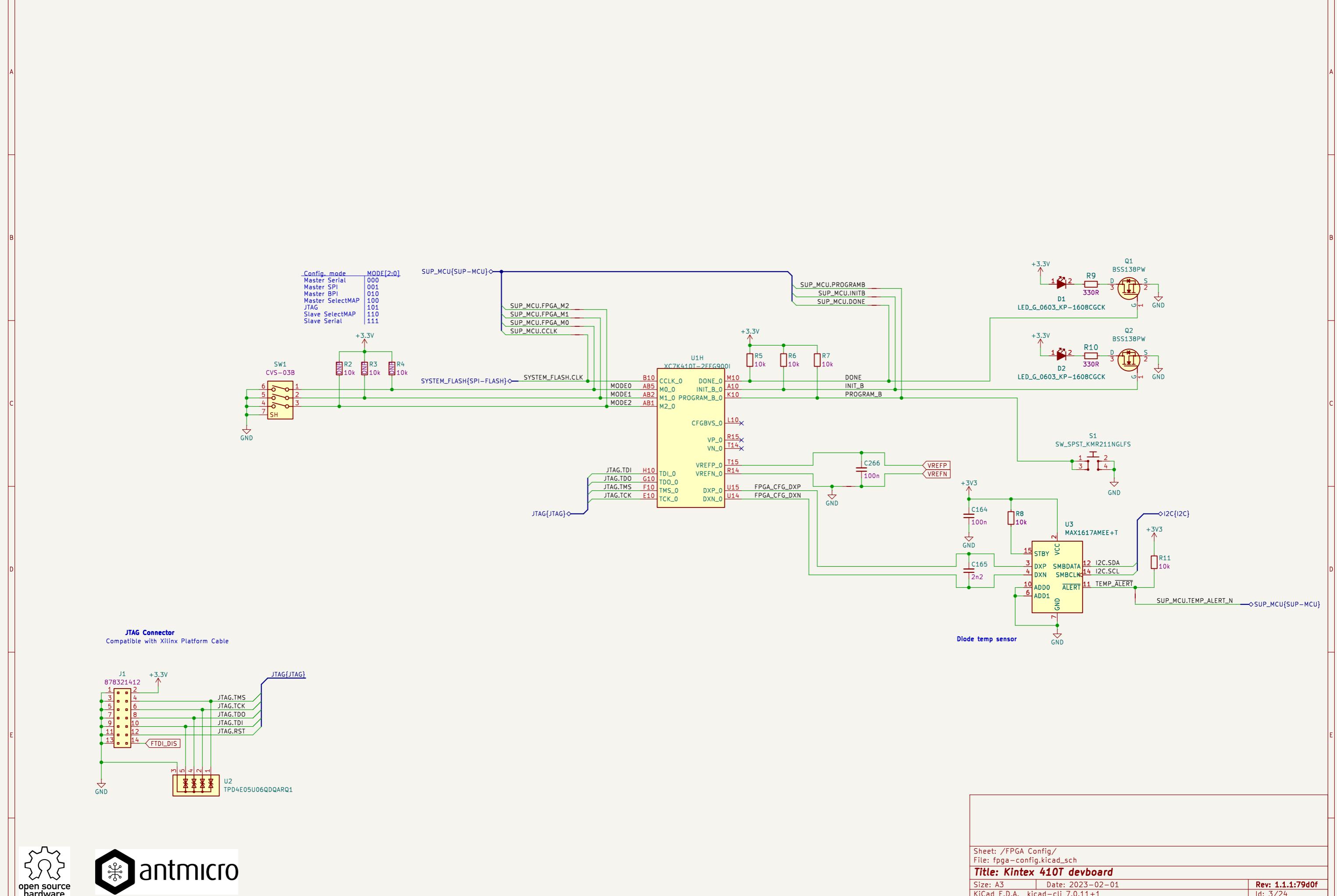


Trace length from the resistor pins to the FPGA pins MGTRREF and MGTAVTRCAL must be equal in length.





A

B

C

D

E

F

A

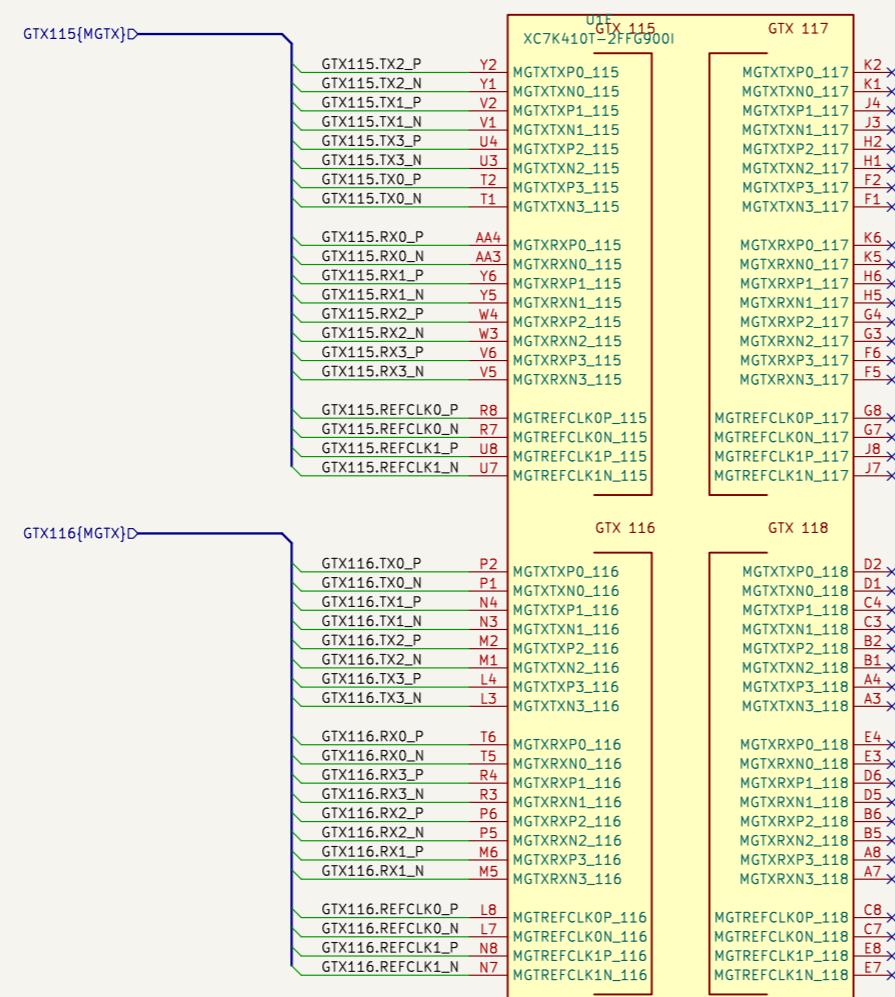
B

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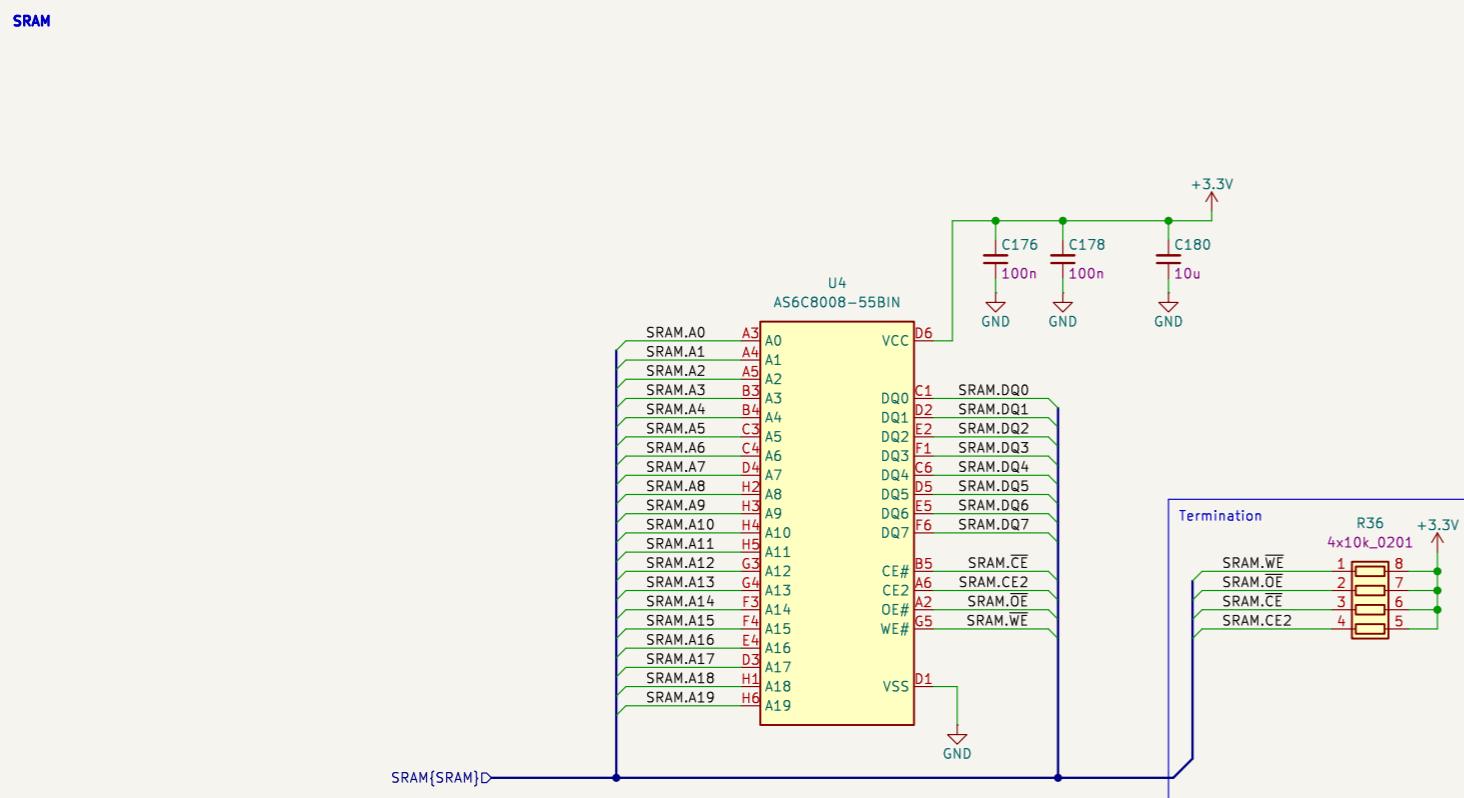
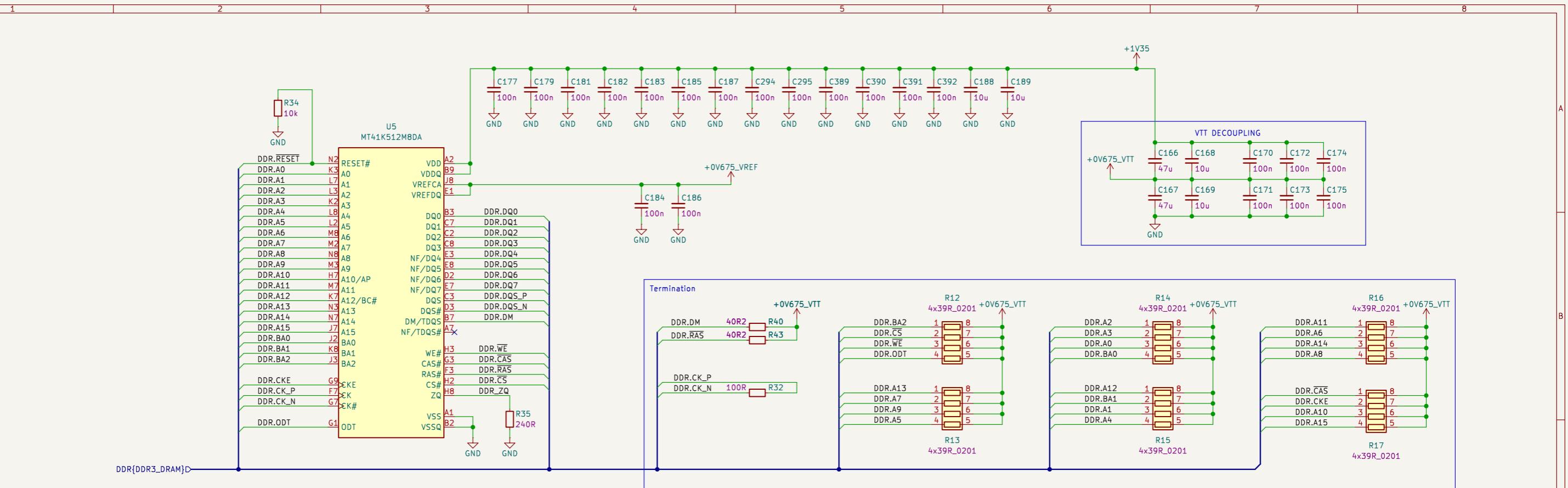


Sheet: /FPGA MGT Interface/
File: fpga-mgt.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f
Id: 4/24

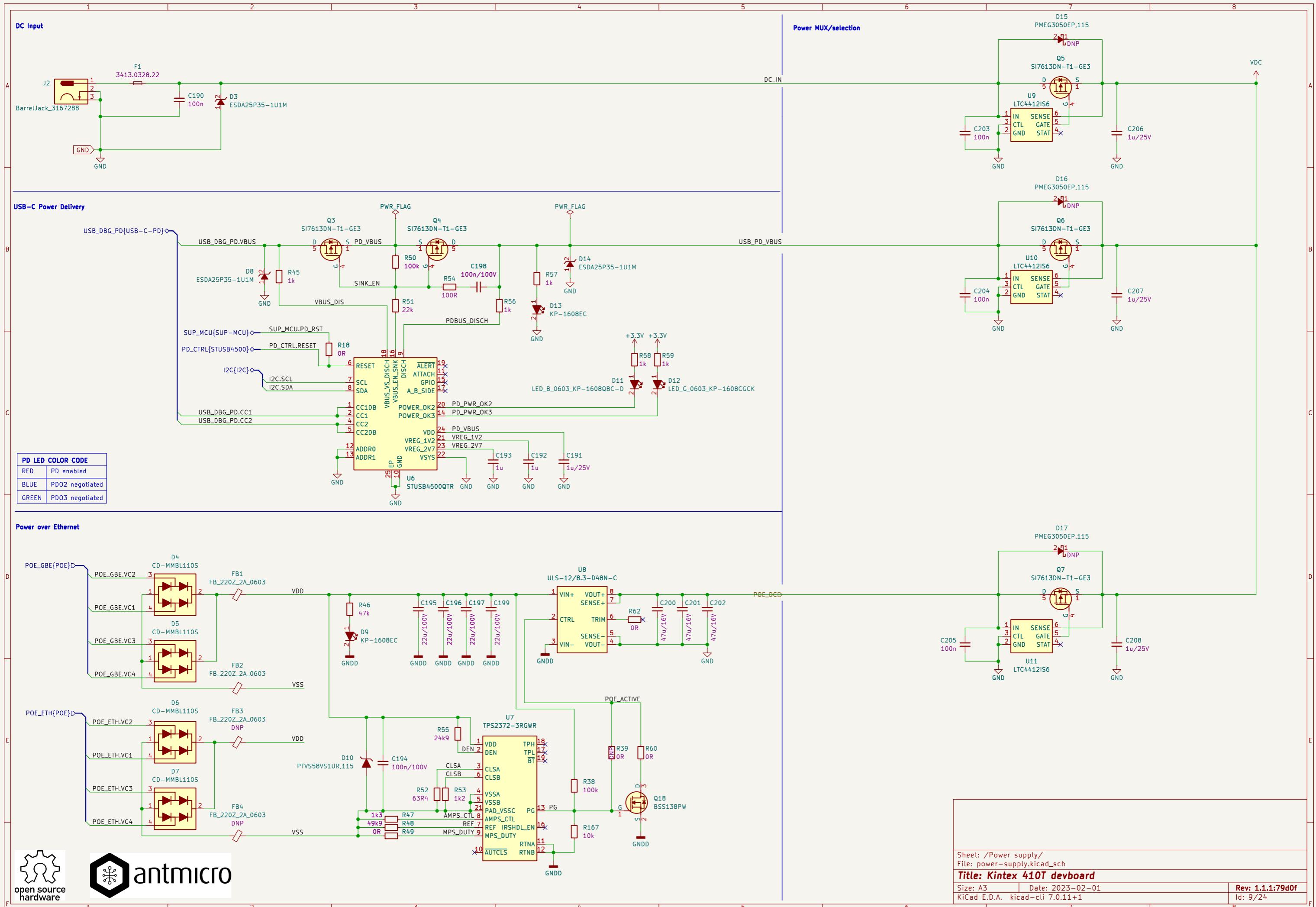


Sheet: /DRAM + SRAM/
File: ram.kicad_sch

Title: Kintex 410T devboard

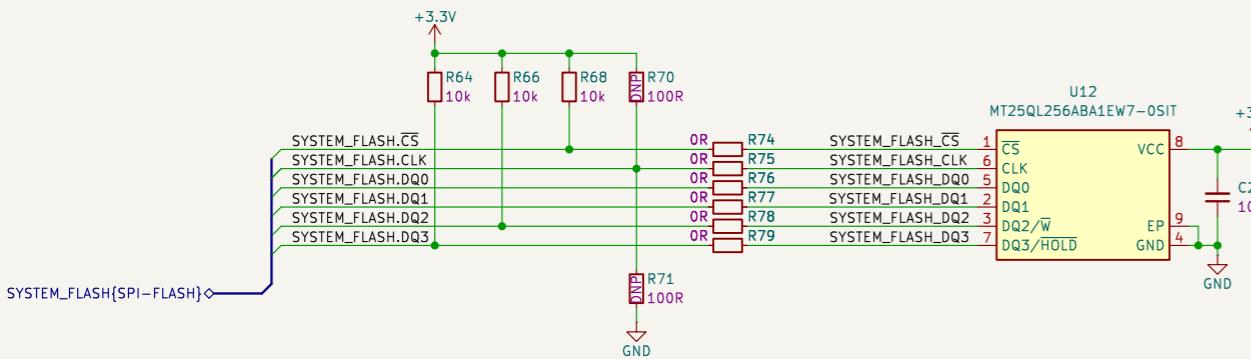
Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cl 7.0.11+1

Rev: 1.1.1:79d0f
Id: 8/24

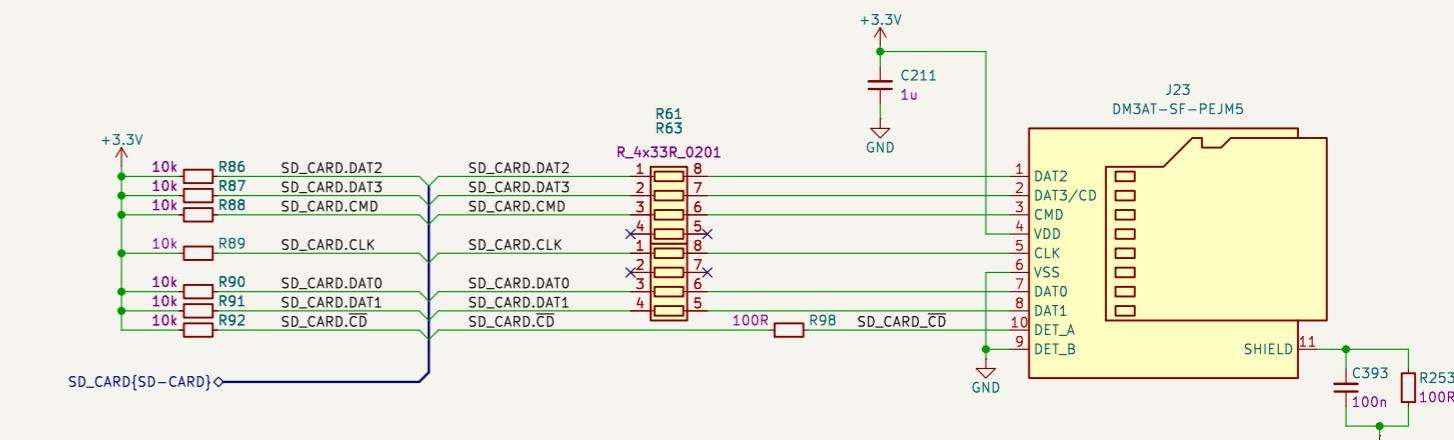


1 2 3 4 5 6 7 8

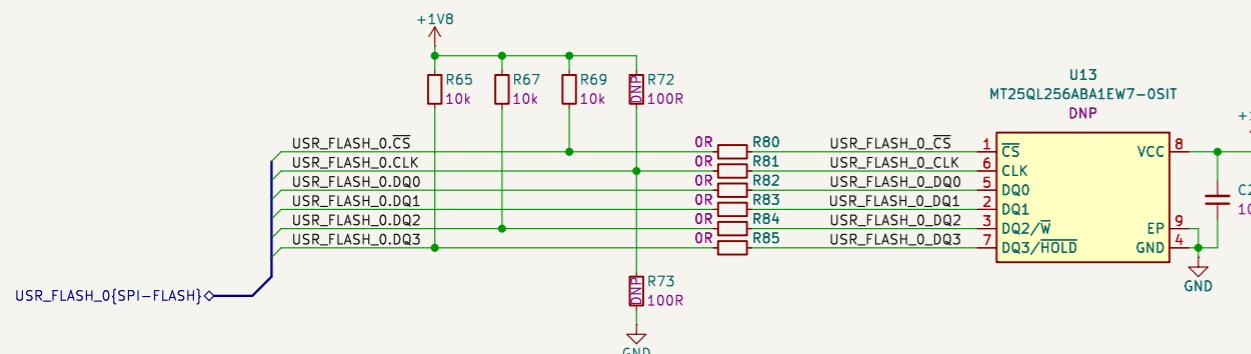
(Q)SPI flash

Master SPI Quad (x4) configuration schemeFollows Figure 2-14 7 Series FPGAs Configuration User Guide
UG470 (v1.15)

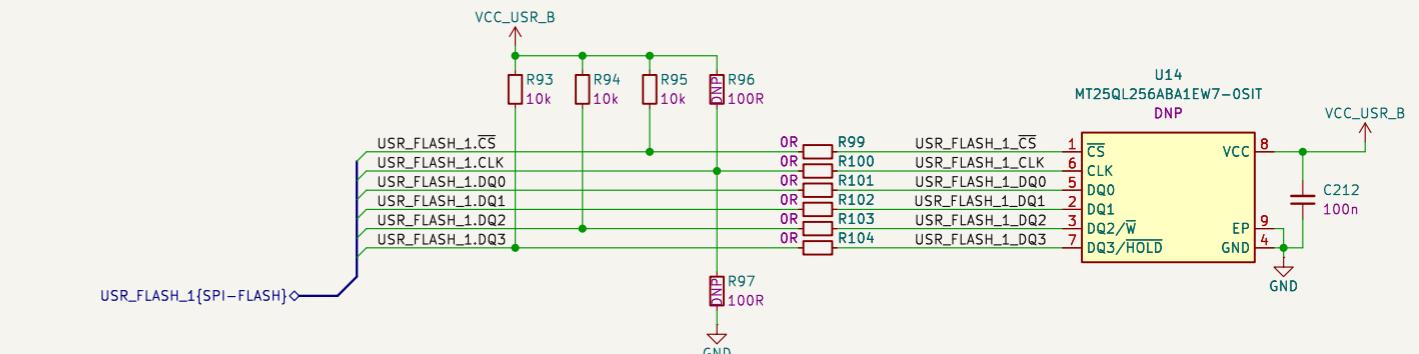
USER SD CARD



USER SPI flash 0

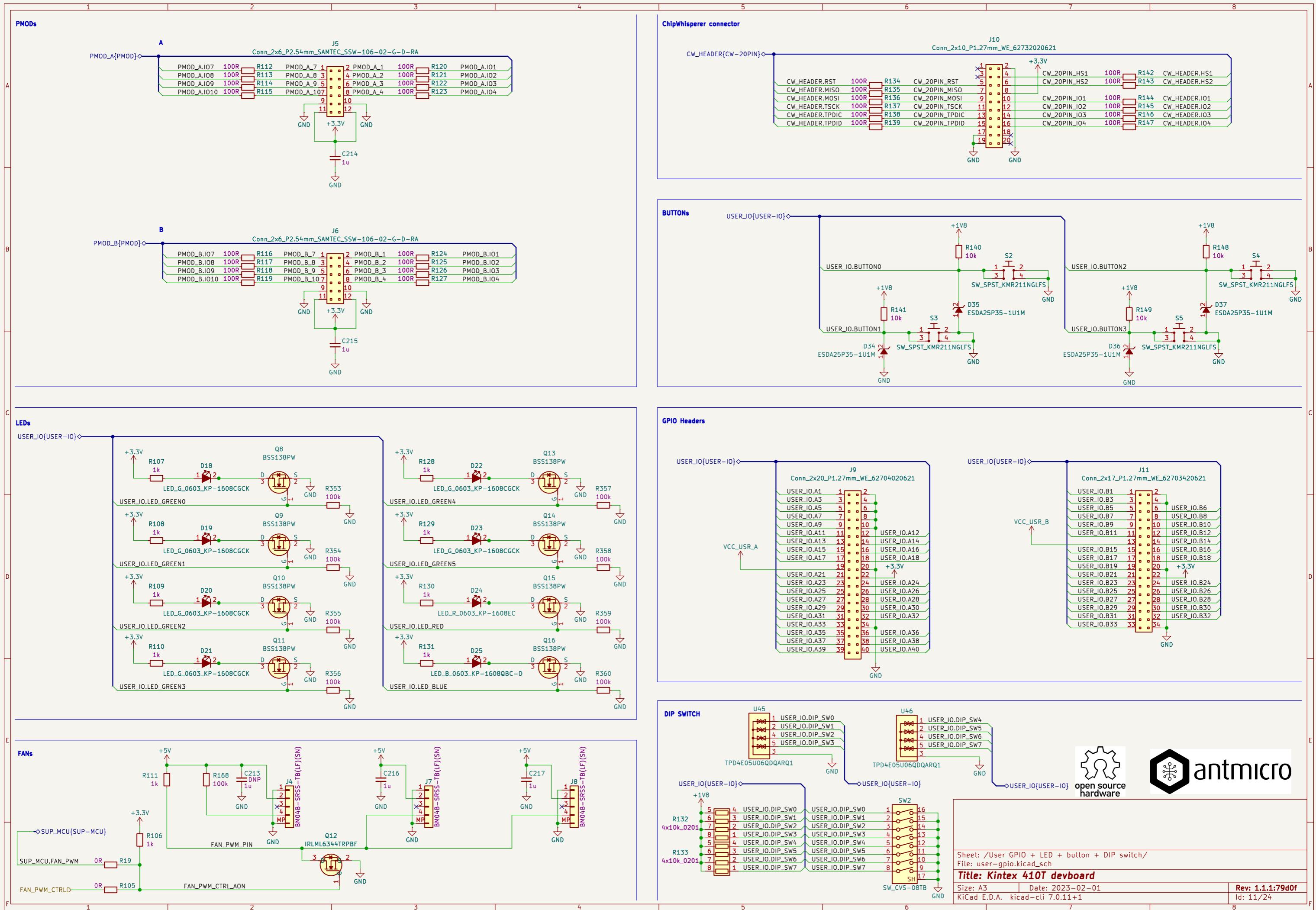


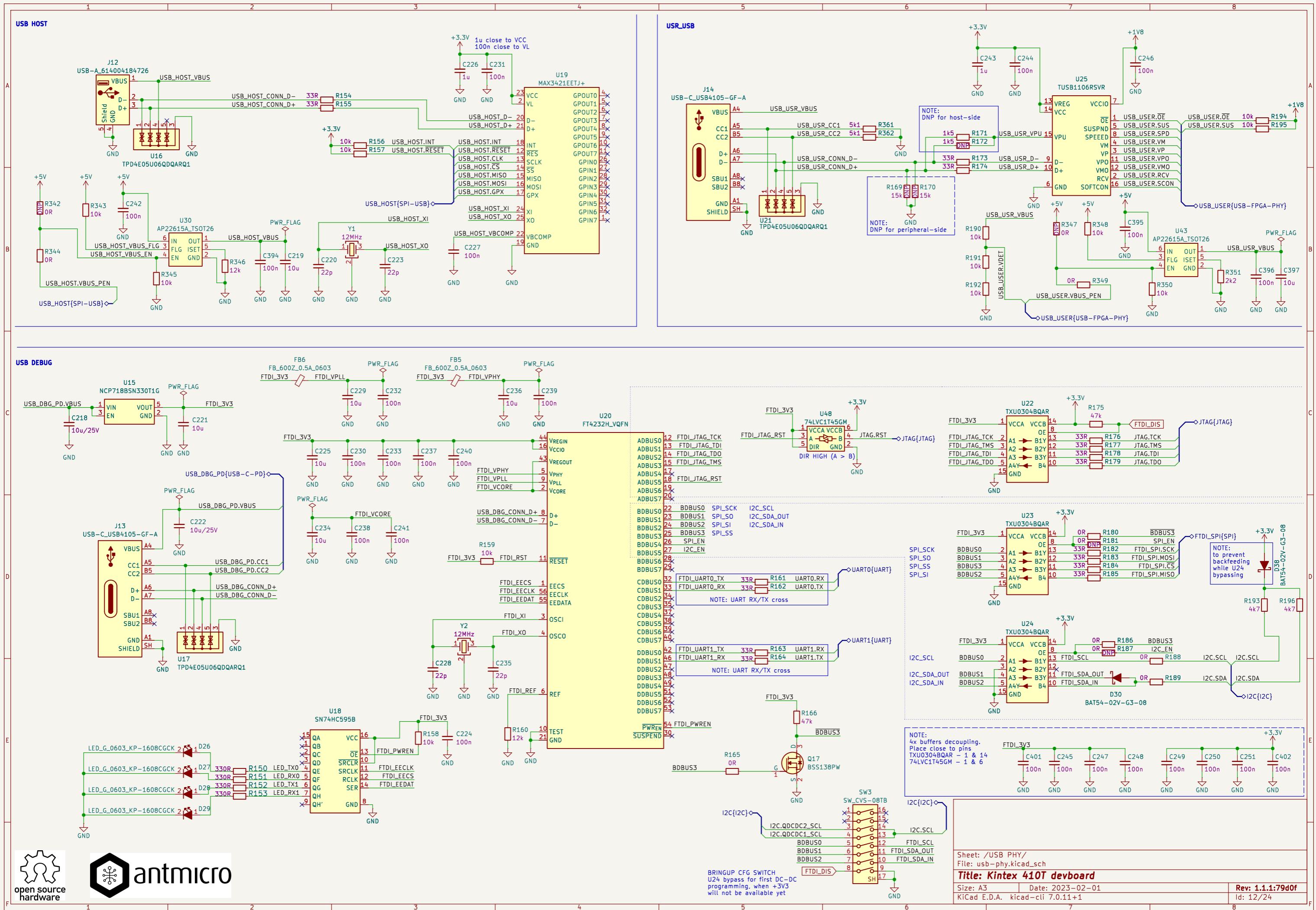
USER SPI flash 1

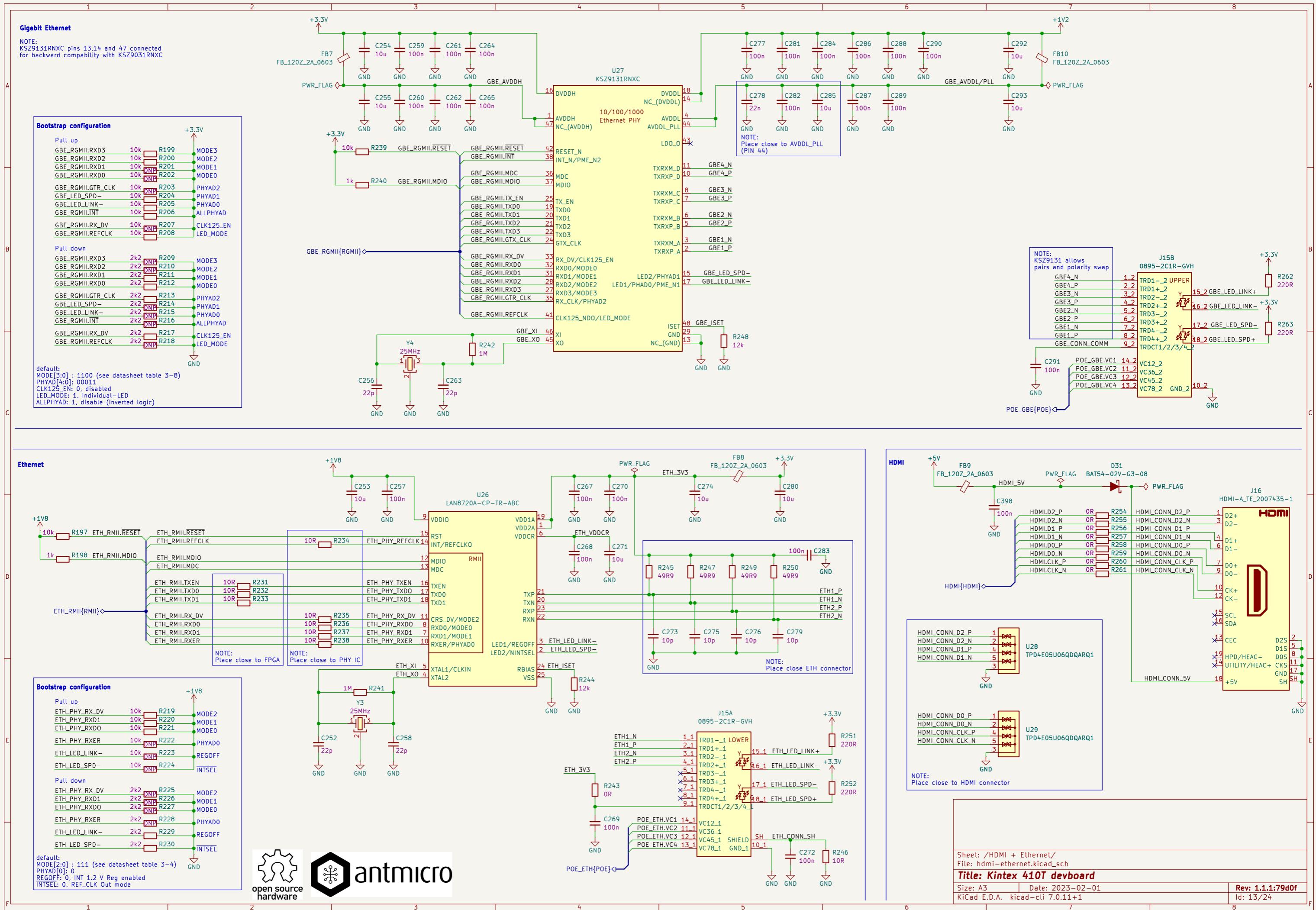
Sheet: /SPI Flash + SD Card/
File: spi-flash.kicad_sch**Title: Kintex 410T devboard**Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cl 7.0.11+1

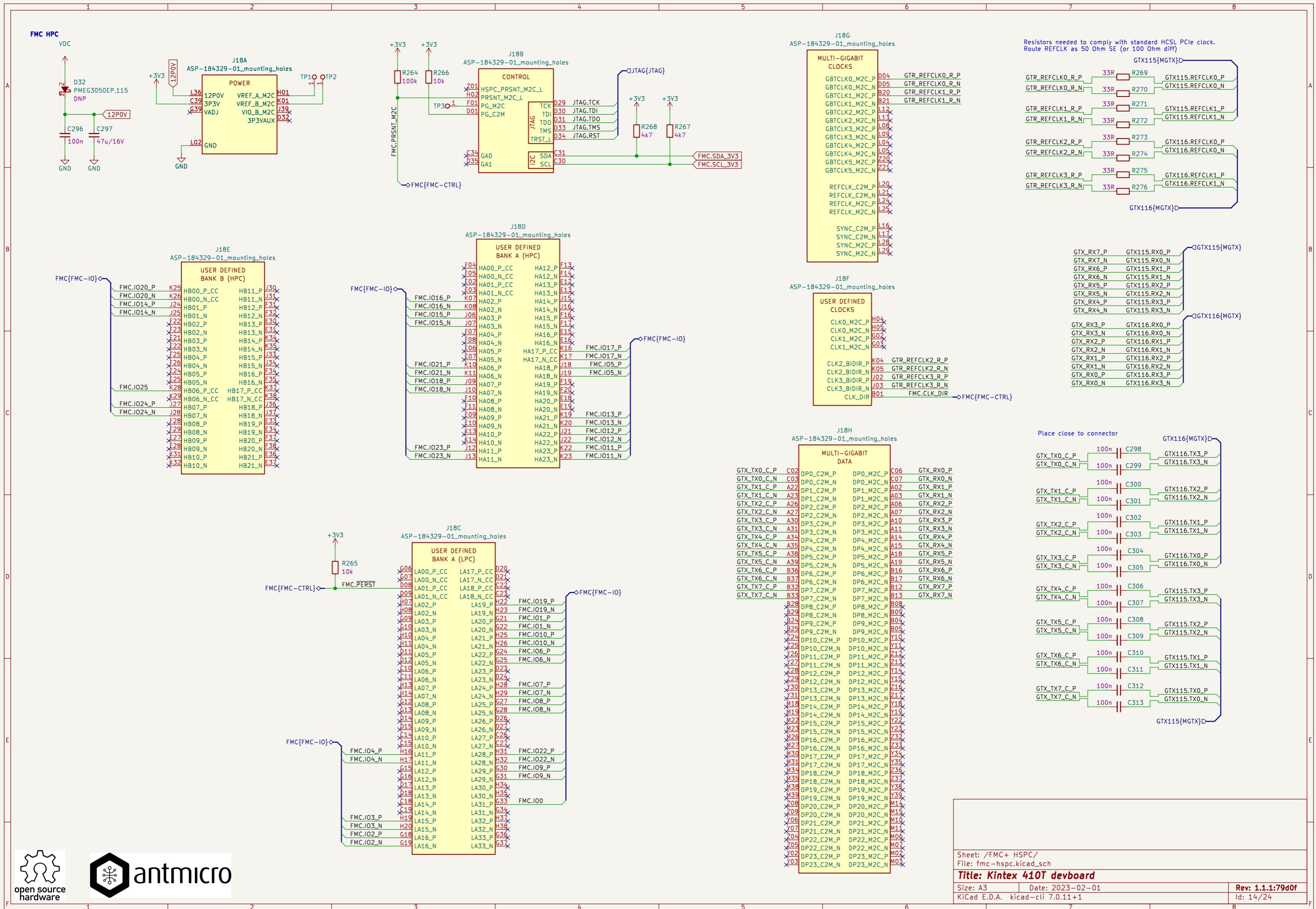
Rev: 1.1.1:79d0f | Id: 10/24

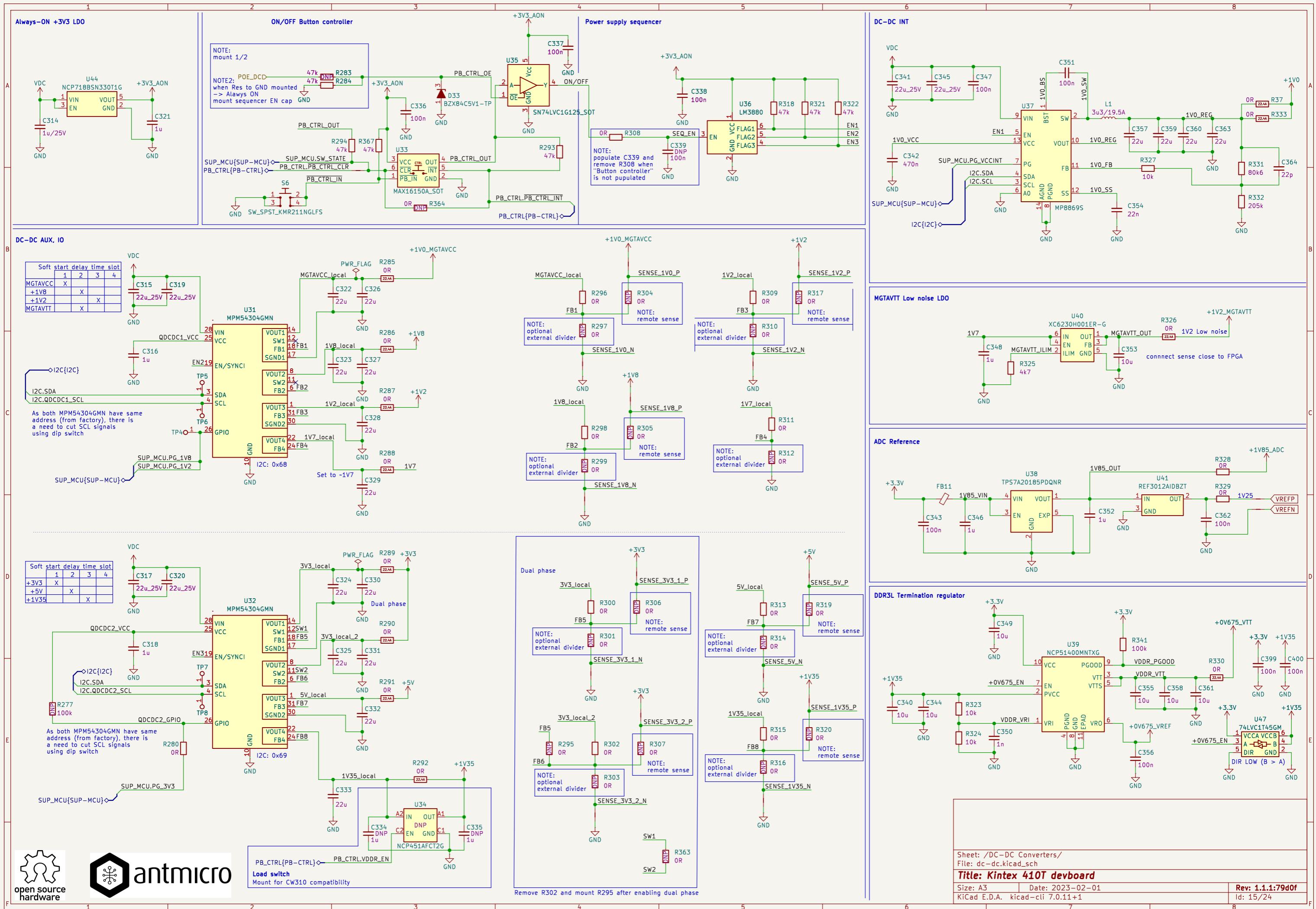
1 2 3 4 5 6 7 8











Main system clock

A

A

B

B

C

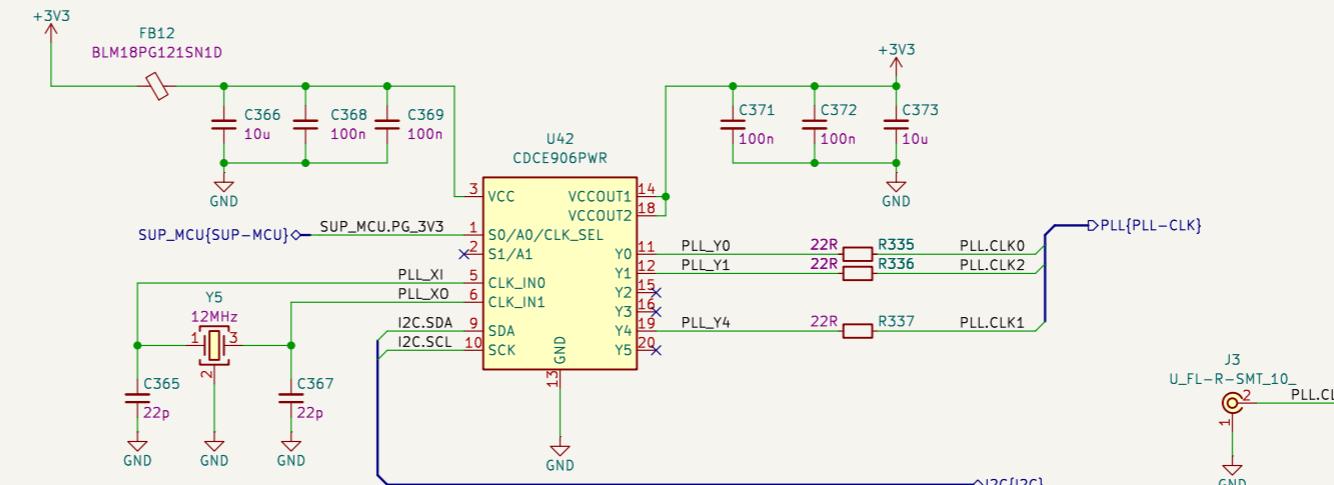
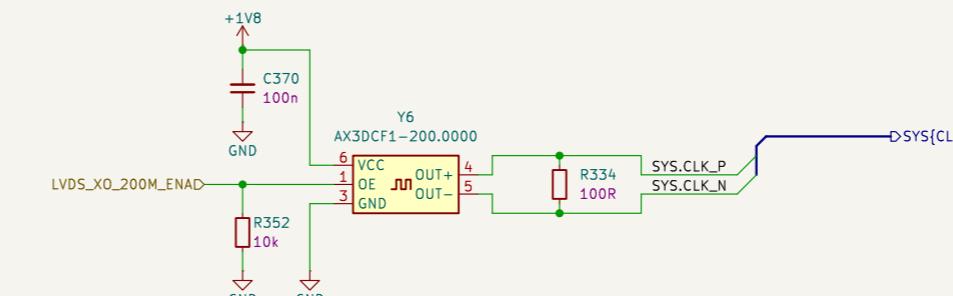
C

D

D

E

E

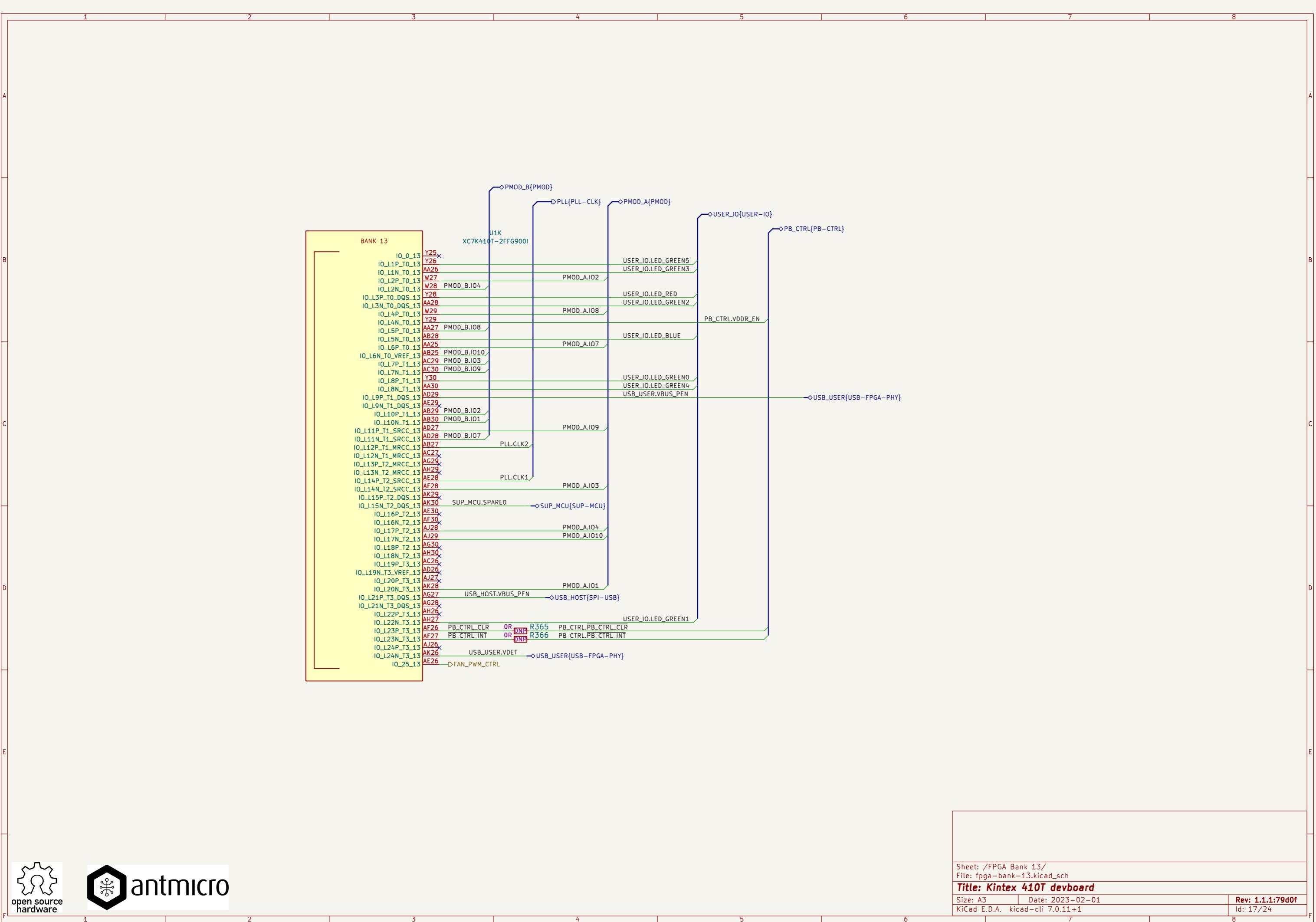


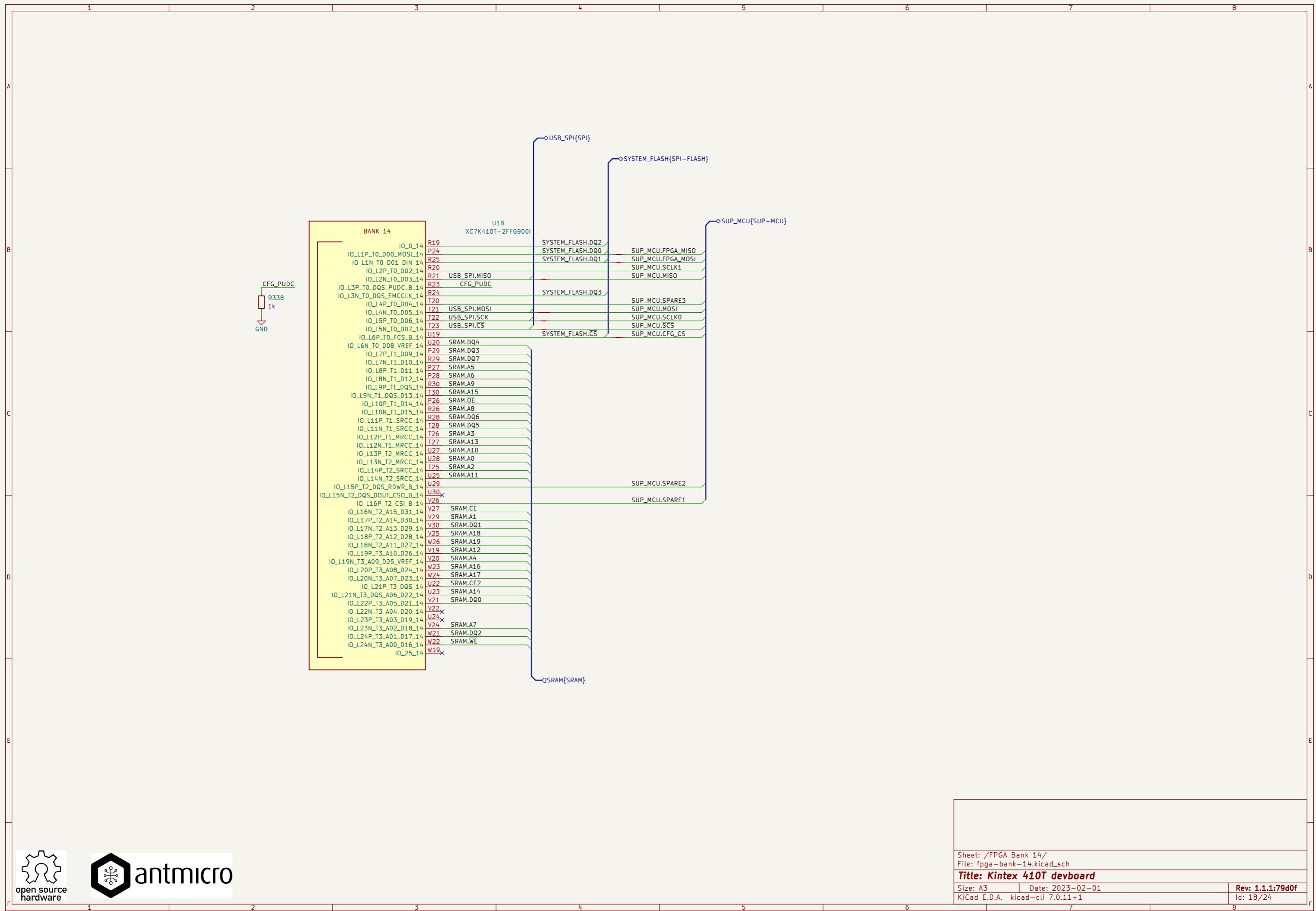
Sheet: /Clocks/
File: clocks.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cliv 7.0.11+1

Rev: 1.1.1:79d0f
Id: 16/24





Sheet: /FPGA Bank 14/
File: fpga-bank-14.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f
Id: 18/24

A

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A

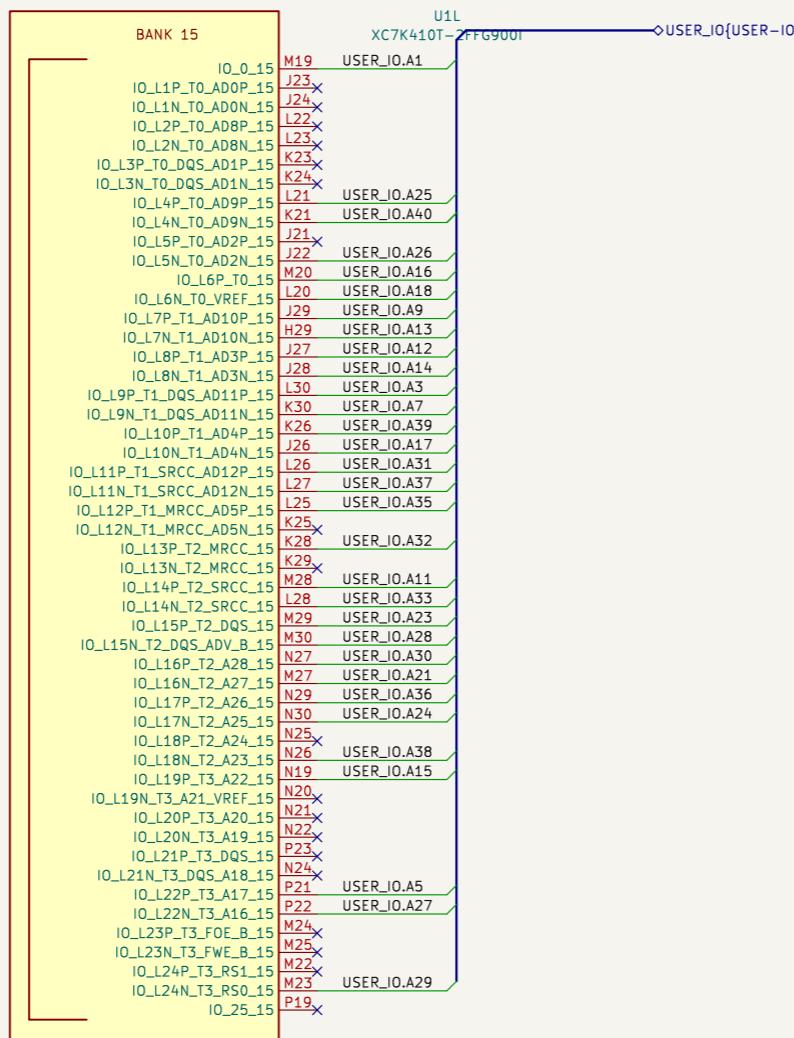
B

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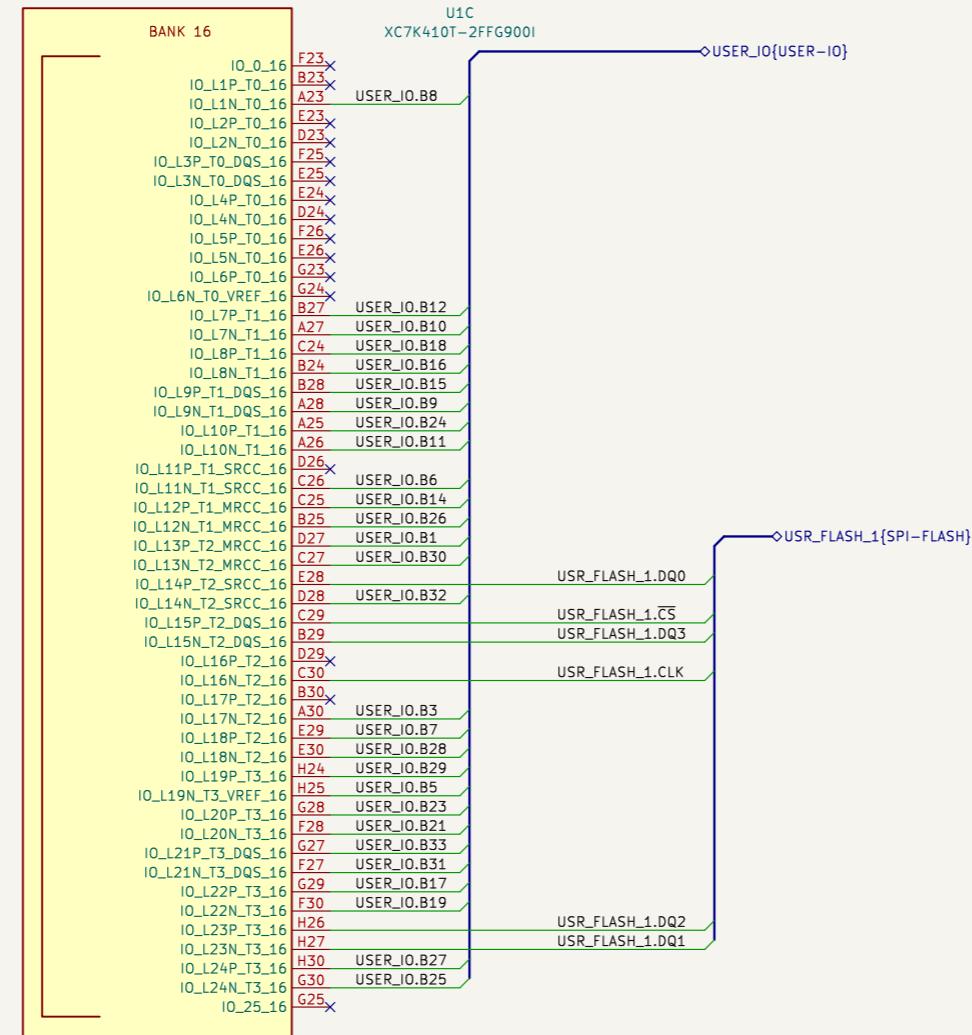


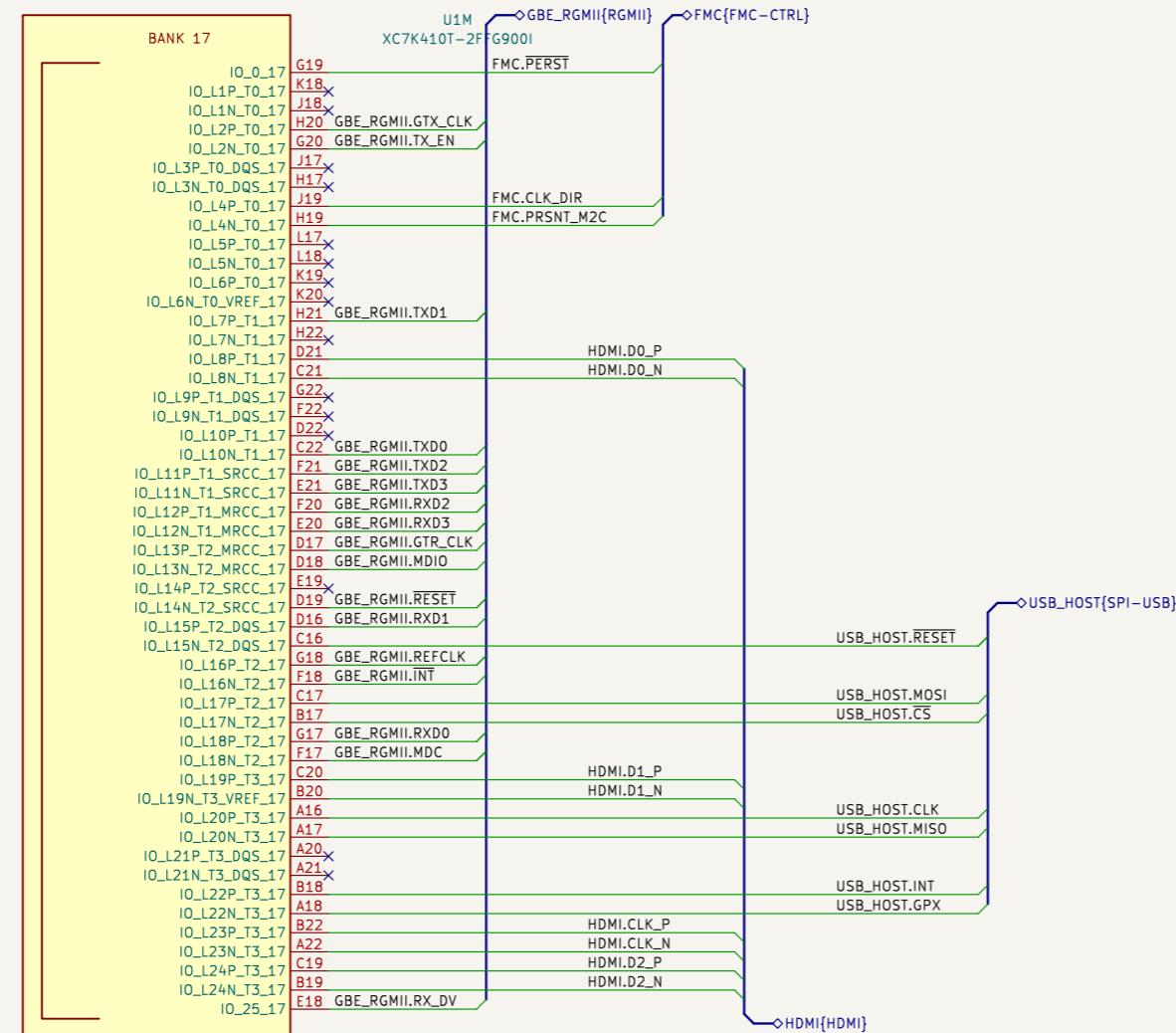
Sheet: /FPGA Bank 15/
File: fpga-bank-15.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f | Id: 19/24





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A

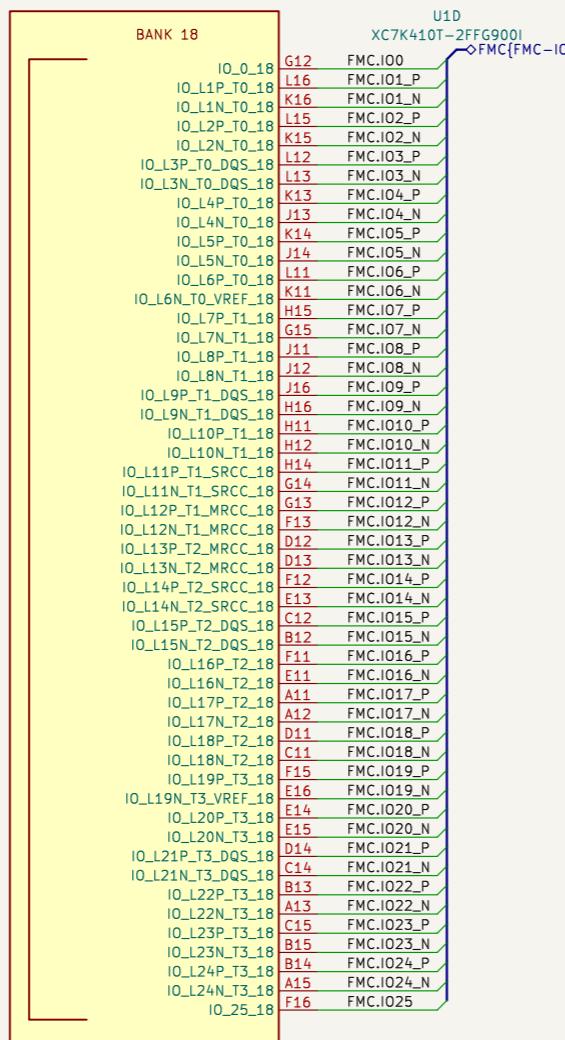
B

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A

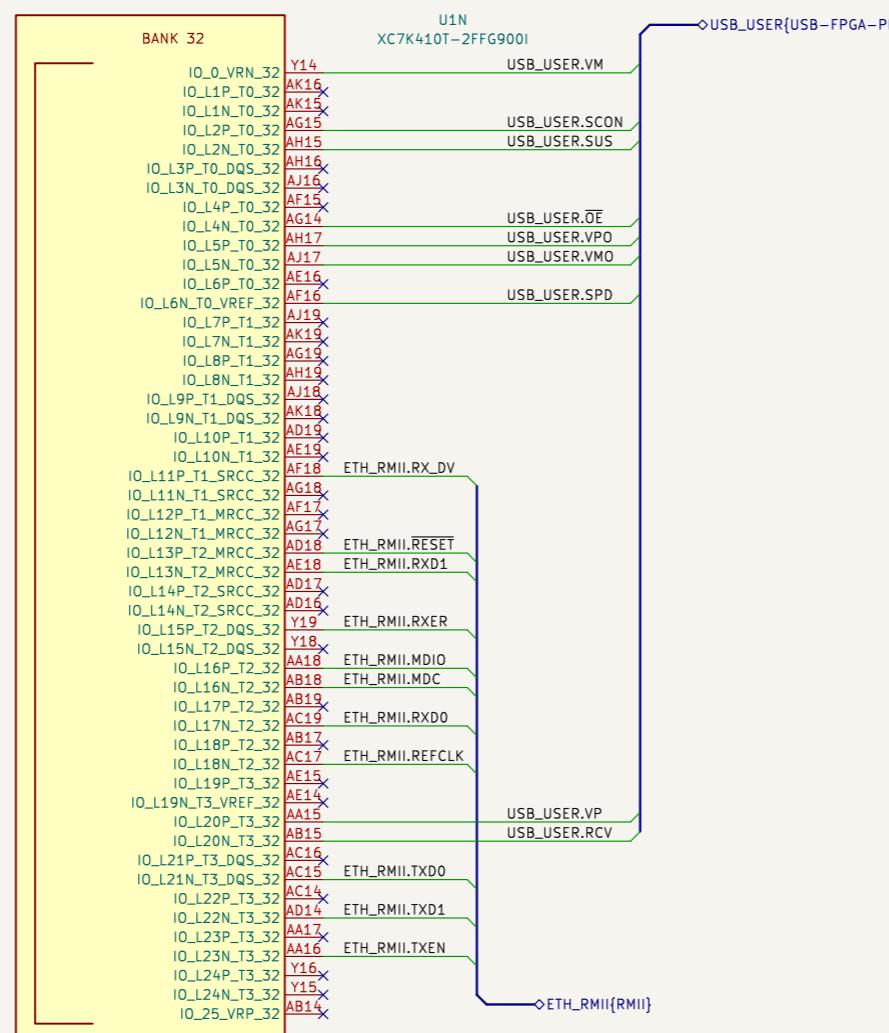
B

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Sheet: /FPGA Bank 32/
File: fpga-bank-32.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f | Id: 23/24

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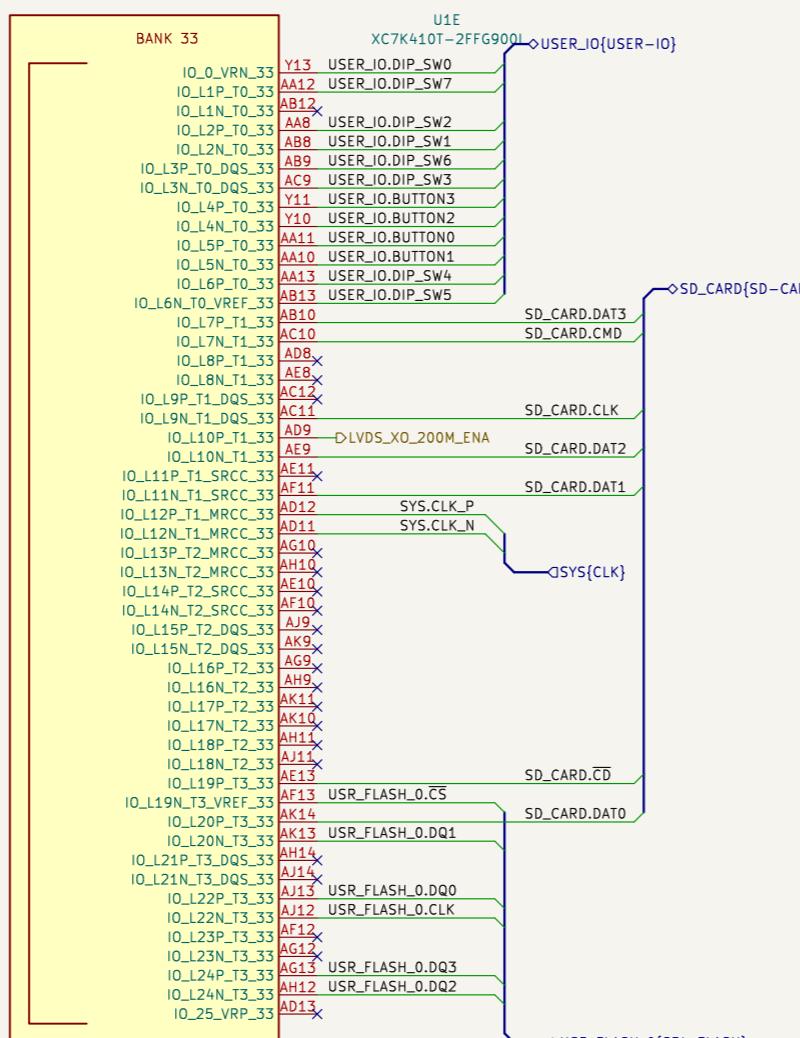
B

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Sheet: /FPGA Bank 33/
File: fpga-bank-33.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f | Id: 24/24

Supervisor MCU

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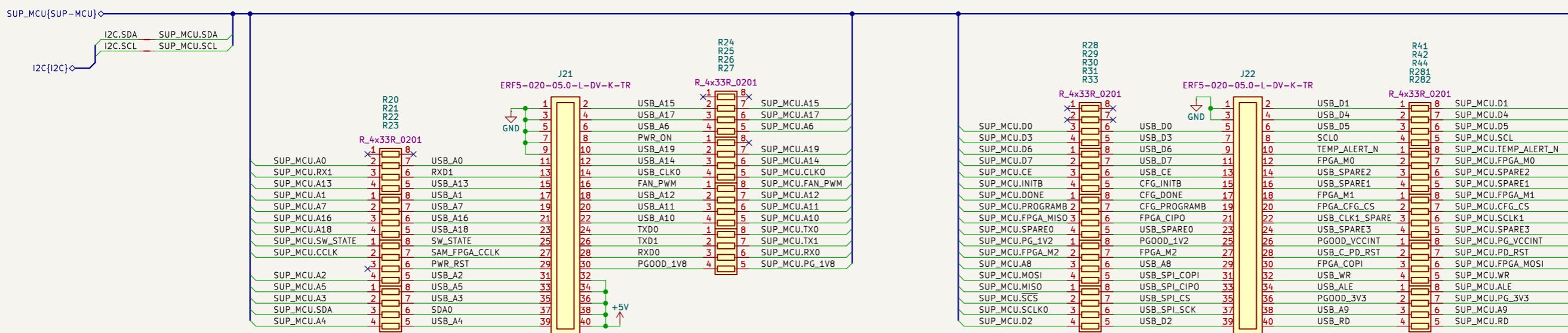
B

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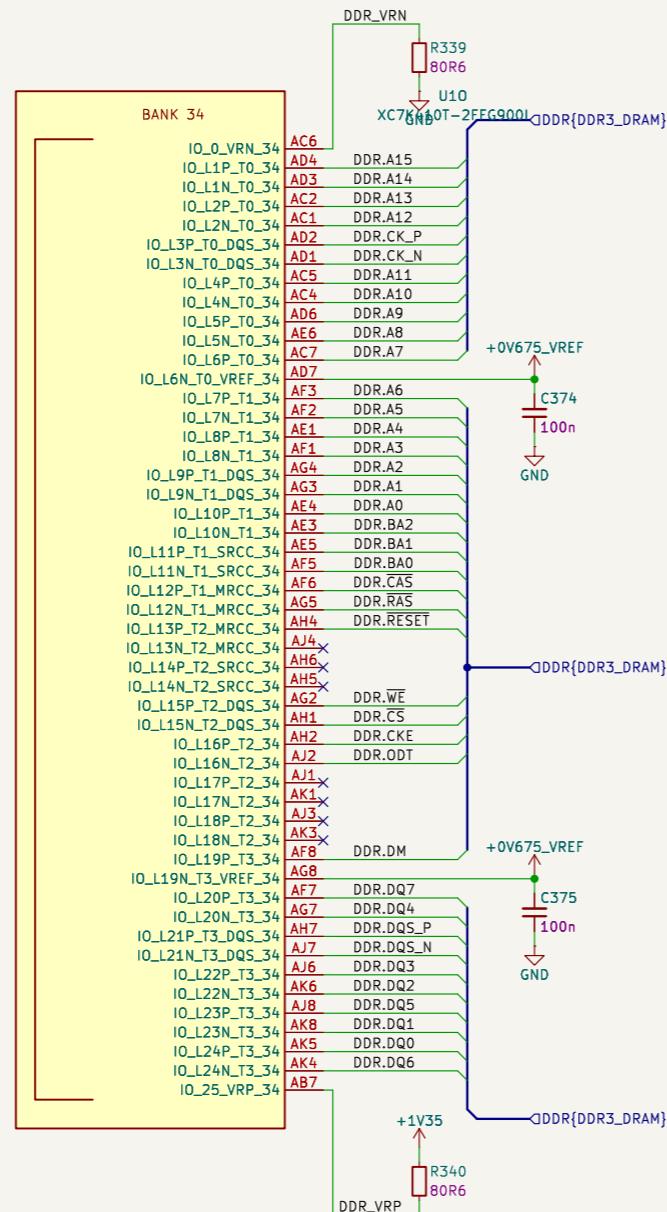


Sheet: /Supervisor MCU/
File: supervisor-mcu.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11+1

Rev: 1.1.1:79d0f
Id: 24/24



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