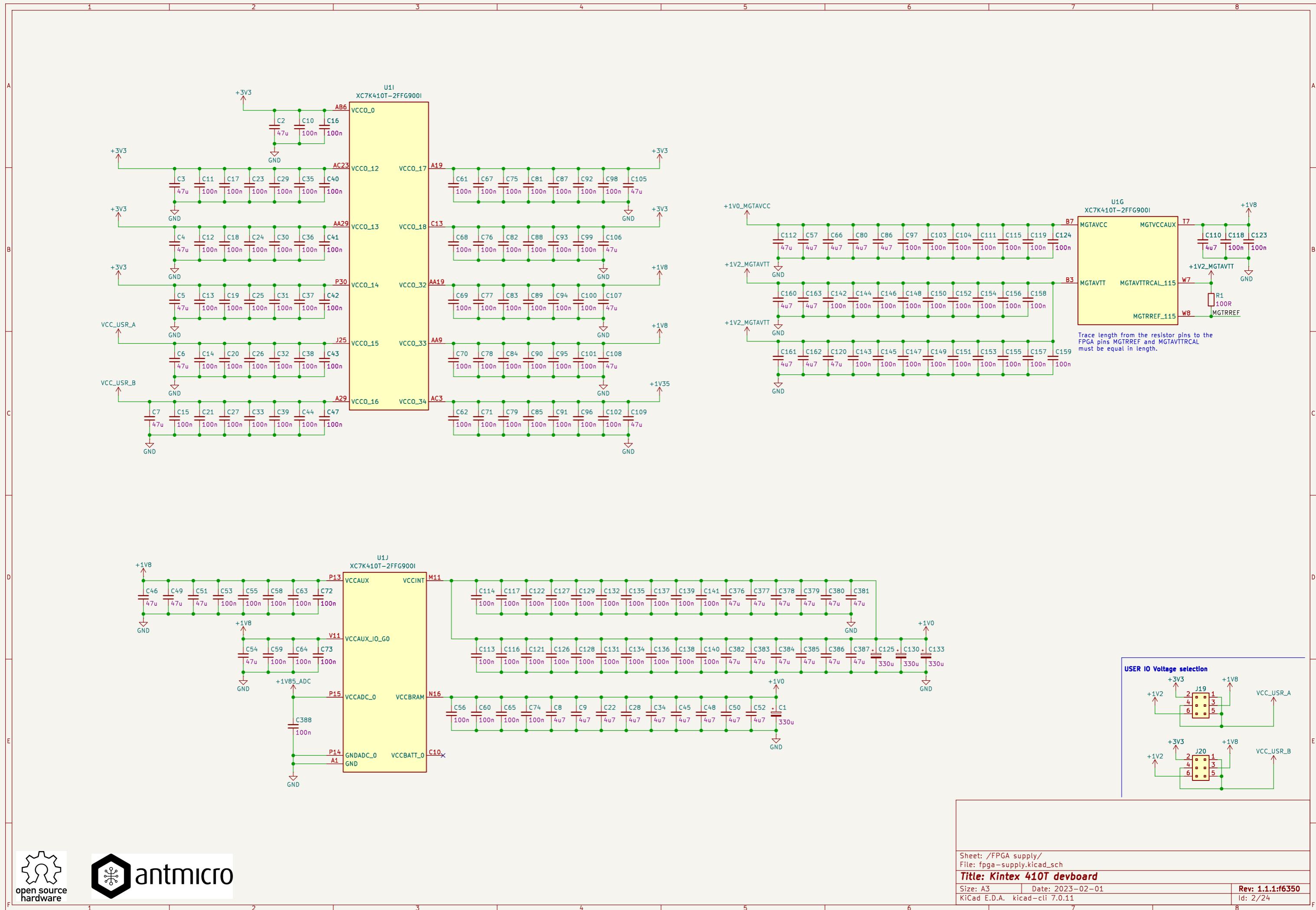
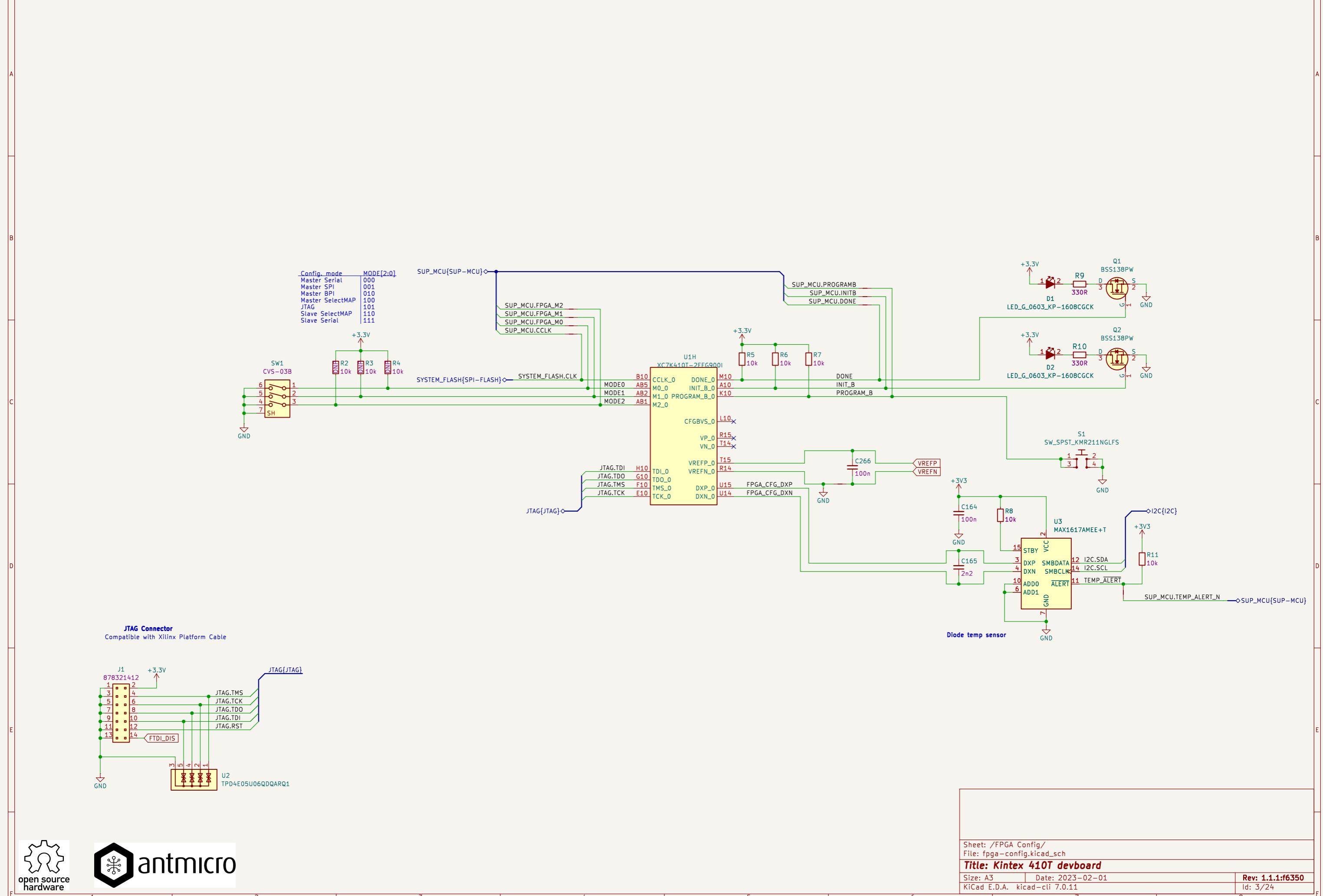


1 2 3 4 5 6 7 8





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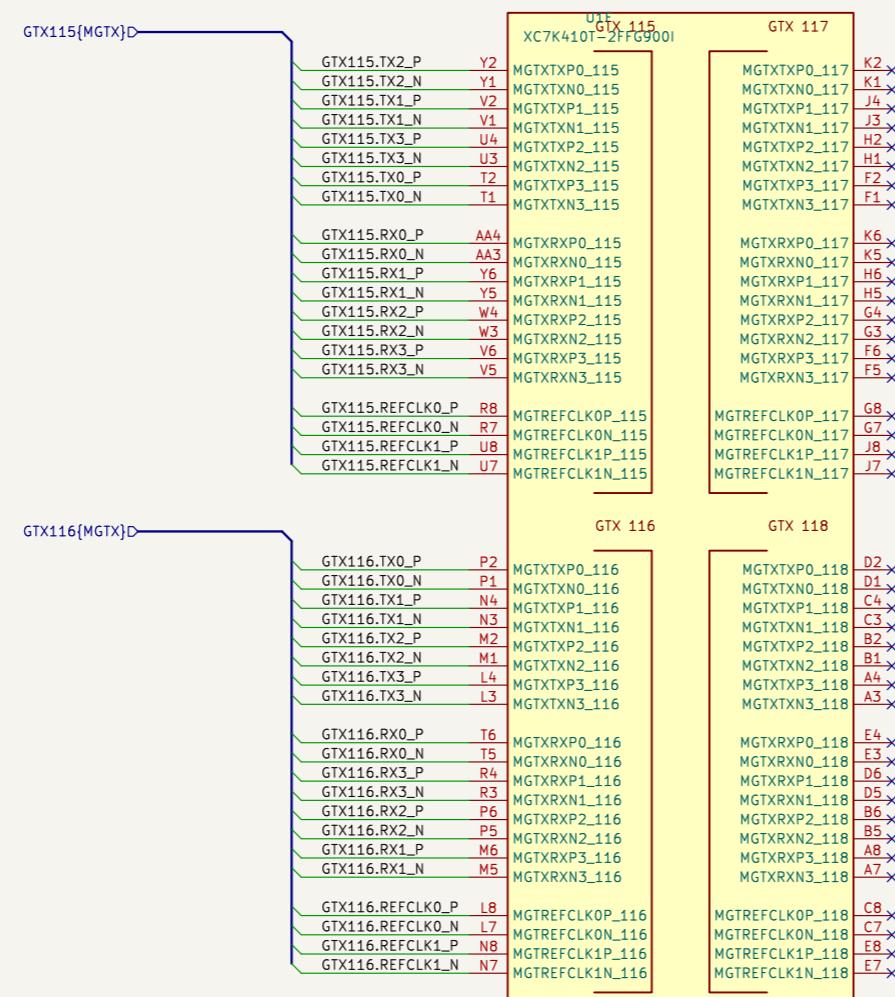
B

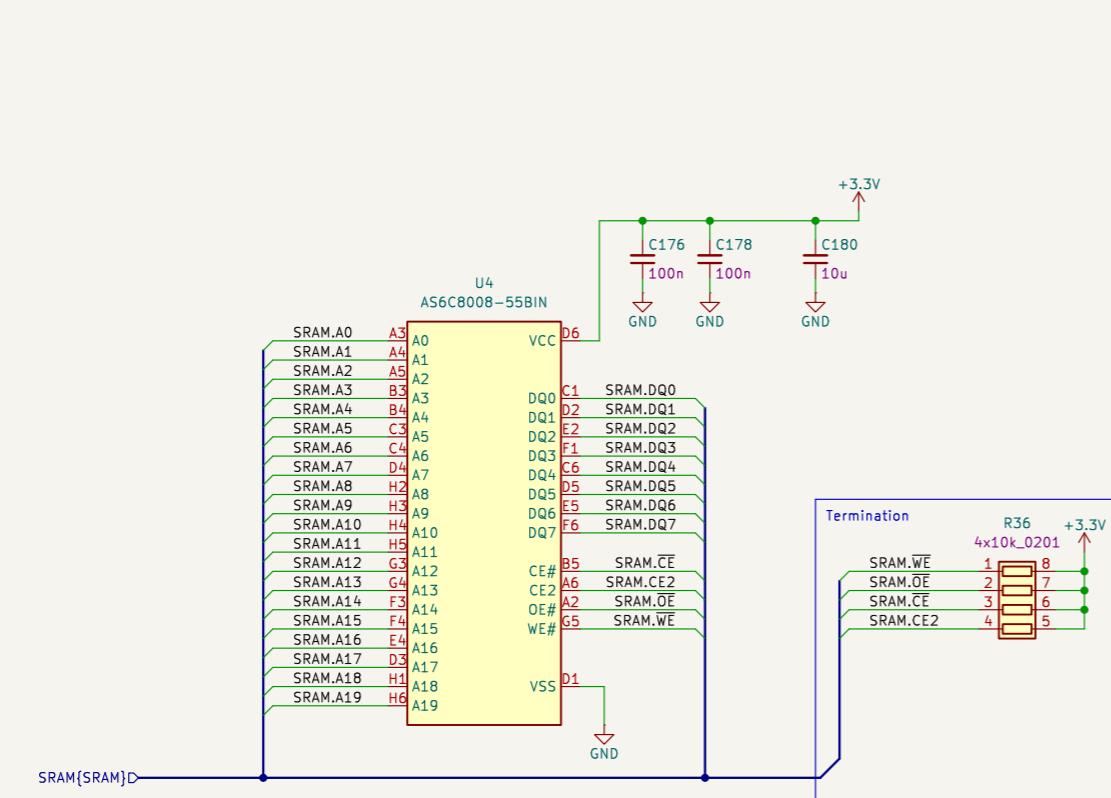
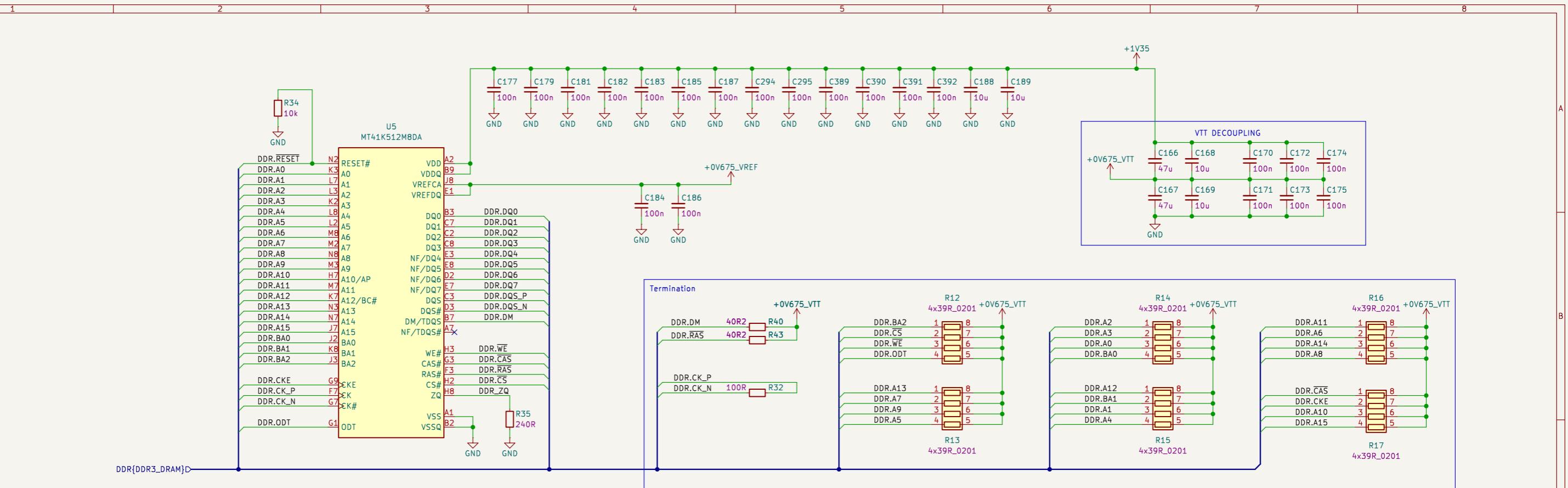
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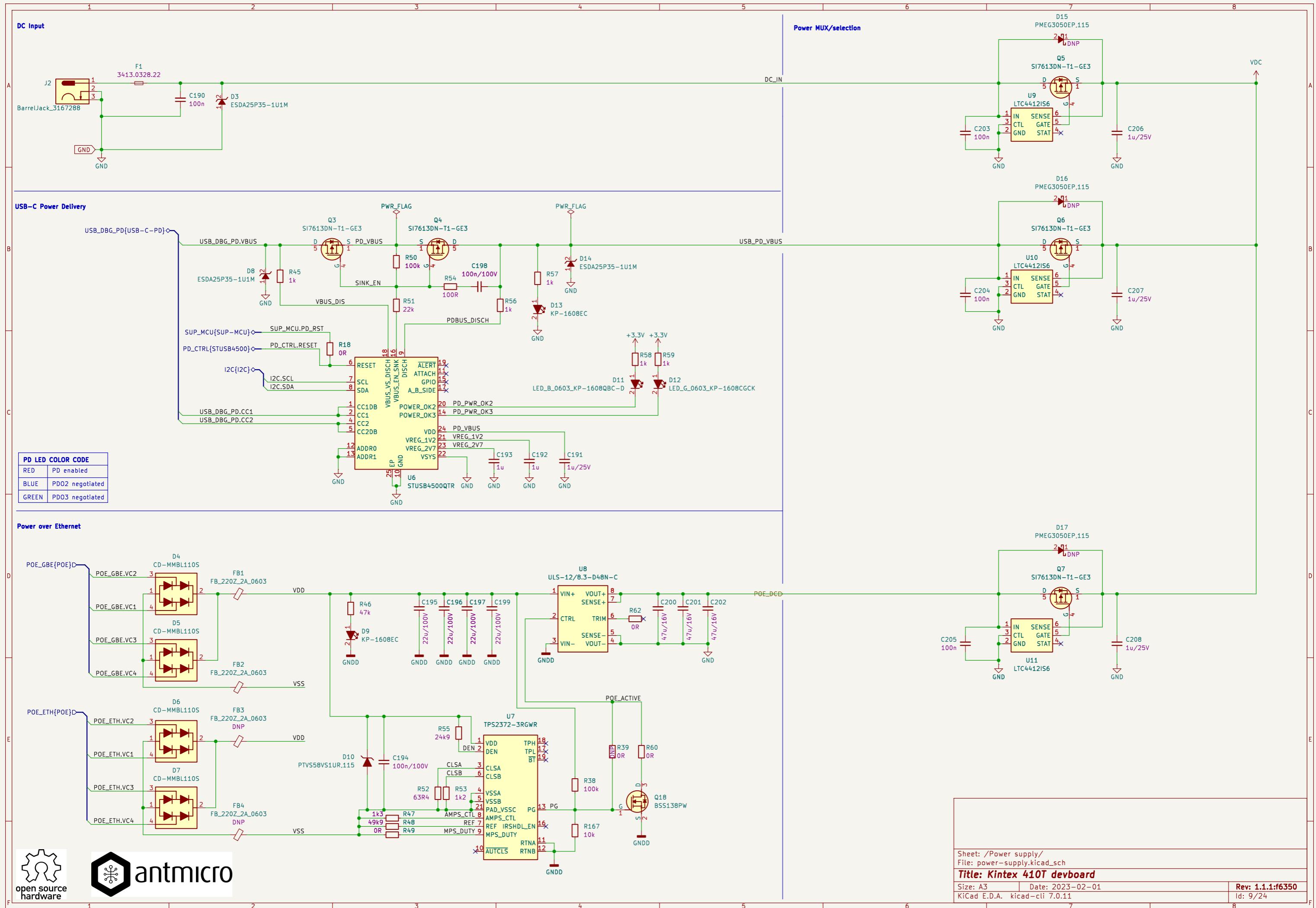


Sheet: /DRAM + SRAM/
File: ram.kicad_sch

Title: Kintex 410T devboard

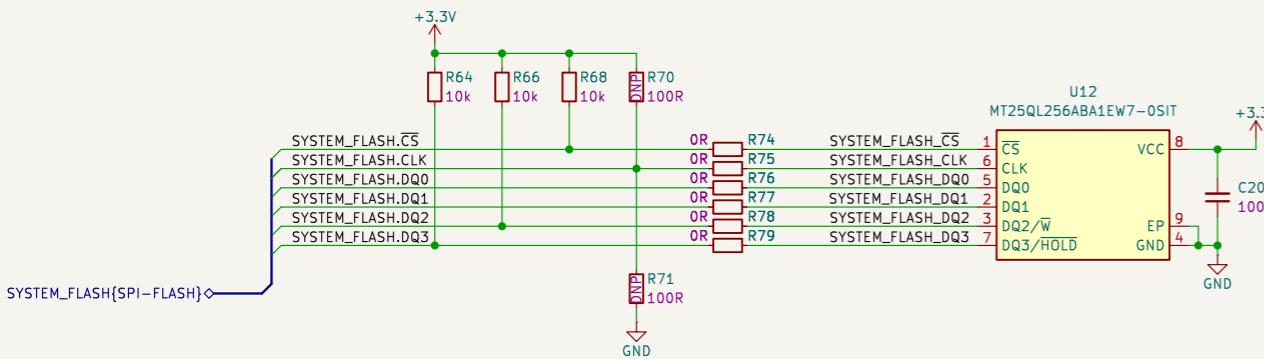
Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.1:f6350 Id: 8/24

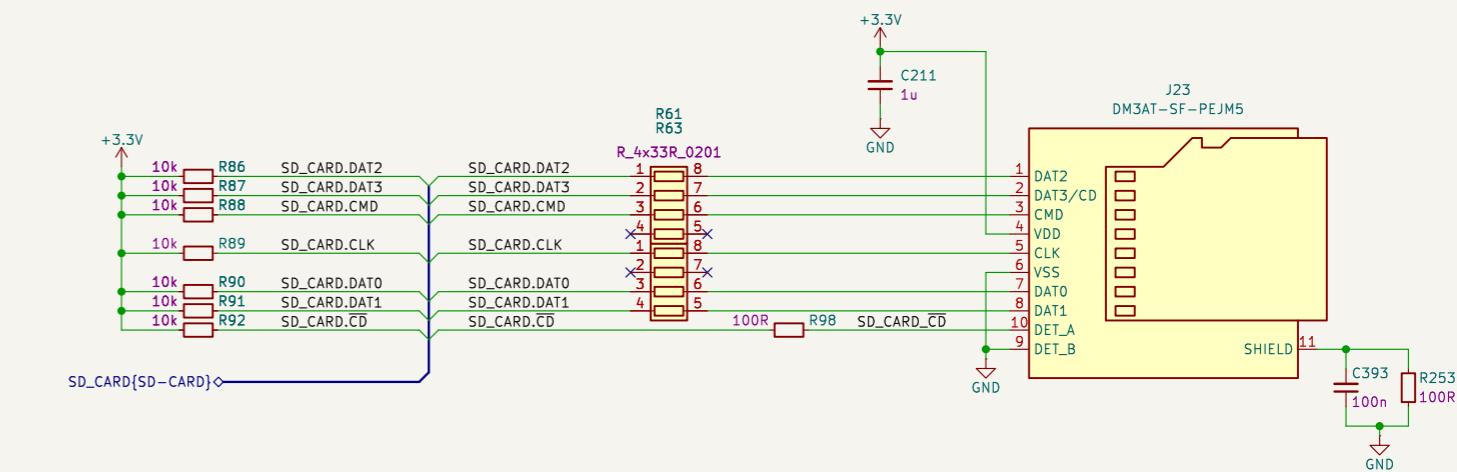


1 2 3 4 5 6 7 8

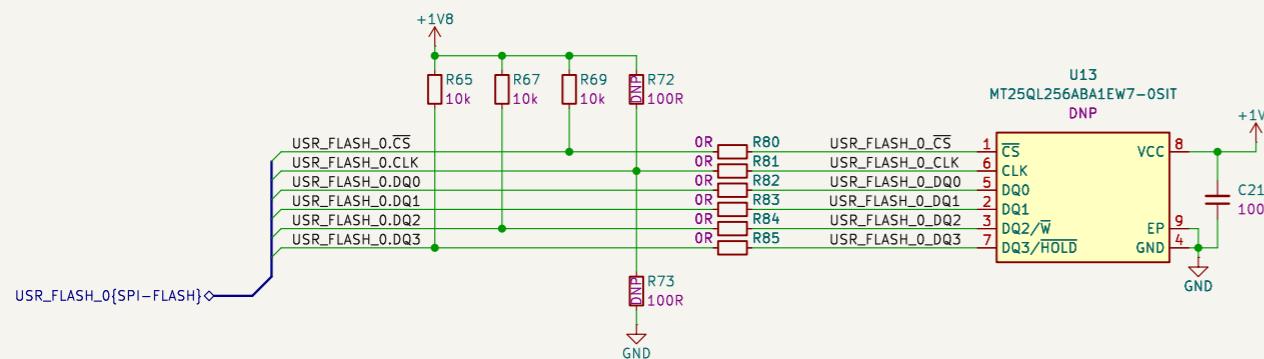
(Q)SPI flash

Master SPI Quad (x4) configuration schemeFollows Figure 2-14 7 Series FPGAs Configuration User Guide
UG470 (v1.15)

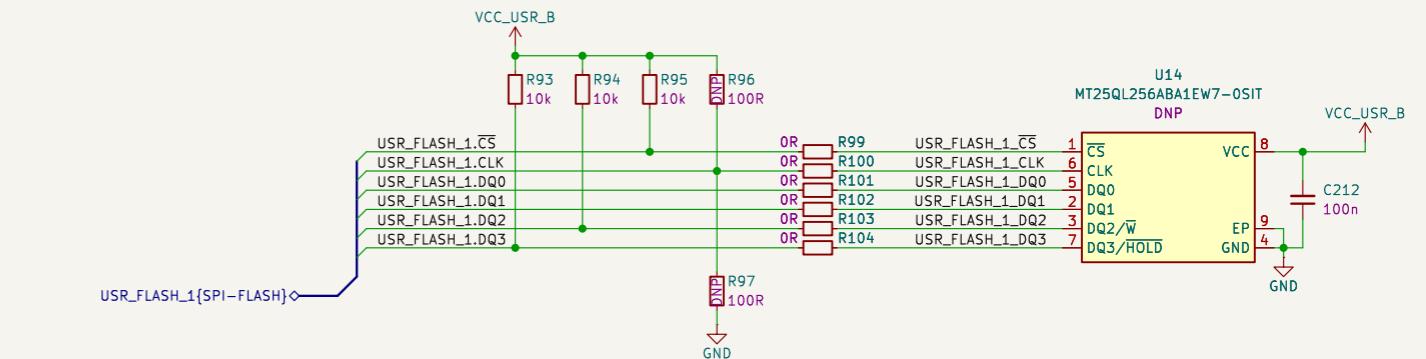
USER SD CARD



USER SPI flash 0



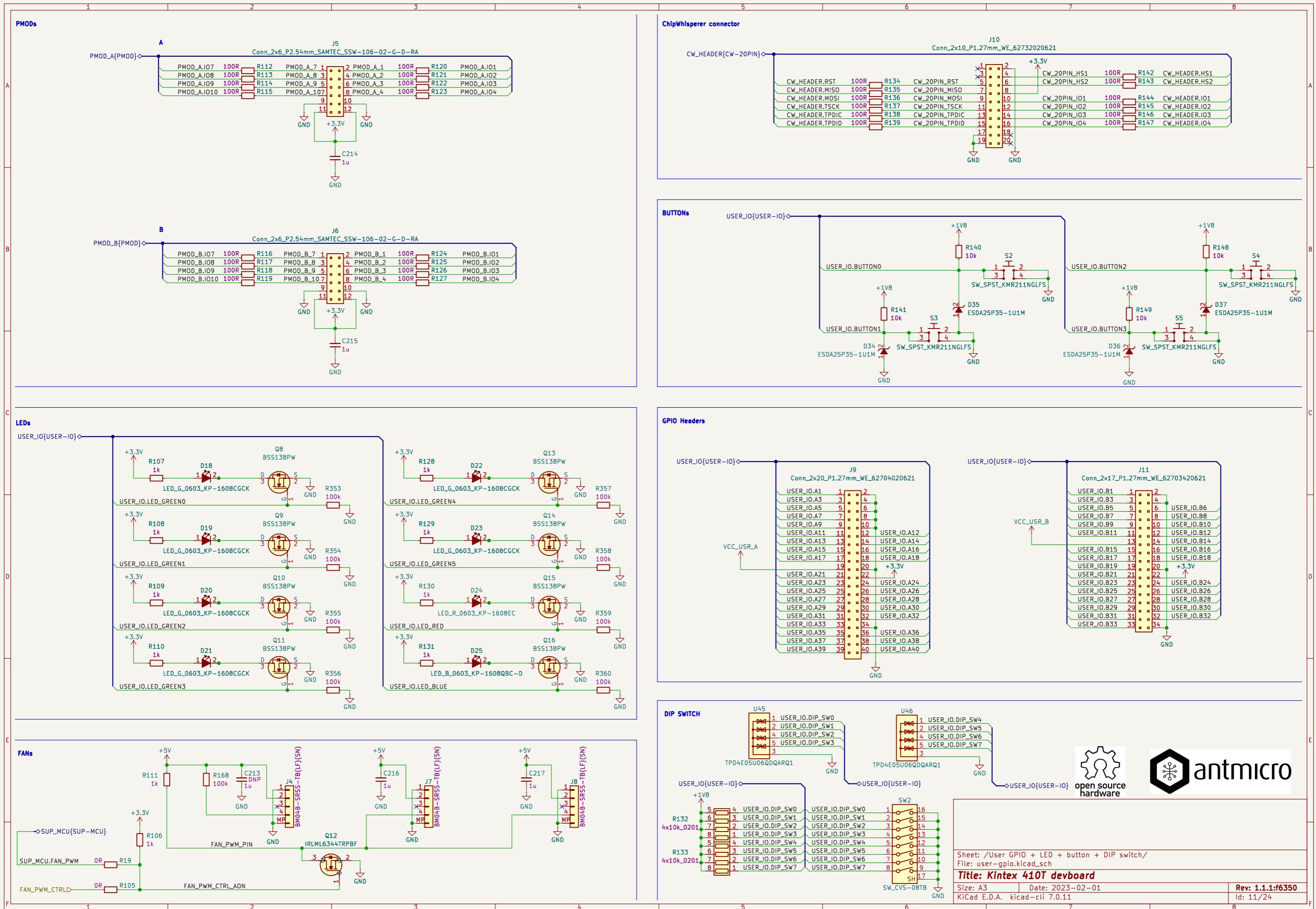
USER SPI flash 1

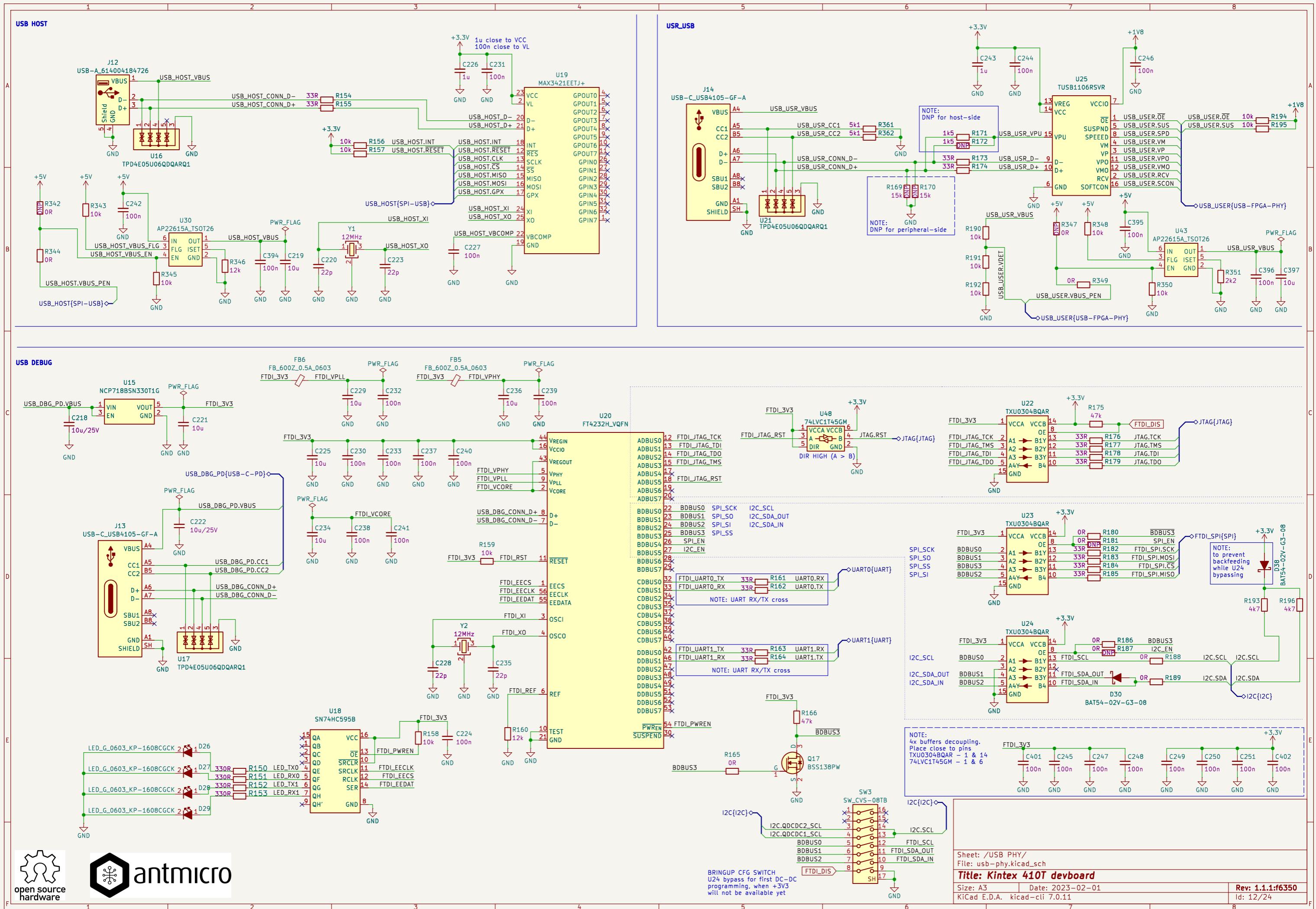
Sheet: /SPI Flash + SD Card/
File: spi-flash.kicad_sch

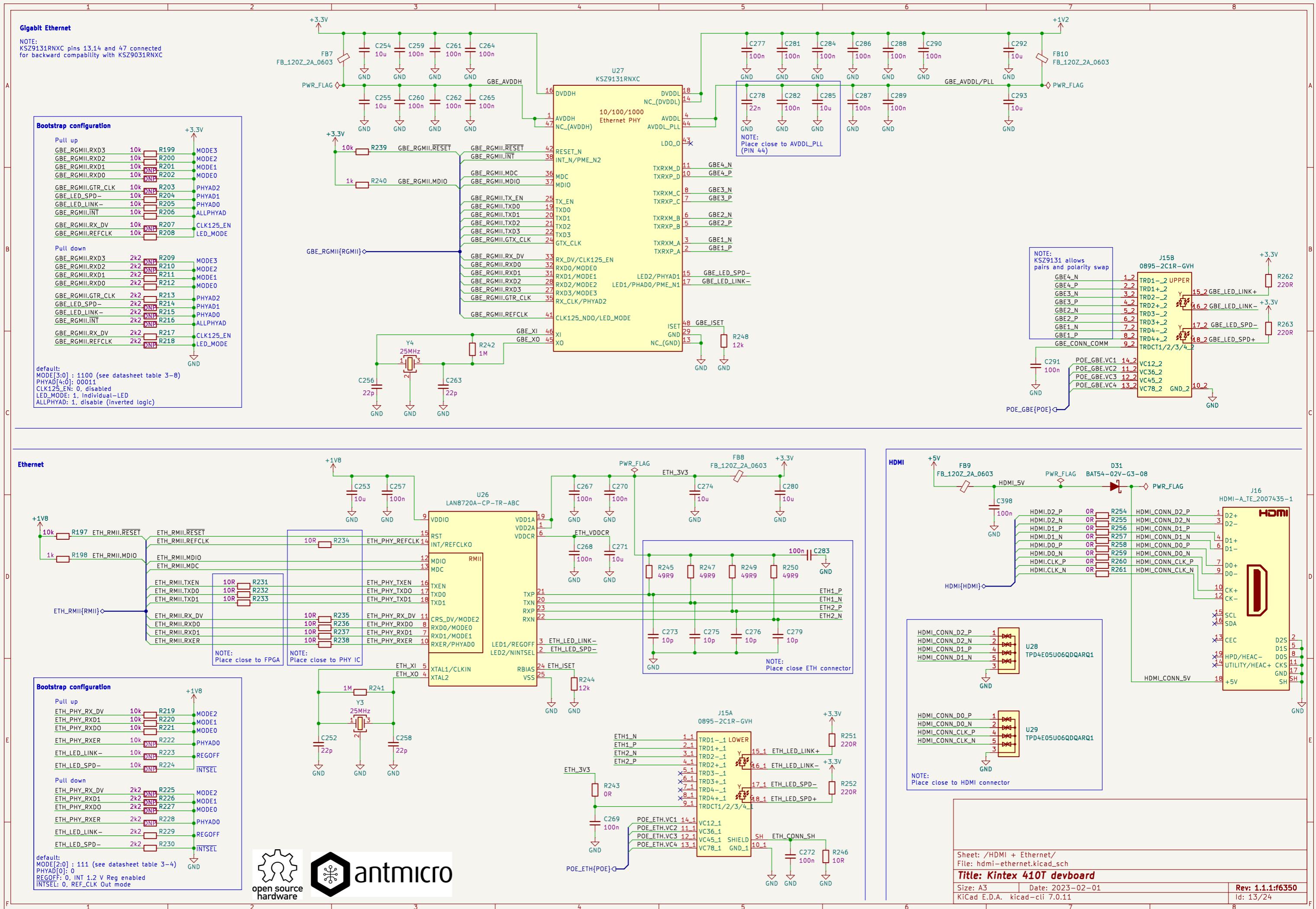
Title: Kintex 410T devboard

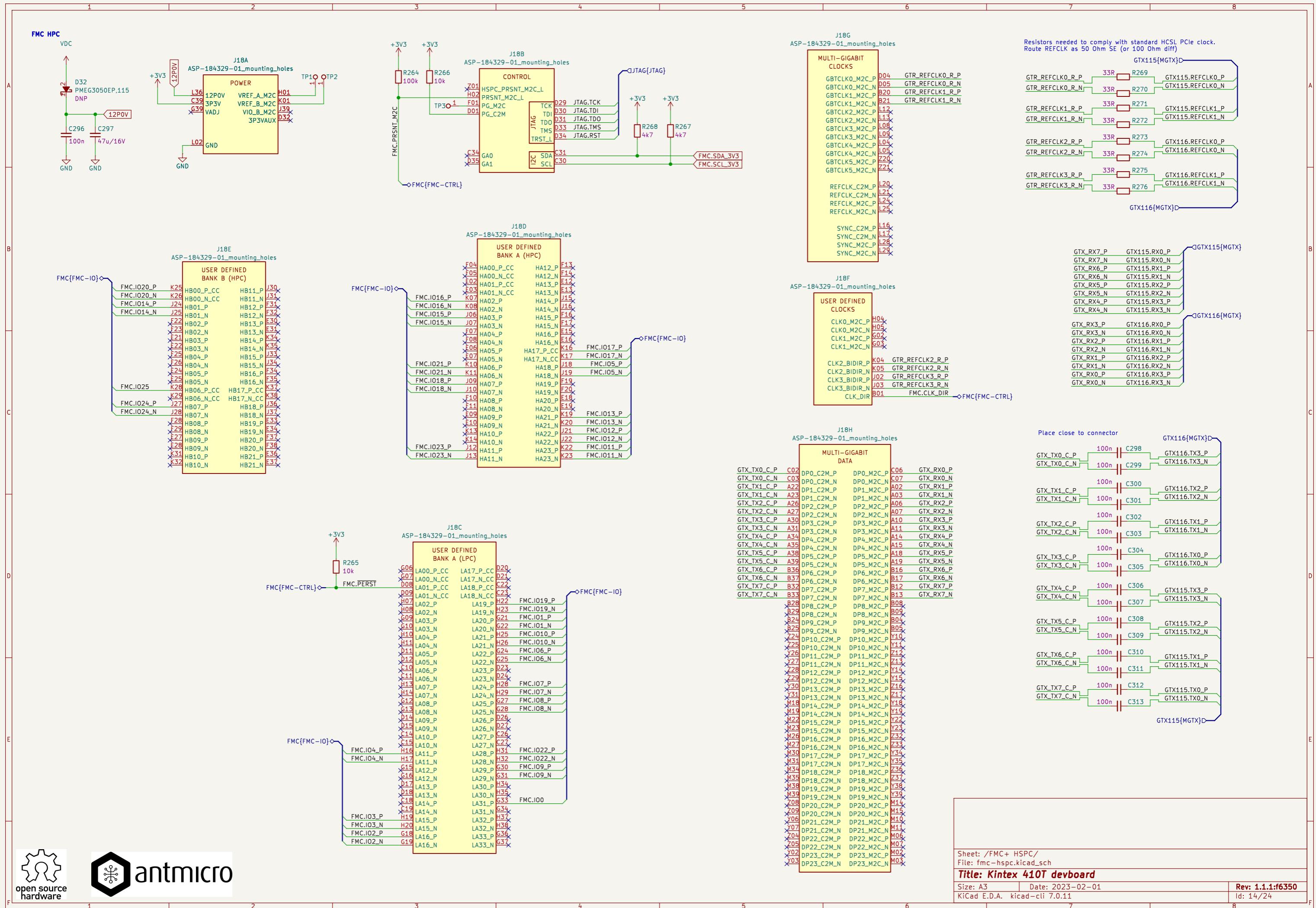
Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cliv 7.0.11

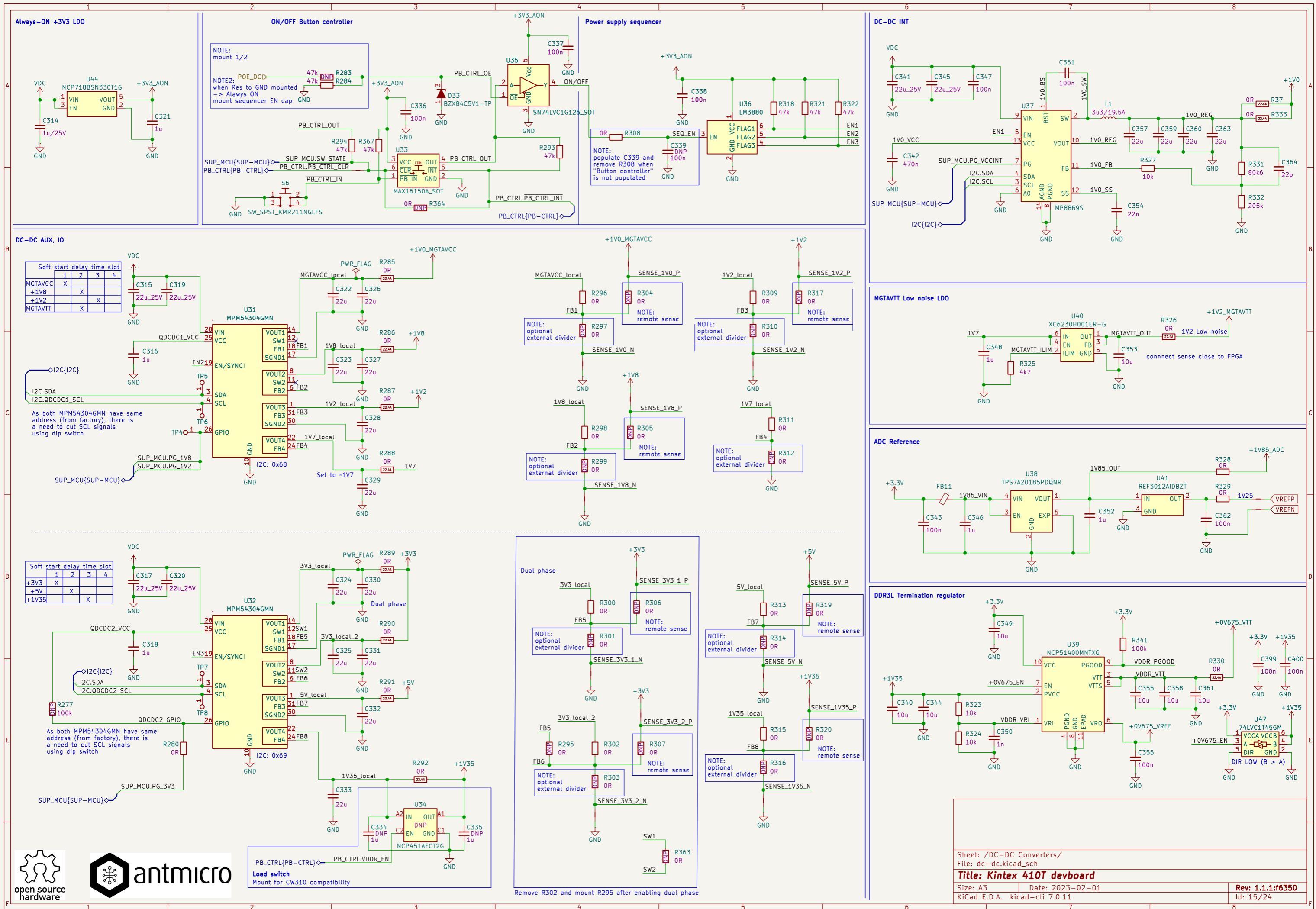
Rev: 1.1.1:f6350 Id: 10/24











antmicro
open source
hardware

Remove R302 and mount R295 after enabling dual phase

Sheet: /DC-DC Converters/
File: dc-dc.kicad_sch

Title: Kintex 410T devboard

Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cl 7.0.11

Rev: 1.1.1:f6350
Id: 15/24

Main system clock

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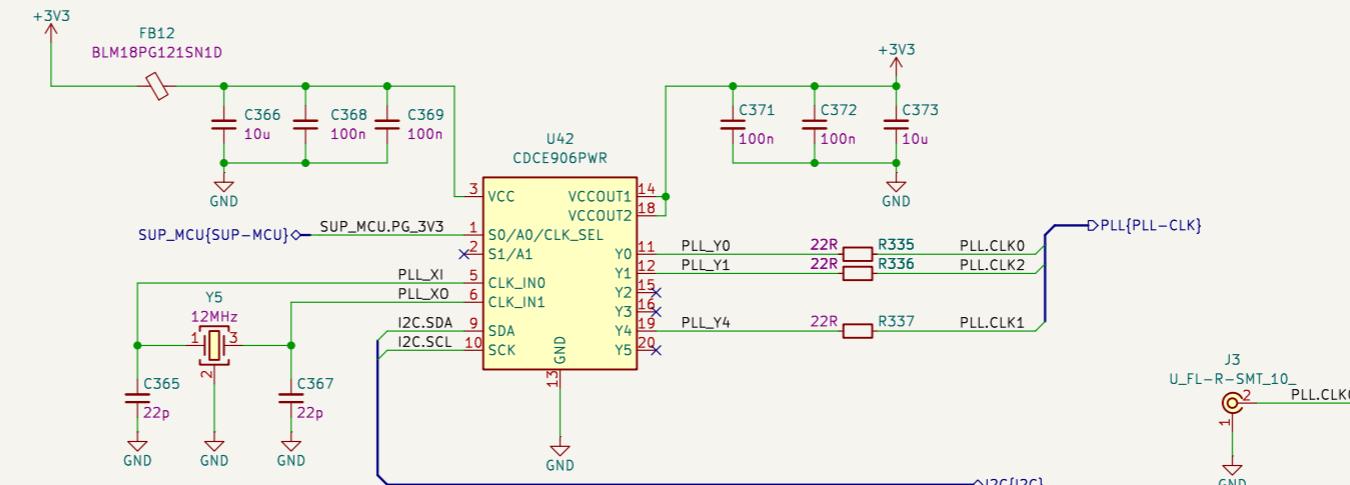
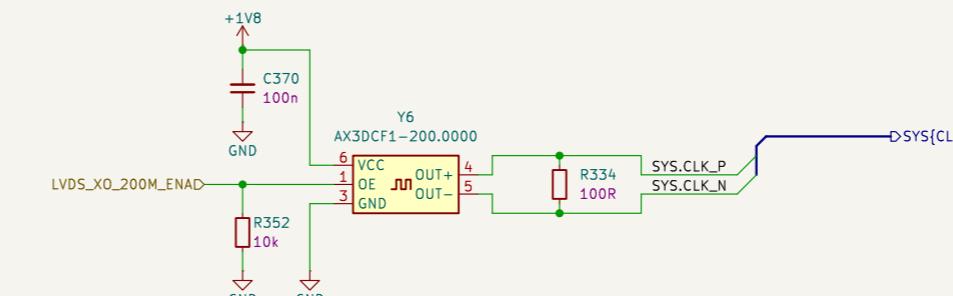
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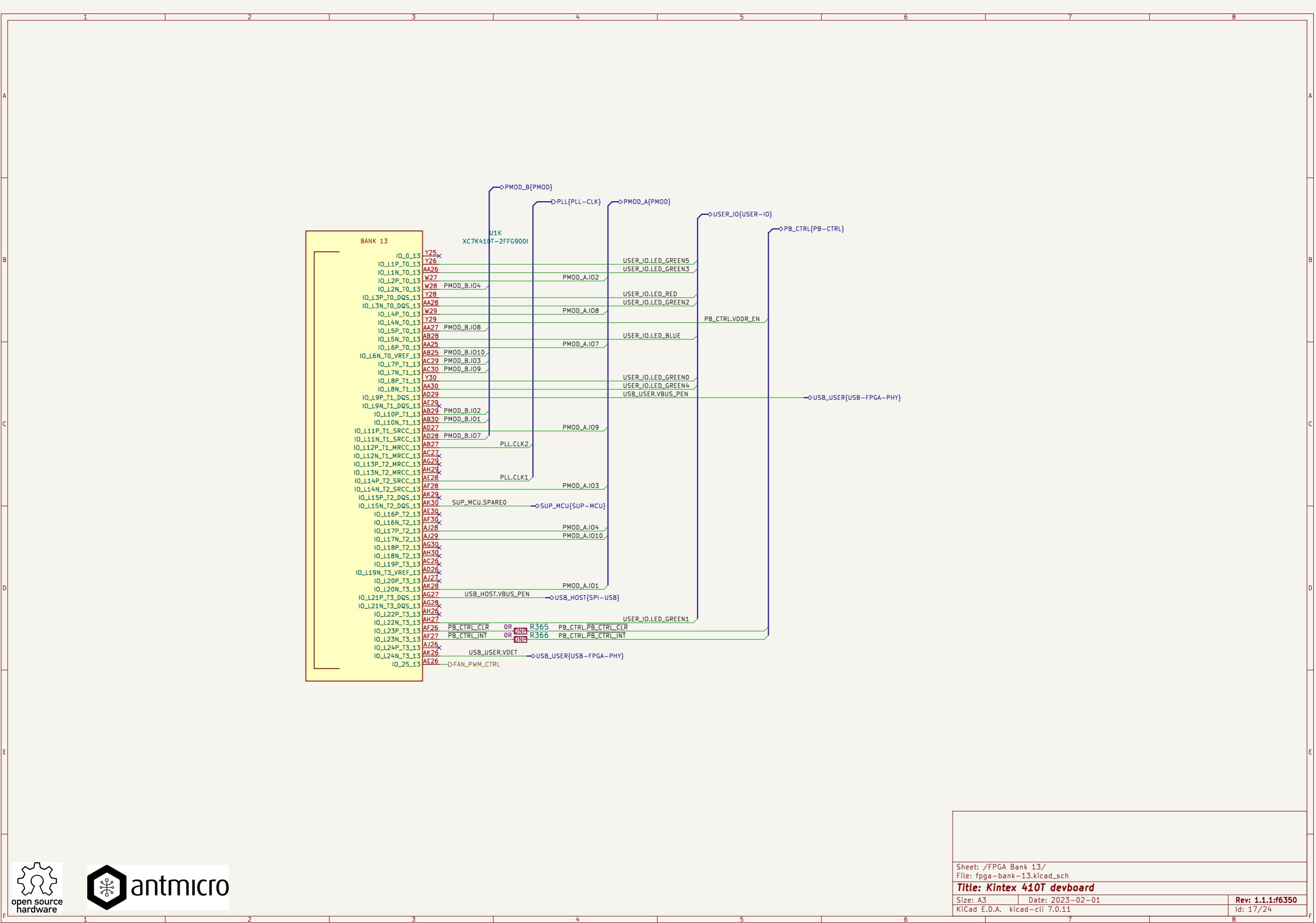


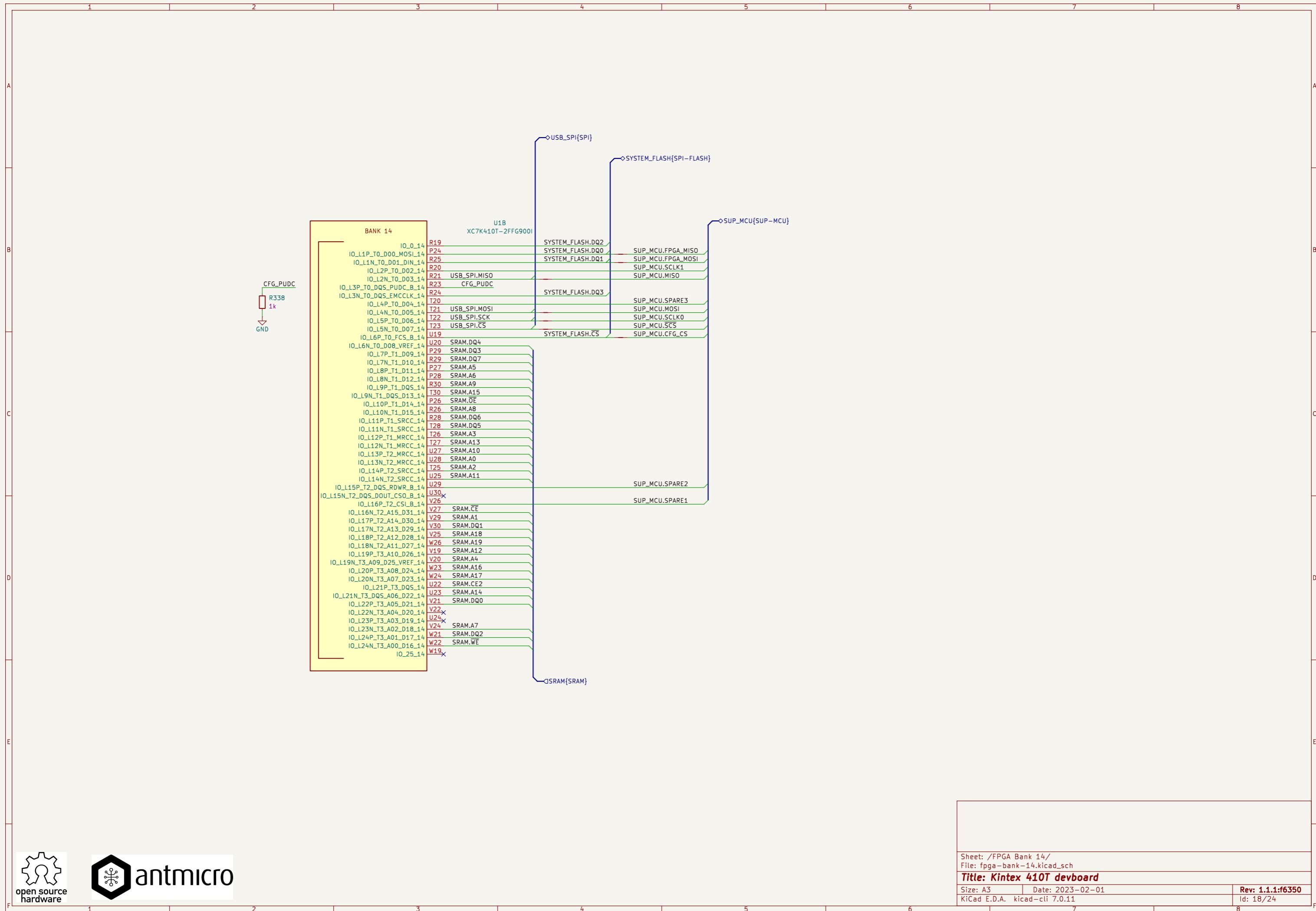
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File: clocks.kicad_sch

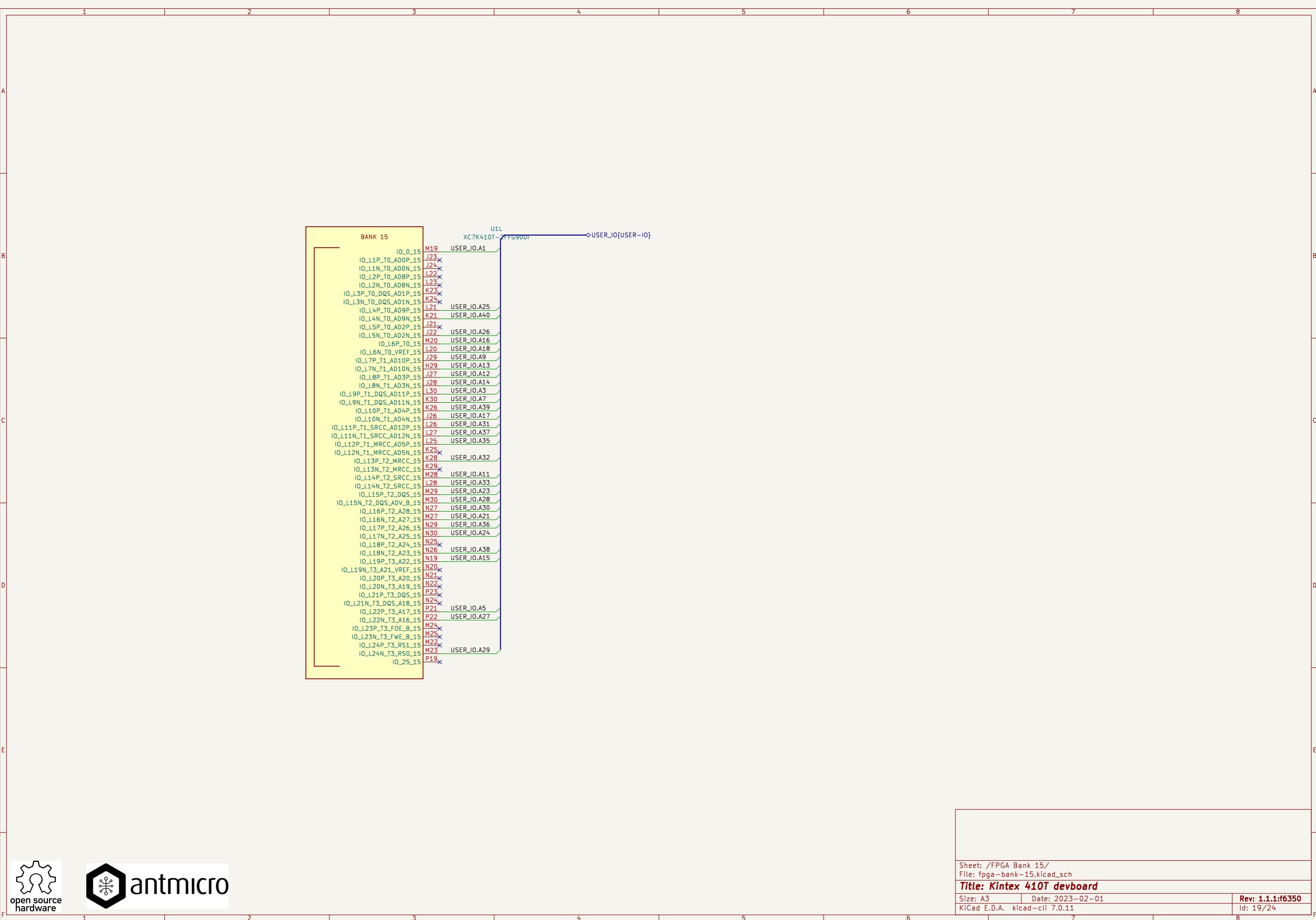
Title: Kintex 410T devboard

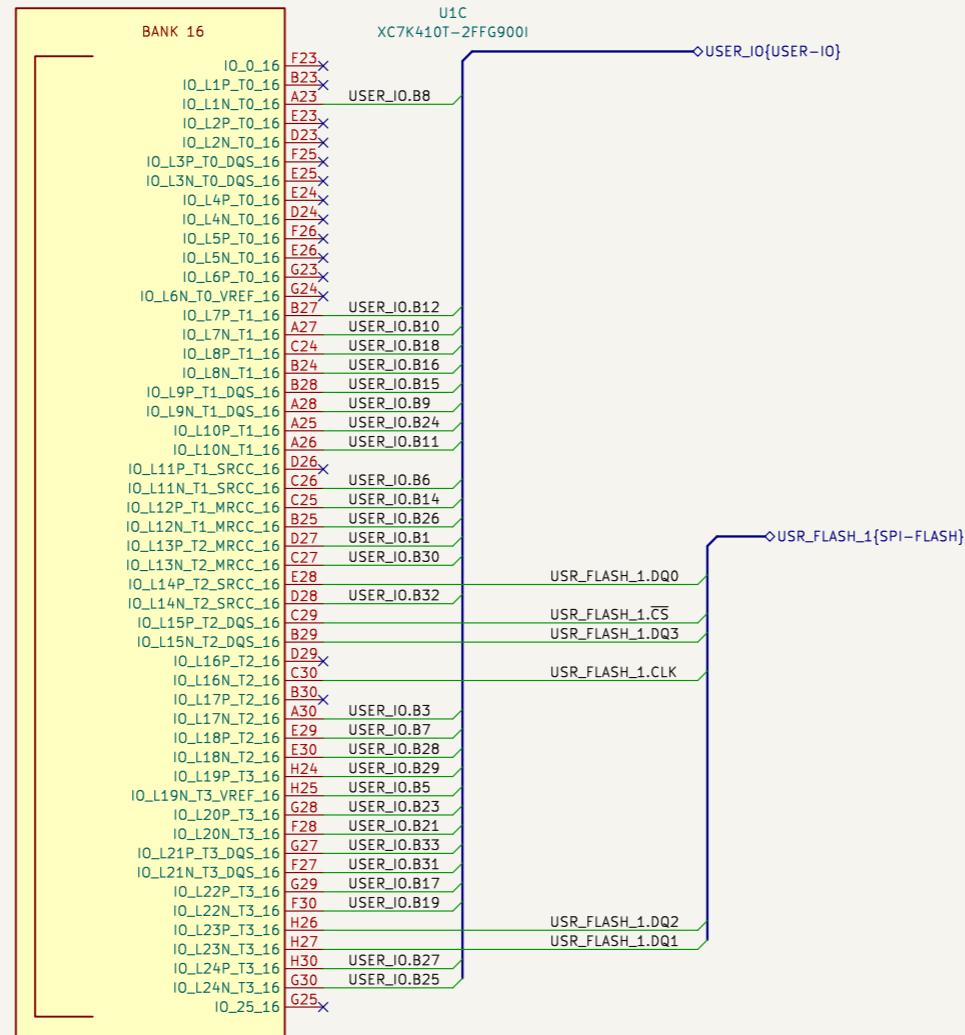
Size: A3 Date: 2023-02-01
KiCad E.D.A. kicad-cliv 7.0.11

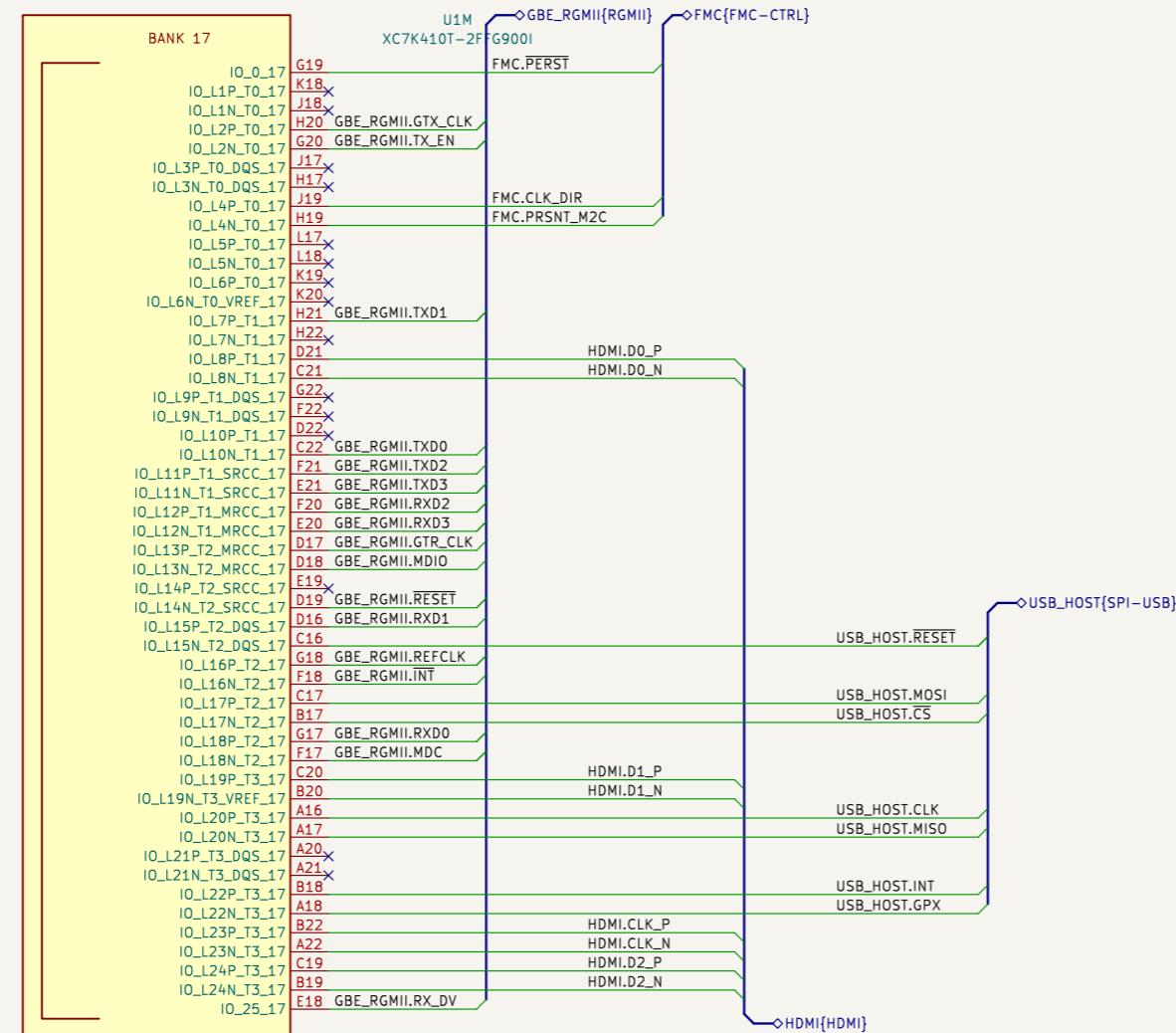
Rev: 1.1.1:f6350
Id: 16/24

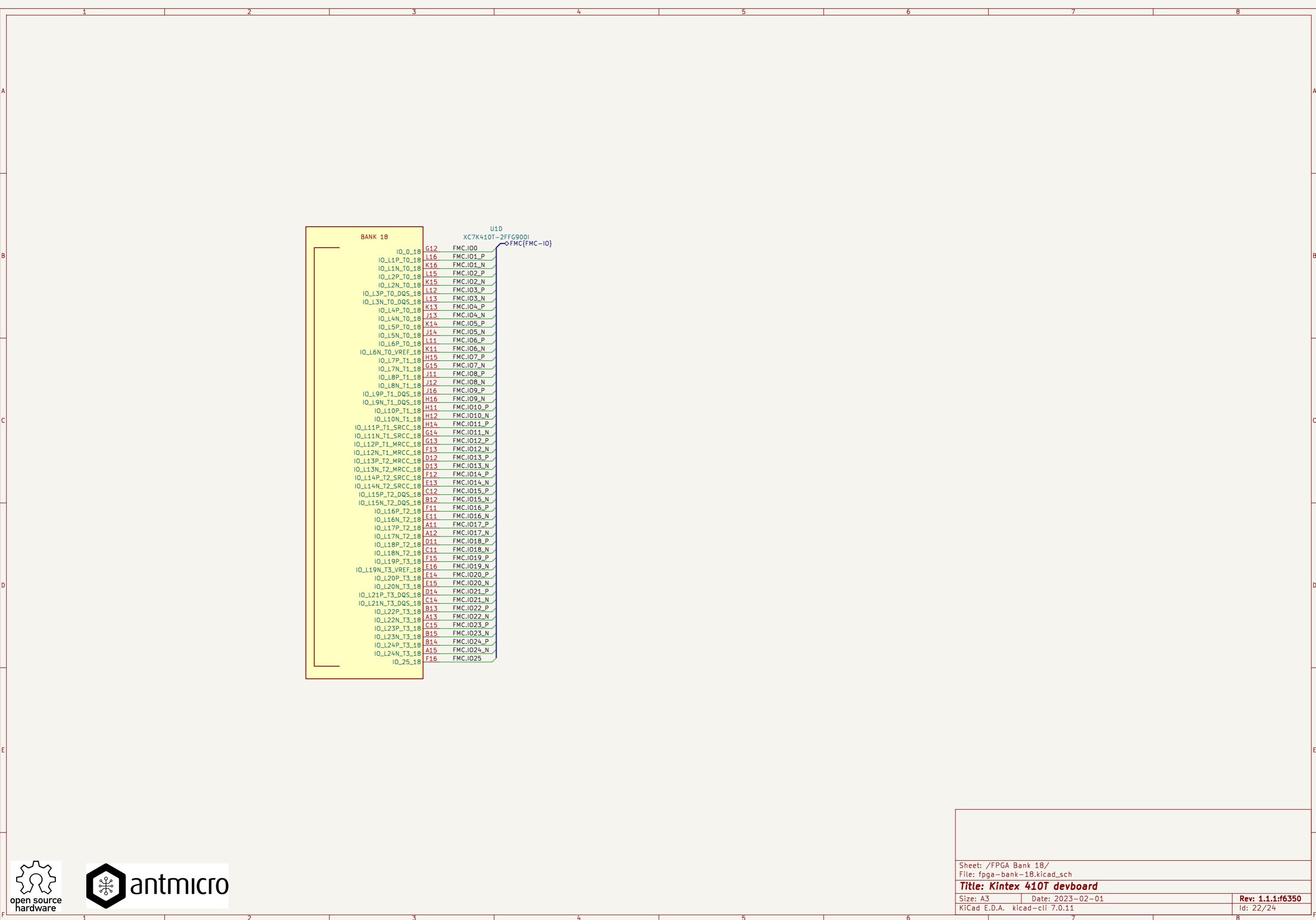












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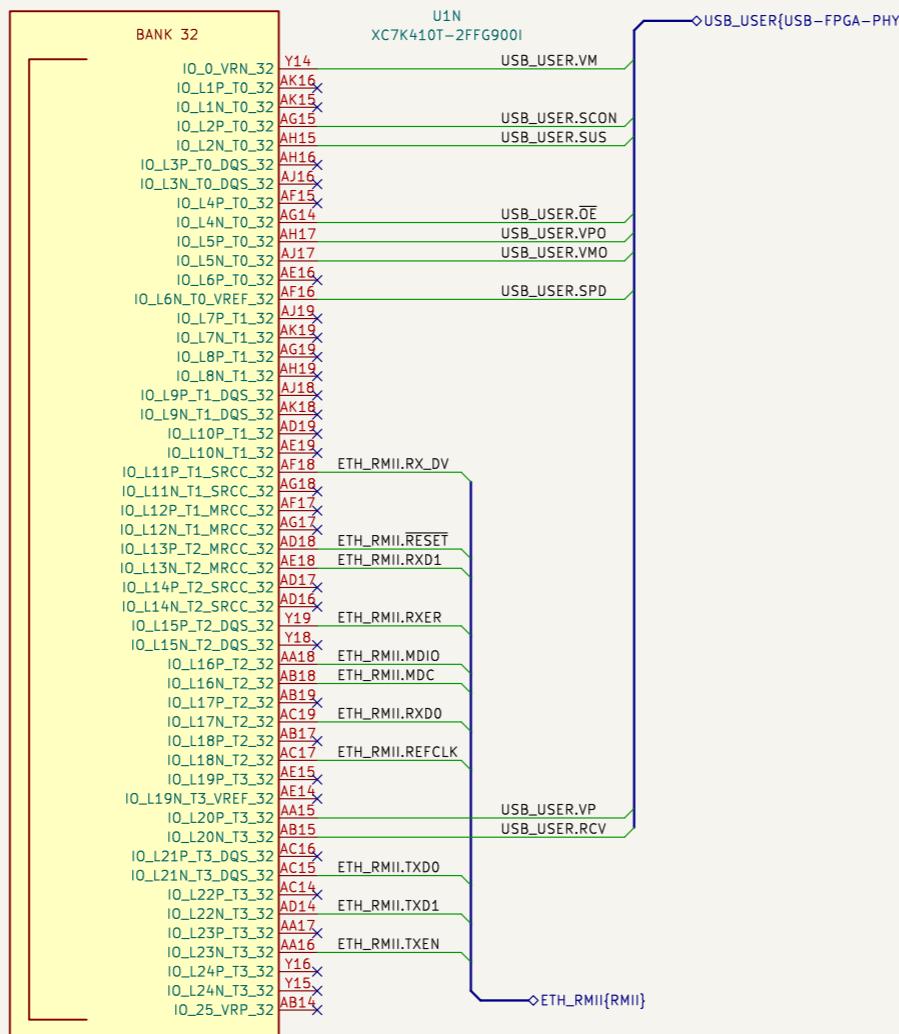
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Sheet: /FPGA Bank 32/
File: fpga-bank-32.kicad_sch

Title: Kintex 410T devboard

Size: A3 | Date: 2023-02-01
KiCad E.D.A. kicad-cli 7.0.11

Rev: 1.1.1:f6350 | Id: 23/24

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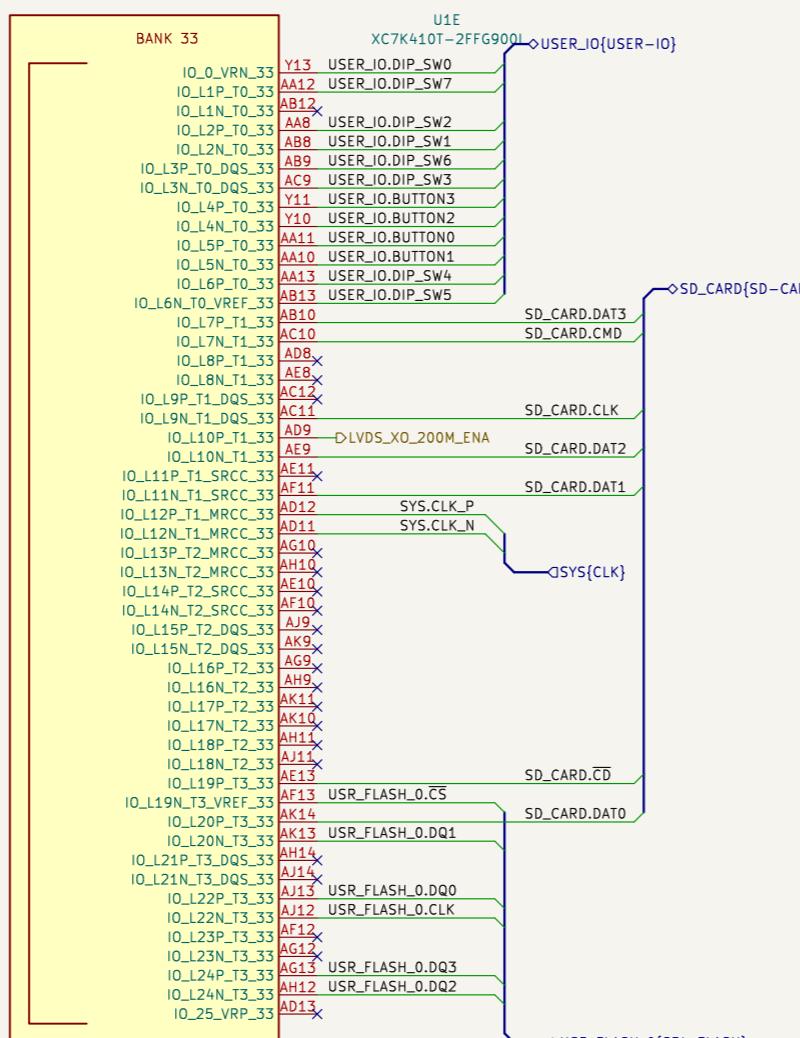
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Supervisor MCU

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