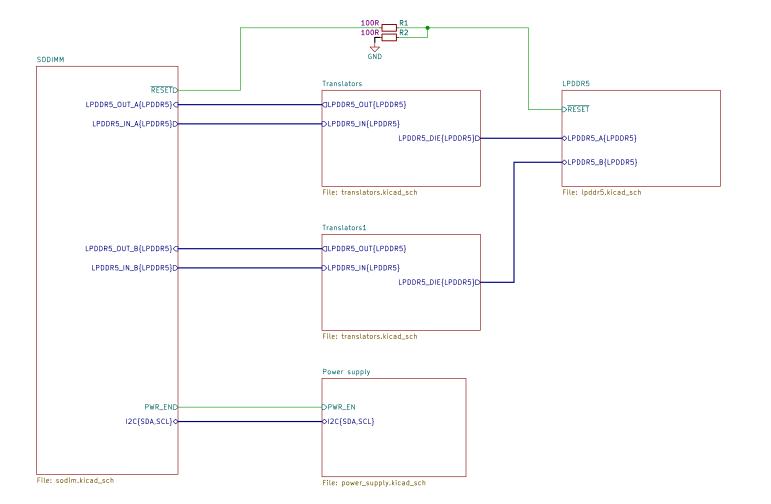
LPDDR5 Testbed





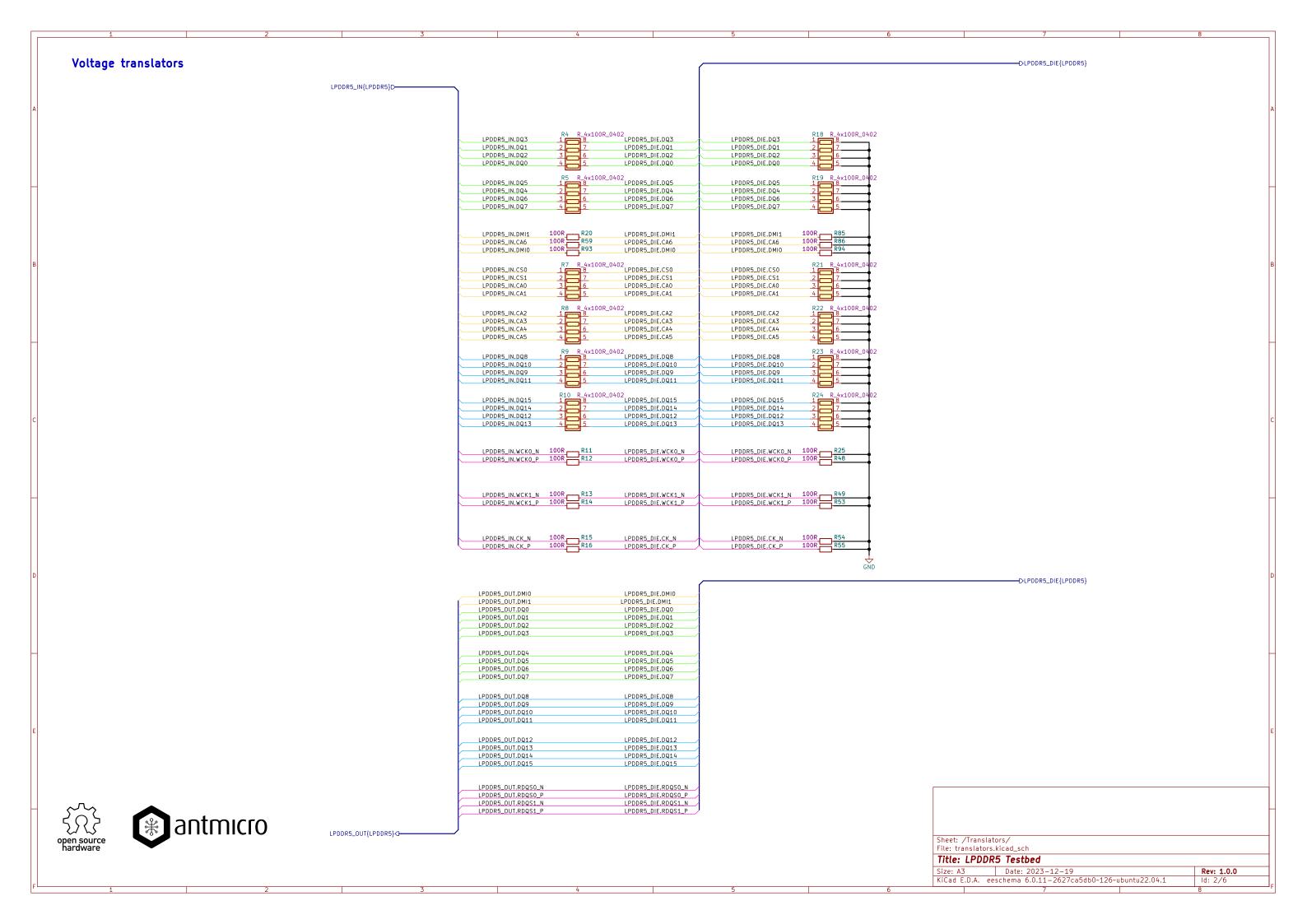


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File: lpddr5-testbed.kicad_sch

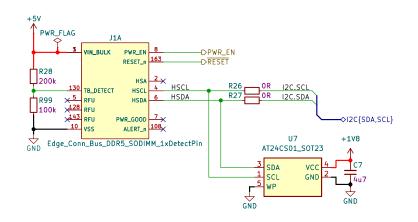
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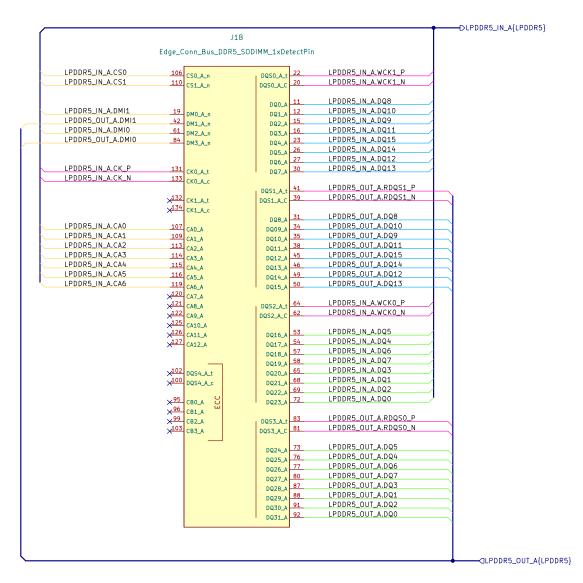
Size: A3 Date: 2023-12-19 Rev: 1.0.0

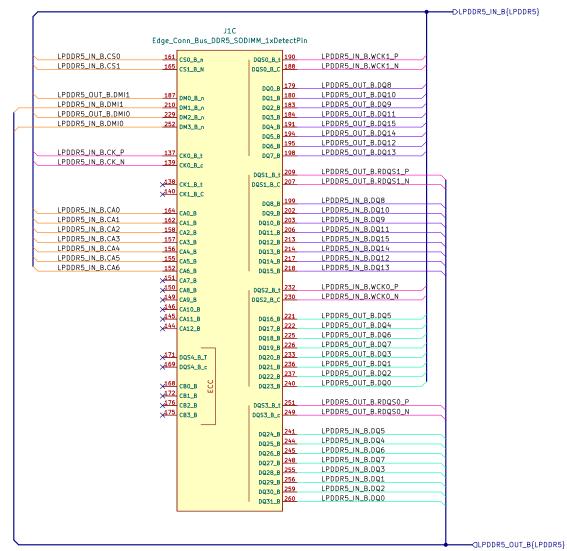
KiCad E.D.A. eeschema 6.0.11-2627ca5db0-126-ubuntu22.04.1 Id: 1/6



SODIMM Connector











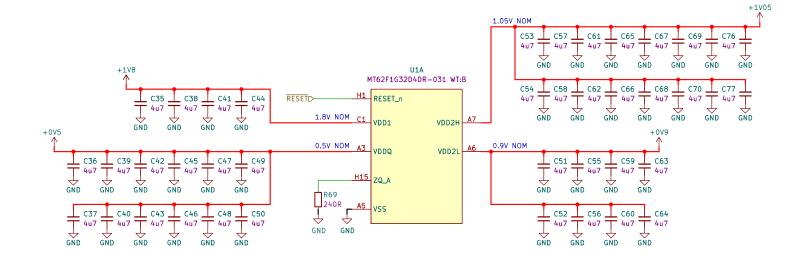
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Title: LPDDR5 Testbed

Size: A3 Date: 2023-12-19 Rev: 1.0.0

KiCad E.D.A. eeschema 6.0.11-2627ca5db0-126-ubuntu22.04.1 Id: 3/6





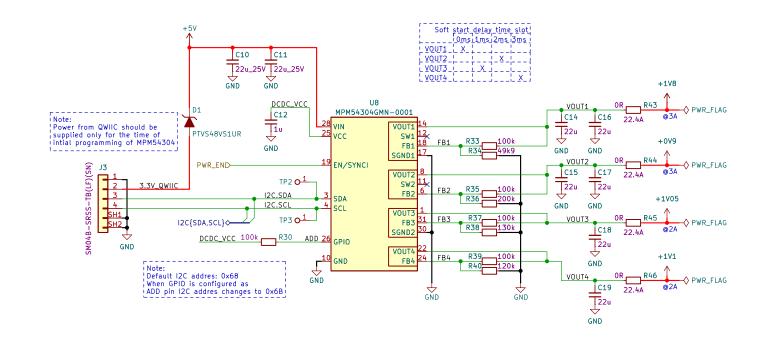
| LPDDR5_A{LPDDR5} | • | | | | | | | |
|------------------|------------------------|-----|------------------|----------------|-----|------------------|--|--|
| | U1B | | | | | | | |
| | MT62F1G32D4DR-031 WT:B | | | | | | | |
| | LPDDR5_A.CS0 | _H7 | CS0_A | RDQS0_t_A | В3_ | LPDDR5_A.RDQS0_P | | |
| | LPDDR5_A.CS1 | G6 | CS1_A | RDQS0_c_A | C4_ | LPDDR5_A.RDQS0_N | | |
| | LPDDR5_A.DMIO | Α4 | DMIO A | RDQS1 t A | B13 | LPDDR5_A.RDQS1_P | | |
| | LPDDR5_A.DMI1 | A12 | DMIO_A DMI1_A | RDQS1_c_A | C12 | | | |
| | LPDDR5_A.WCK0_P | E4 | | | D1 | LPDDR5_A.DQ0 | | |
| | LPDDR5_A.WCKO_P | D5 | WCK0_t_A | DQ0_A | C2 | LPDDR5_A.DQ1 | | |
| | | | WCK0_c_A | DQ1_A DQ2_A | E2 | LPDDR5_A.DQ2 | | |
| | LPDDR5_A.WCK1_P | E12 | WCK1_t_A | DQZ_A DQ3_A | D3 | LPDDR5_A.DQ3 | | |
| | LPDDR5_A.WCK1_N | D11 | WCK1_c_A | DQ4_A | B5_ | LPDDR5_A.DQ4 | | |
| | | | | DQ5_A | C6 | LPDDR5_A.DQ5 | | |
| | LPDDR5_A.CK_P | Н9 | CK t A | DQ6_A | E6 | LPDDR5_A.DQ6 | | |
| | LPDDR5_A.CK_N | | CK_c_A | DQ7_A | F5 | LPDDR5_A.DQ7 | | |
| | | | | DQ8_A | D15 | LPDDR5_A.DQ8 | | |
| | LPDDR5_A.CA0 | G4 | CAO_A | DQ9_A | C14 | LPDDR5_A.DQ9 | | |
| | LPDDR5_A.CA1 | H5 | CA1_A | DQ10_A | E14 | LPDDR5_A.DQ10 | | |
| | LPDDR5_A.CA2 | _G8 | CA2_A | DQ11_A | D13 | LPDDR5_A.DQ11 | | |
| | LPDDR5_A.CA3 | H11 | CA3_A | DQ12_A | B11 | LPDDR5_A.DQ12 | | |
| | LPDDR5_A.CA4 | G10 | CA4 A | DQ13_A | C10 | LPDDR5_A.DQ13 | | |
| | LPDDR5_A.CA5 | H13 | CA5_A | DQ14_A | E10 | LPDDR5_A.DQ14 | | |
| | LPDDR5_A.CA6 | G12 | CA6_A | DQ15_A | F11 | LPDDR5_A.DQ15 | | |
| | | | | | J | | | |

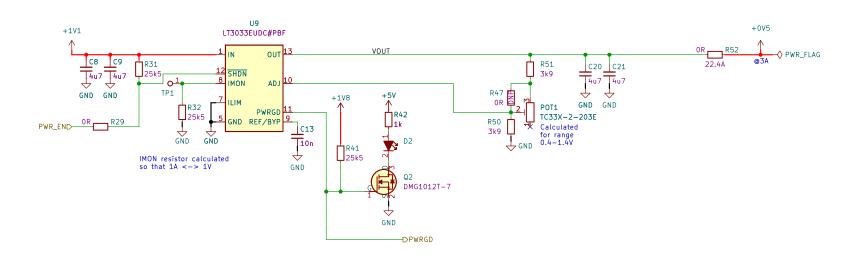
| LPDDR5_B{LPDDR5} | • | | | | | | |
|------------------|--|----------------|-------------------------|----------------------------|-------------------|---|--|
| | U1C MT62F1G32D4DR-031 WT:B | | | | | | |
| | | P9 R10 | CS0_B CS1_B | RDQSO_t_B RDQSO_c_B | W12 | LPDDR5_B.RDQS0_P LPDDR5_B.RDQS0_N | |
| | TENDRE B.DWIT | AA4 | Dilli_D | RDQS1_t_B RDQS1_c_B | Y3 W4 | LPDDR5_B.RDQS1_P LPDDR5_B.RDQS1_N | |
| | LPDDR5_B.WCK0_P LPDDR5_B.WCK0_N | U12 V11 | WCK0_t_B WCK0_c_B | DQO_B DQ1_B DQ2_B | V15 W14 U14 | LPDDR5_B.DQ0 LPDDR5_B.DQ1 LPDDR5_B.DQ2 | |
| | LPDDR5_B.WCK1_P LPDDR5_B.WCK1_N | U4 V5 | WCK1_t_B WCK1_c_B | DQ3_B DQ4_B | V13 Y11 W10 | | |
| | LPDDR5_B.CK_P LPDDR5_B.CK_N | P7 N7 | CK_t_B CK_c_B | DQ5_B DQ6_B DQ7_B | U10 T11 V1 | LPDDR5_B.DQ6 LPDDR5_B.DQ7 LPDDR5_B.DQ8 | |
| | LPDDR5_B.CA1 | R12 P11 | CA0_B CA1_B | DQ8_B DQ9_B DQ10_B | W2 U2 | LPDDR5_B.DQ9 LPDDR5_B.DQ10 | |
| | LPDDR5_B.CA2 LPDDR5_B.CA3 LPDDR5_B.CA4 | R8 P5 R6 | CA2_B CA3_B CA4_B | DQ11_B DQ12_B DQ13_B | V3 Y5 W6 | LPDDR5_B.DQ11 LPDDR5_B.DQ12 LPDDR5_B.DQ13 | |
| | LPDDR5_B.CA5 LPDDR5_B.CA6 | P3 R4 | CA5_B CA6_B | DQ14_B DQ15_B | U6 T5 | LPDDR5_B.DQ14 LPDDR5_B.DQ15 | |





| Charte /I DDDDE | / | | | | |
|---|--|------------|--|--|--|
| Sheet: /LPDDR5/ File: lpddr5.kicad_sch | | | | | |
| Title: LPDDR5 Testbed | | | | | |
| Size: A3 | Date: 2023-12-19 | Rev: 1.0.0 | | | |
| KiCad E.D.A. ee | schema 6.0.11-2627ca5db0~126~ubuntu22.04.1 | ld: 4/6 | | | |
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Sheet: /Power supply/
File: power_supply.kicad_sch

Title: LPDDR5 Testbed

Size: A3 Date: 2023-12-19 Rev: 1.0.0

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