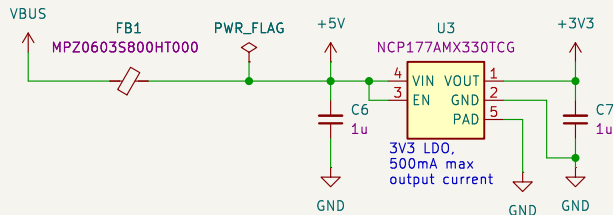
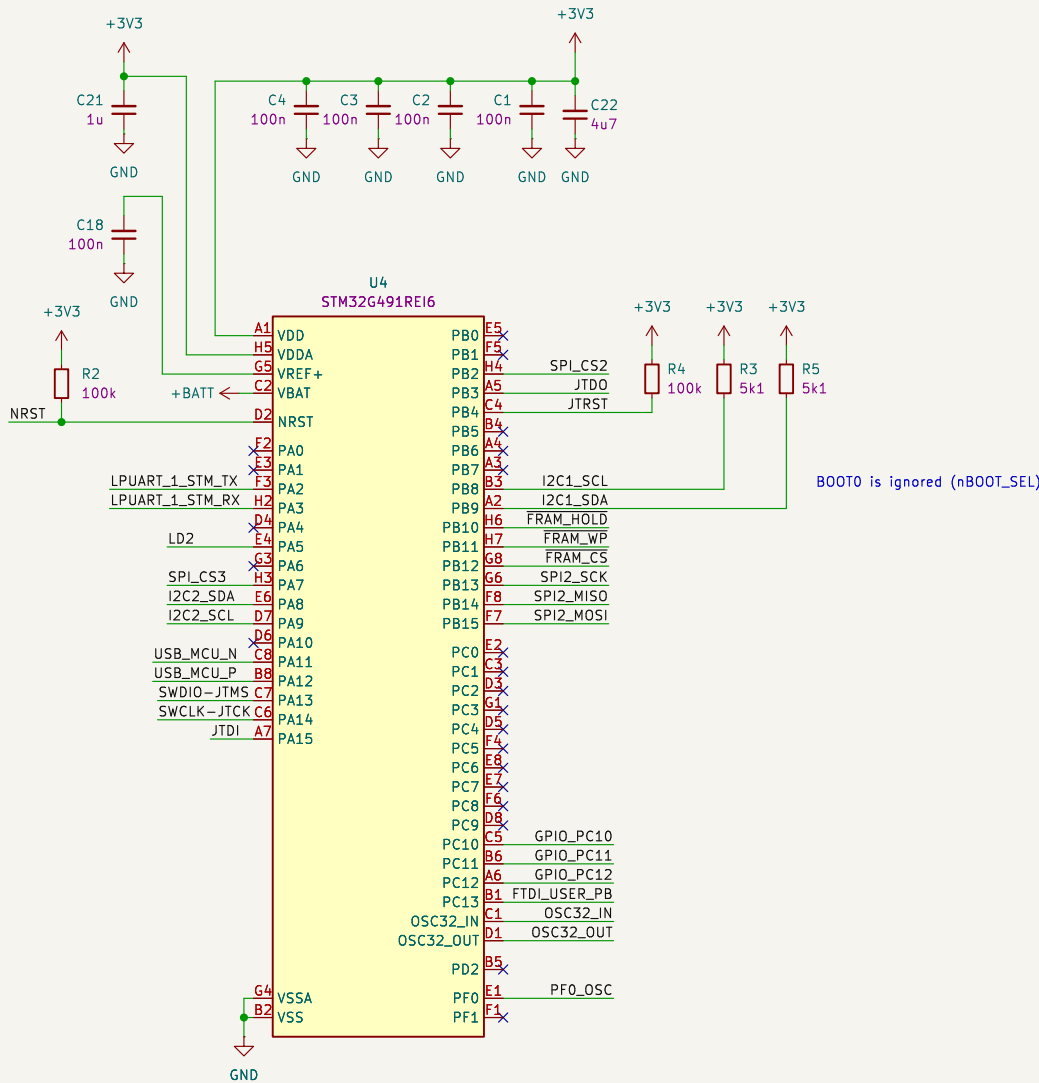


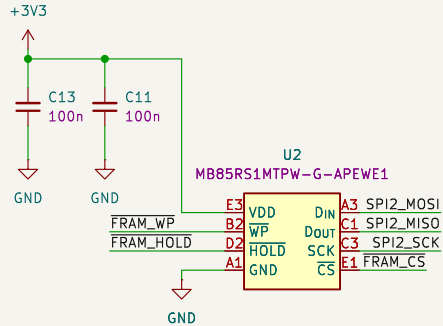
3V3 Regulator



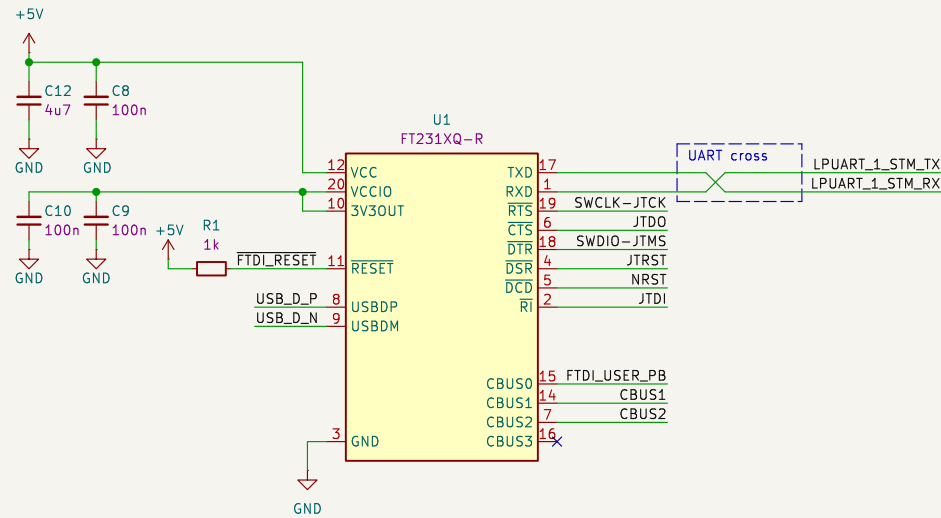
STM32 Microcontroller



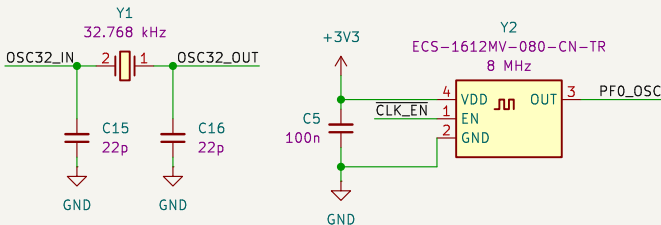
FeRAM SPI Memory



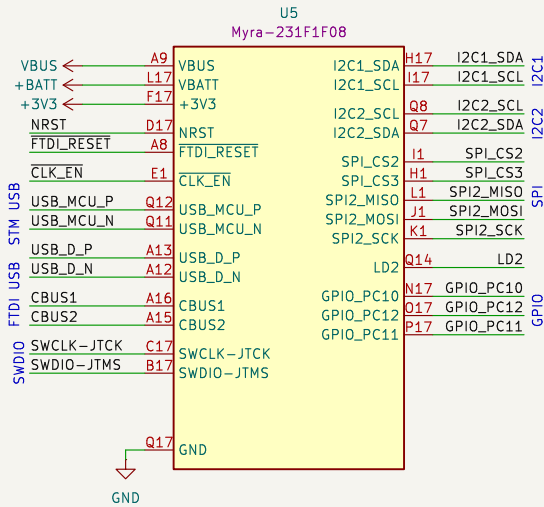
FT231XQ USB UART



Clocks



MYRA SiP footprint



Pull-down CLK_EN to disable 8MHz generator
Leave floating to enable